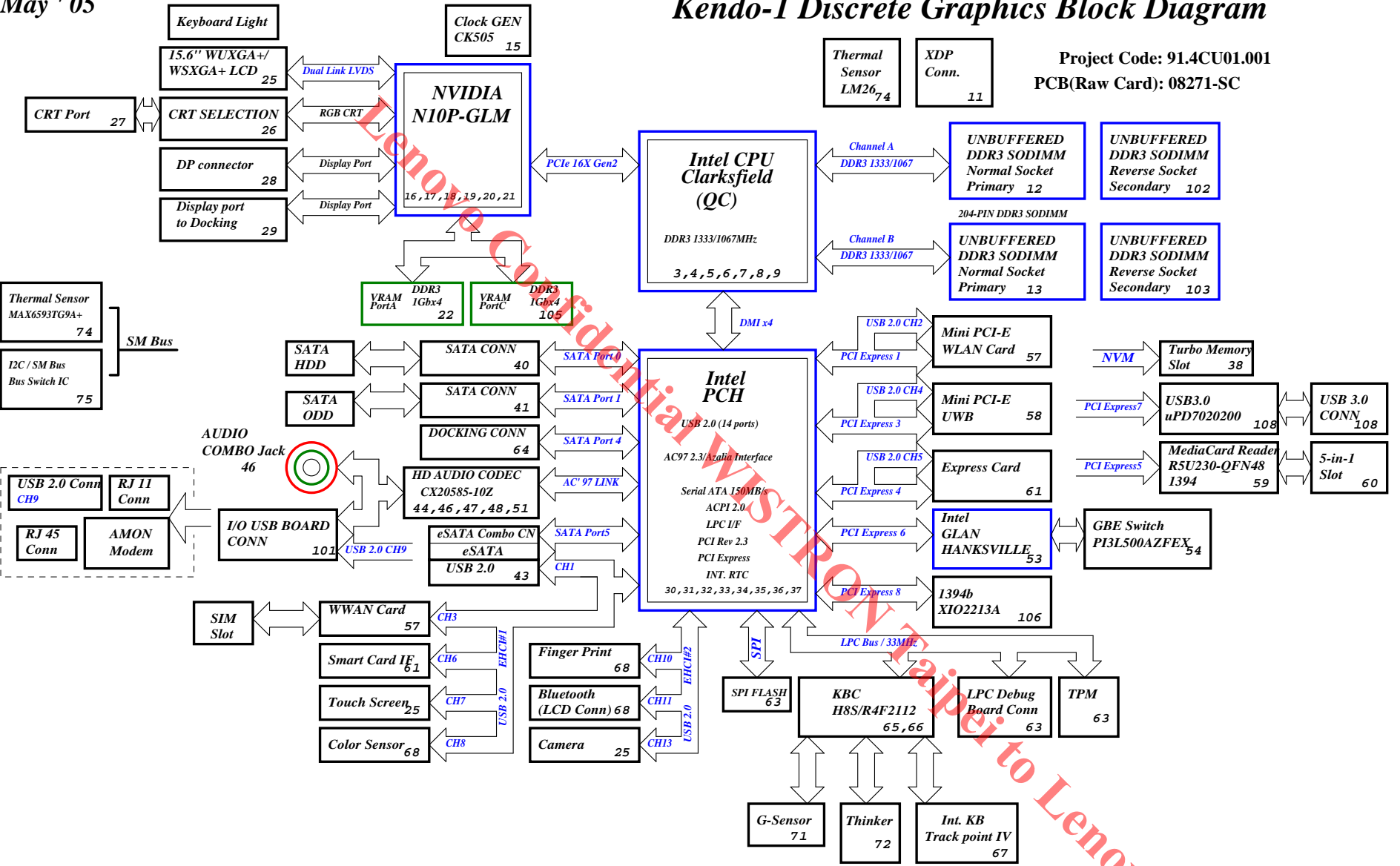


# Kendo-1 Discrete Graphics Block Diagram

Project Code: 91.4CU01.001  
PCB(Raw Card): 08271-SC



**PCB Layer Stackup**

L1: TOP
L2: Signal 1
L3: GND
L4: Signal 2
L5: VCC
L6: GND
L7: Signal 3
L8: GND
L9: Signal 4
L10: BOTTOM

**Battery Charger/Selector**

BQ24741RHDR	78
INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR
	S-BAT-PWR
<b>System DC/DC</b>	
TPS51222RTV	82
VINT20	VCC5M
	VCC3M
<b>CPU DC/DC</b>	
ADP3212MNR2G	83
VINT20	VCCPUCORE
<b>GFXCORE_D</b>	
ADP3211	85
VINT20	VCCGFXCORE
<b>VCCIR5VIDEO</b>	
MAX8792	91
VCC5M	VCCIR5VIDEO
<b>VCCIR5A/DDR3_REF</b>	
VCC0R75B	89
TPS51116	
VCC5M	DDR3_VREF
	VCC0R75B
	VCCIR5A
<b>VCCIR8B</b>	
BD3551	92
VCC3M	VCCIR8B
VCC5M	
<b>VCCIRIB_VTT</b>	
VCCIR05LAN	87
ISL6228	
VINT20	VCCIRIB_VTT
VCC5M	VCCIR05LAN

<Variant Name>

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Title: **Block Diagram**

Size: Custom Document Number: **Kendo-1 WS** Rev: **SC**

Date: 1:uescdw, May 05, 2009 Sheet: 1 of 109

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**RESISTOR**

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance  
 For the value, it can be read by the number before R. (R means resistor)  
 For the tolerance, it can be read from the last letter.  
 For the rating, we don't show on the symbol name.  
 For the size, R2=>0402, R3=>0603, R5=>0805,.....

**CAPACITOR**

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is  
 Capacitor type + value + rating + size + tolerance + material  
 SCD1U10V2MX-1  
 SC=> SMT Ceremic, TC=> POS cap or SP cap  
 D1U => 0.1uF  
 10V => the voltage rating is 10V  
 2=> 0402, 3=>0603, 5=>0805  
 M=>tolerance M, K, Z  
 X=> X7R/X5R, Y=> Y5V  
 -1 => symbol version, nonsense to EE characteristic

**PLANAR\_ID[3..0]**

PCH GPIO <sub>n</sub>	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_ID <sub>n</sub>	3	2	1	0		
	0	0	0	0	SDV	SA
	0	0	0	1	SDV-R	SB
	0	0	1	0	SIV	SC
	0	0	1	1		
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

**EC HISTORY**

Stage	Date	EC No.	Page	Note
SIV	0413	001-018		EC_21_DIS_SIV_090413
SIV	0420	019-033		EC_21_DIS_SIV_090420
SIV	0424	034-044		EC_21_DIS_SIV_090424
SIV	0429	045-067		EC_21_DIS_SIV_090429

**PCI TABLE**

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#3
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#0
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>

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 Taipei Hsien 221, Taiwan, R.O.C.

**EC HISTORY**

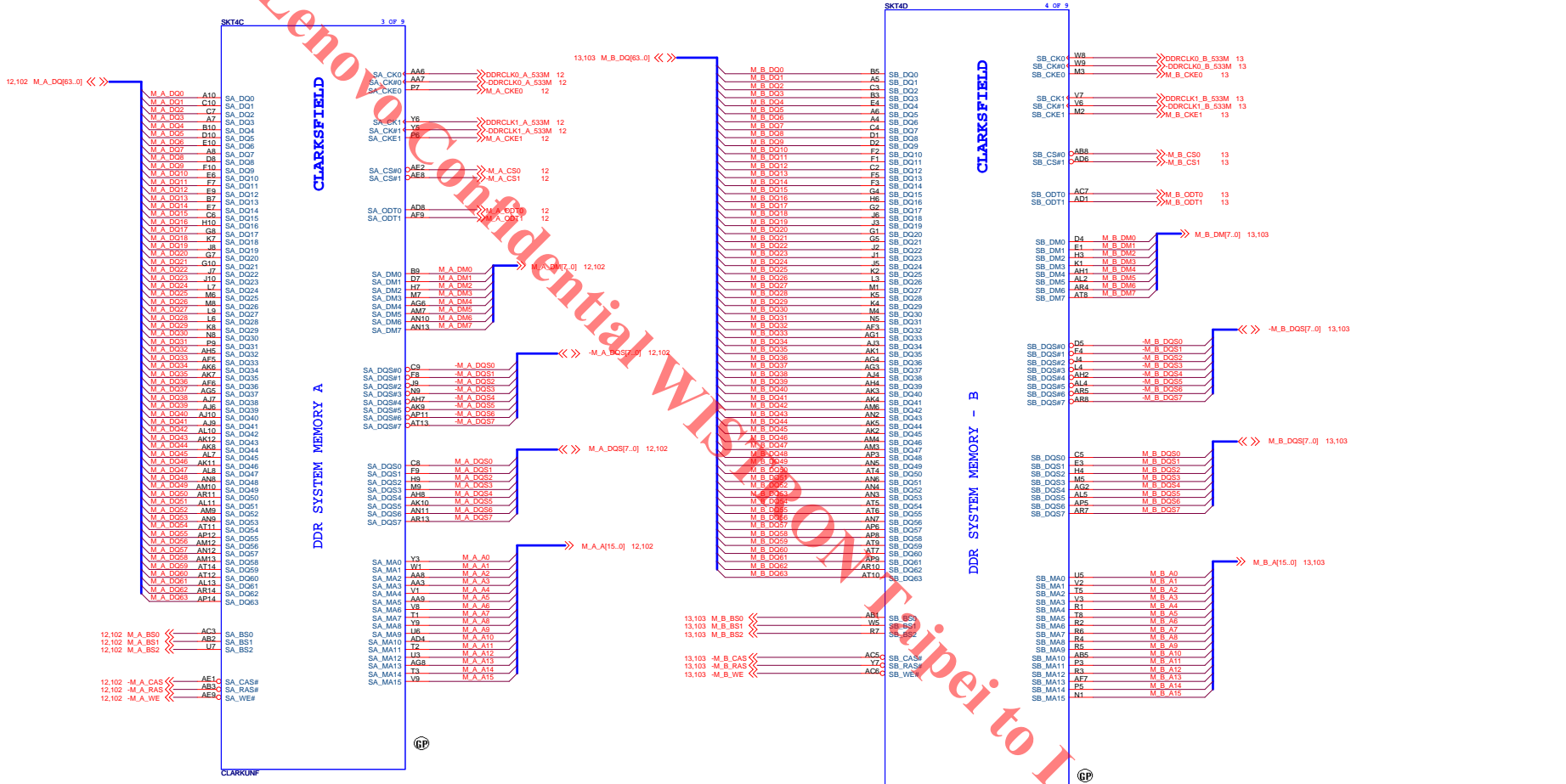
Size A3 Document Number **Kendo-1 WS** Rev SC

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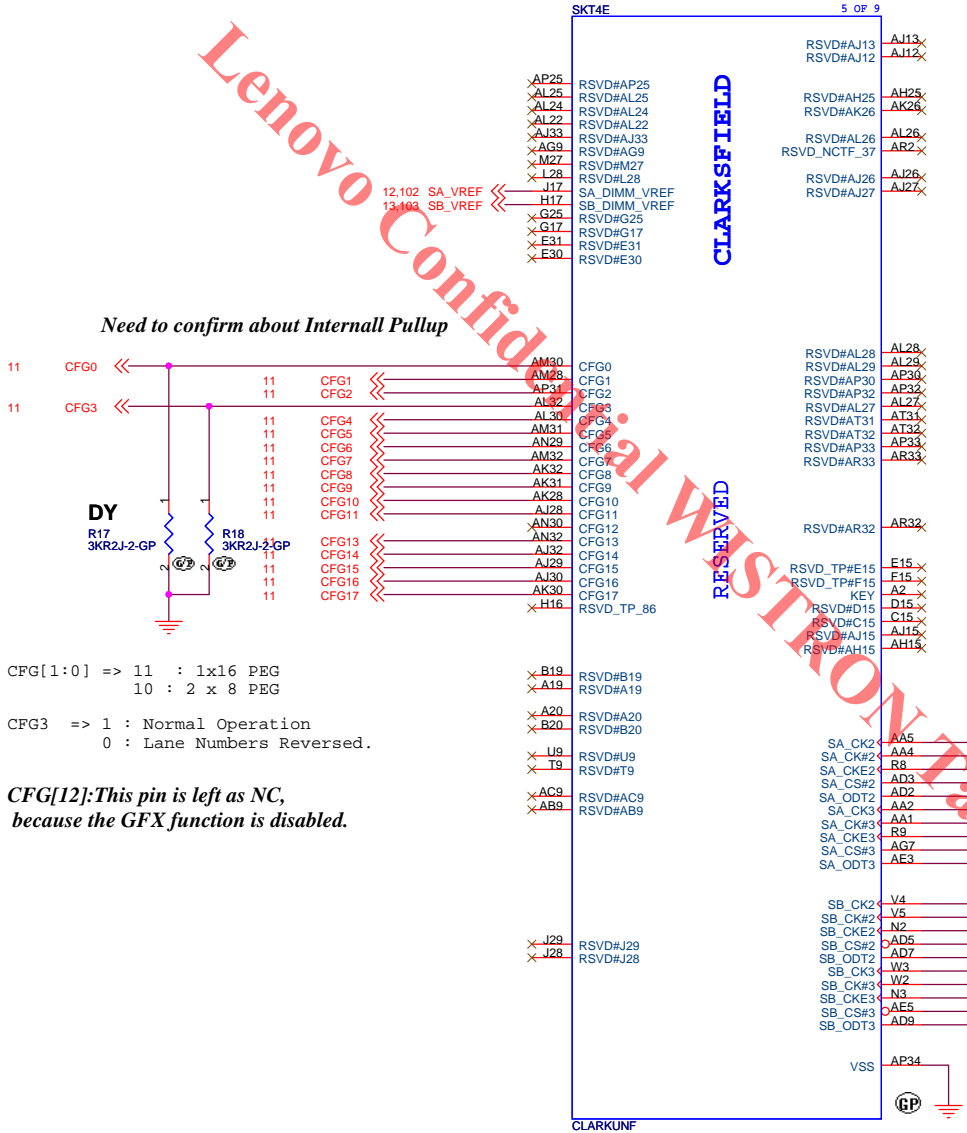


12,102 M\_A\_B50 <<< AC1 SA\_B50  
12,102 M\_A\_BS1 <<< AB2 SA\_BS1  
12,102 M\_A\_BS2 <<< U7 SA\_BS2  
  
12,102 -M\_A\_CAS <<< AE1 SA\_CAS#  
12,102 -M\_A\_RAS <<< AB3 SA\_RAS#  
12,102 -M\_A\_WE <<< AE3 SA\_WE#

13,103 M\_B\_B50 <<< AB1 SB\_B50  
13,103 M\_B\_BS1 <<< W5 SB\_BS1  
13,103 M\_B\_BS2 <<< R7 SB\_BS2  
  
13,103 -M\_B\_CAS <<< AC3 SB\_CAS#  
13,103 -M\_B\_RAS <<< Y3 SB\_RAS#  
13,103 -M\_B\_WE <<< AC6 SB\_WE#

M\_B\_A0 M\_B\_A15, 0] 13,103  
M\_B\_A0 M\_B\_A15, 0] 13,103  
M\_B\_DQ[7..0] 13,103  
M\_B\_DQ[7..0] 13,103  
M\_B\_DQ[63..0] <<< 13,103  
M\_B\_DQ[63..0] <<< 13,103  
M\_B\_DM[7..0] 13,103  
M\_B\_DM[7..0] 13,103

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*Need to confirm about Internall Pullup*

CFG[1:0] => 11 : 1x16 PEG  
 10 : 2 x 8 PEG

CFG3 => 1 : Normal Operation  
 0 : Lane Numbers Reversed.

*CFG[12]: This pin is left as NC, because the GFX function is disabled.*

CLARKSFIELD

RESERVED

**4-DIMM SUPPORT**

- VCCPUCORE
- SKT4F
- 6 OF 9
- AG35 VCC
- AG34 VCC
- AG33 VCC
- AG32 VCC
- AG31 VCC
- AG30 VCC
- AG29 VCC
- AG28 VCC
- AG27 VCC
- AG26 VCC
- AF35 VCC
- AF34 VCC
- AF33 VCC
- AF32 VCC
- AF31 VCC
- AF30 VCC
- AF29 VCC
- AF28 VCC
- AF27 VCC
- AF26 VCC
- AD35 VCC
- AD34 VCC
- AD33 VCC
- AD32 VCC
- AD31 VCC
- AD30 VCC
- AD29 VCC
- AD28 VCC
- AD27 VCC
- AD26 VCC
- AC35 VCC
- AC34 VCC
- AC33 VCC
- AC32 VCC
- AC31 VCC
- AC30 VCC
- AC29 VCC
- AC28 VCC
- AC27 VCC
- AC26 VCC
- AA35 VCC
- AA34 VCC
- AA33 VCC
- AA32 VCC
- AA31 VCC
- AA30 VCC
- AA29 VCC
- AA28 VCC
- AA27 VCC
- AA26 VCC
- Y35 VCC
- Y34 VCC
- Y33 VCC
- Y32 VCC
- Y31 VCC
- Y30 VCC
- Y29 VCC
- Y28 VCC
- Y27 VCC
- Y26 VCC
- V35 VCC
- V34 VCC
- V33 VCC
- V32 VCC
- V31 VCC
- V30 VCC
- V29 VCC
- V28 VCC
- V27 VCC
- V26 VCC
- U35 VCC
- U34 VCC
- U33 VCC
- U32 VCC
- U31 VCC
- U30 VCC
- U29 VCC
- U28 VCC
- U27 VCC
- U26 VCC
- R35 VCC
- R34 VCC
- R33 VCC
- R32 VCC
- R31 VCC
- R30 VCC
- R29 VCC
- R28 VCC
- R27 VCC
- R26 VCC
- P35 VCC
- P34 VCC
- P33 VCC
- P32 VCC
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- P29 VCC
- P28 VCC
- P27 VCC
- P26 VCC
- CLARKUNF

**CLARKSFIELD**

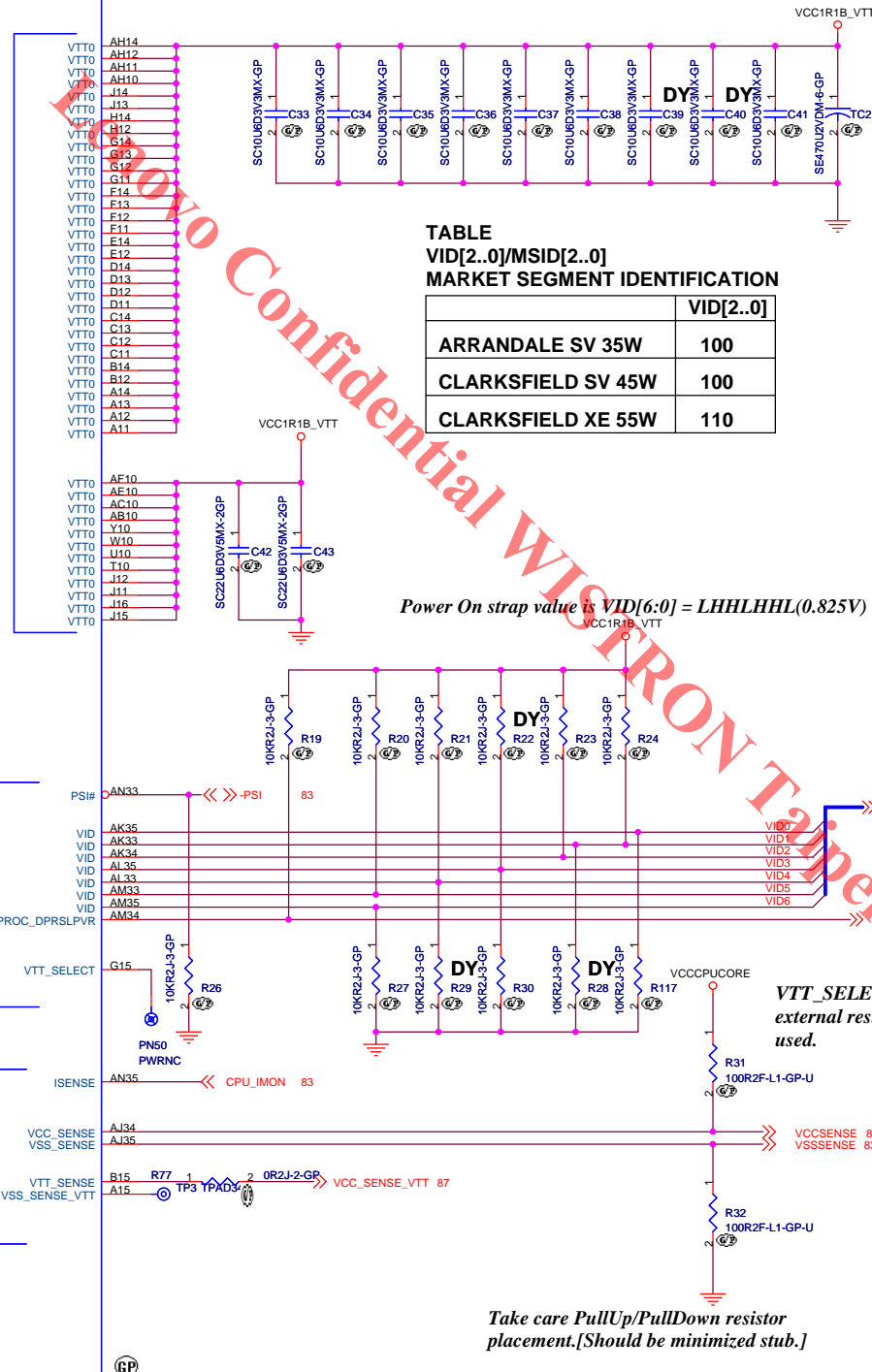
**1.1V RAIL POWER**

**CPU CORE SUPPLY**

**POWER**

**CPU VIDS**

**SENSE LINES**



**TABLE VID[2..0]/MSID[2..0] MARKET SEGMENT IDENTIFICATION**

	VID[2..0]
ARRANDALE SV 35W	100
CLARKSFIELD SV 45W	100
CLARKSFIELD XE 55W	110

**TABLE VID[5..3]/CSC[2..0] CURRENT SENSE CONFIGURATION**

IMAX	VID[5..3]
DISABLE	000
20A	001
30A	010
40A	011
50A	100
60A	101
70A	110
80A	111

ARD SV Iccmax=48A  
CF SV Iccmax=52A  
CF XE Iccmax=65A

Power On strap value is VID[6:0] = LHHLLH (0.825V)

VTT\_SELECT [G15]: Because VCC1R05B\_VTT is fixed voltage with external resistor in DC/DC circuits, the VTT\_SELECT signal is not used.

Take care PullUp/PullDown resistor placement. [Should be minimized stub.]

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**CPU(5/7): PWR**

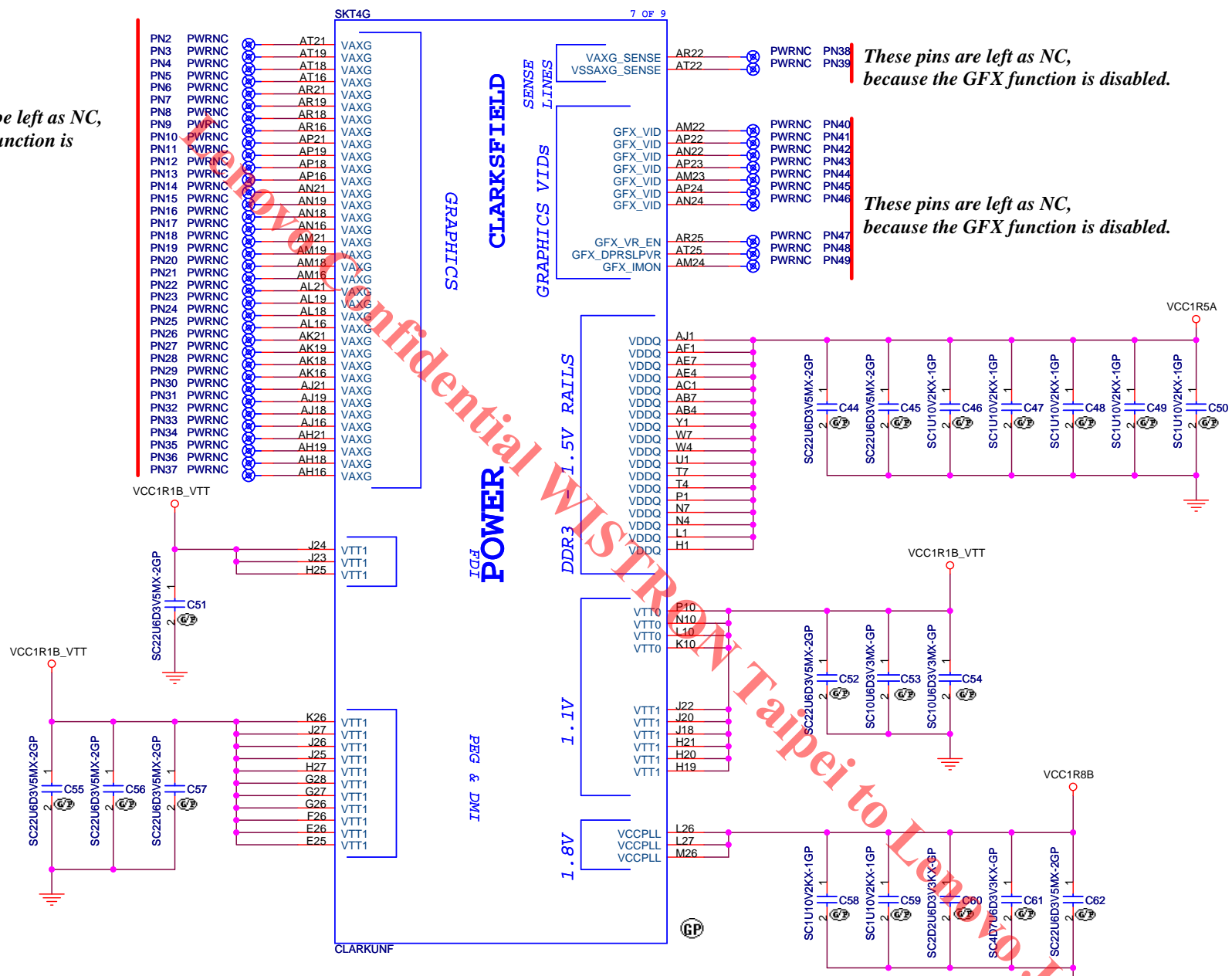
Document Number: **Kendo-1 WS**

Date: Tuesday, May 05, 2009 Sheet 7 of 109


These pins should be left as NC, because the GFX function is disabled.

These pins are left as NC, because the GFX function is disabled.

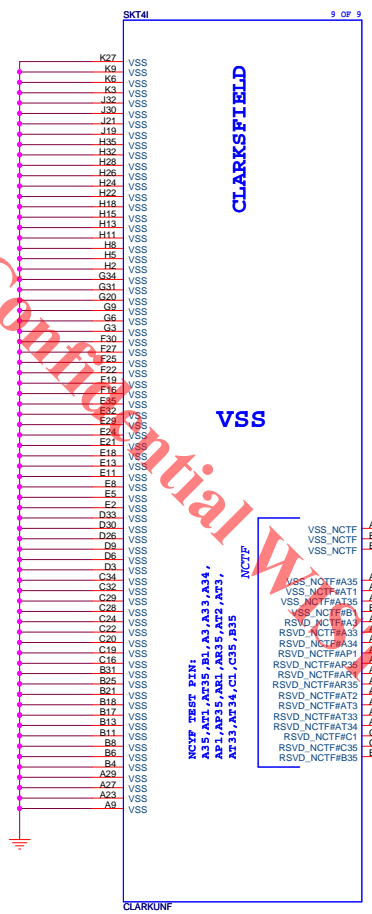
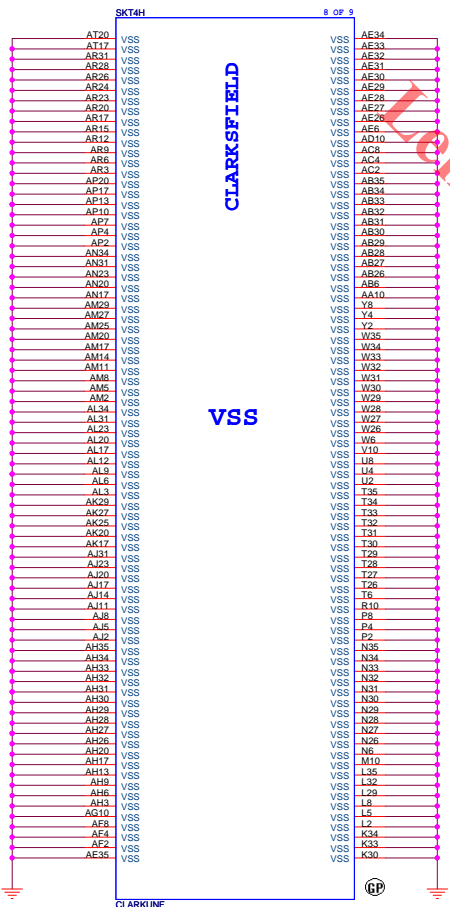
These pins are left as NC, because the GFX function is disabled.



<Variant Name>

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Title	
<b>CPU(6/7): GFX/PWR</b>	
Size	Document Number
Custom	<b>Kendo-1 WS</b>
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NCTF TEST PIN:  
 A35, AT1, AT35, B1, A3, A33, A34,  
 AP1, AP35, AM1, AR35, AT2, AT3,  
 AT31, AT34, CL, C15, B35

NCTF

AR34  
 B34  
 B2

VSS, NCTF  
 VSS, NCTF  
 VSS, NCTF

A35  
 AT1  
 AT35  
 B1

VSS, NCTF#A35  
 VSS, NCTF#A11  
 VSS, NCTF#A35  
 VSS, NCTF#B1

A33 X  
 A34 X  
 AP1 X  
 AP35 X  
 AR1 X  
 AR35 X  
 AT2 X  
 AT3 X  
 AT34 X  
 CL X  
 C15 X  
 B35 X

TP6 TPAD34  
 TP7 TPAD34  
 TP8 TPAD34  
 TP9 TPAD34

For Solder Crack Detection.

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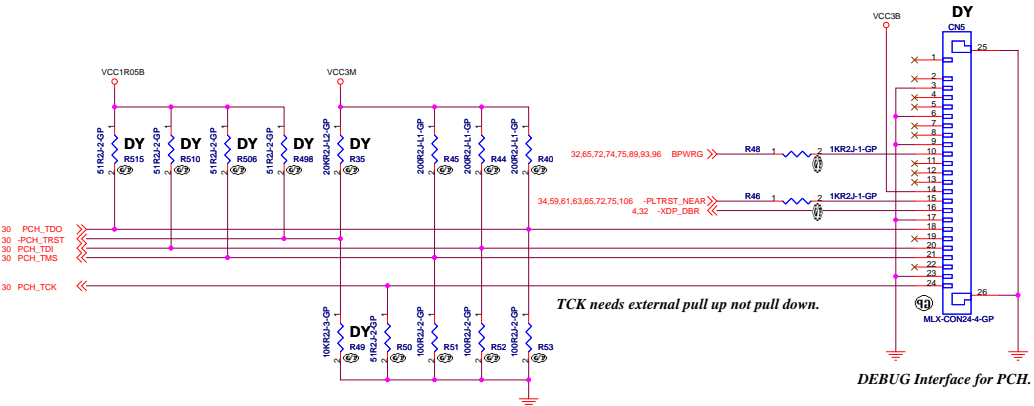
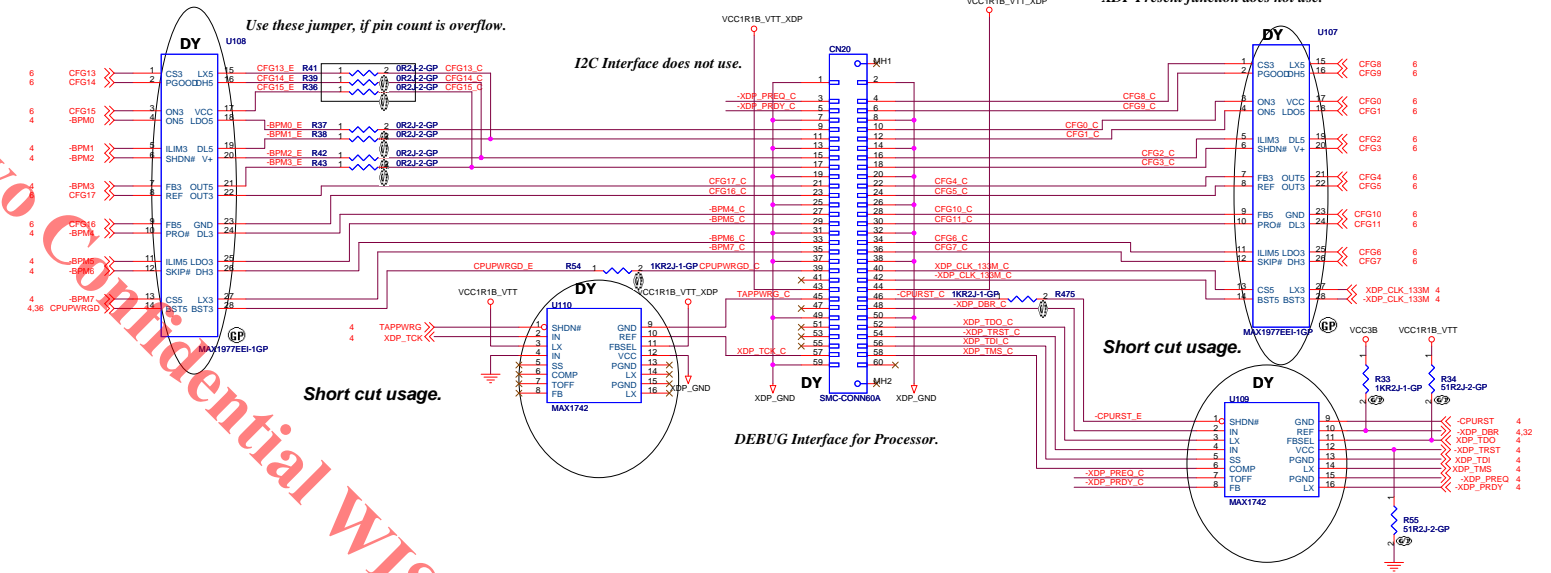
**BLANK**

-<Variant Name>		
緯創資通		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title		
<b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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PCH PIN	REF DES	PCH ES1 JTAG		PCH ES2 JTAG		MP	
		ENABLE	DISABLE	ENABLE	DISABLE	ENABLE	DISABLE
TDO	R40	NO_ASM	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R53	NO_ASM	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R515	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TMS	R37	200	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R51	100	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R510	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TDI	R38	200	20K	200	NO_ASM	NO_ASM	NO_ASM
	R52	100	10K	100	NO_ASM	NO_ASM	NO_ASM
	R506	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TCK	R50	51	51	51	51	51	51
TRST#	R35	20K	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
	R49	10K	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
	R498	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM

↑ LOGIC



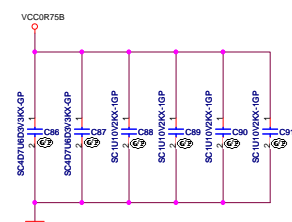
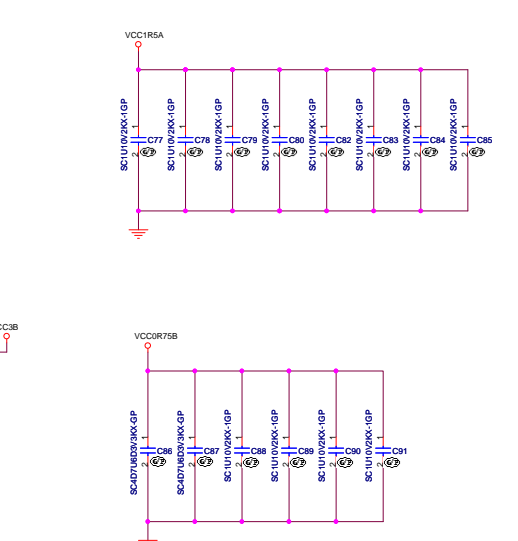
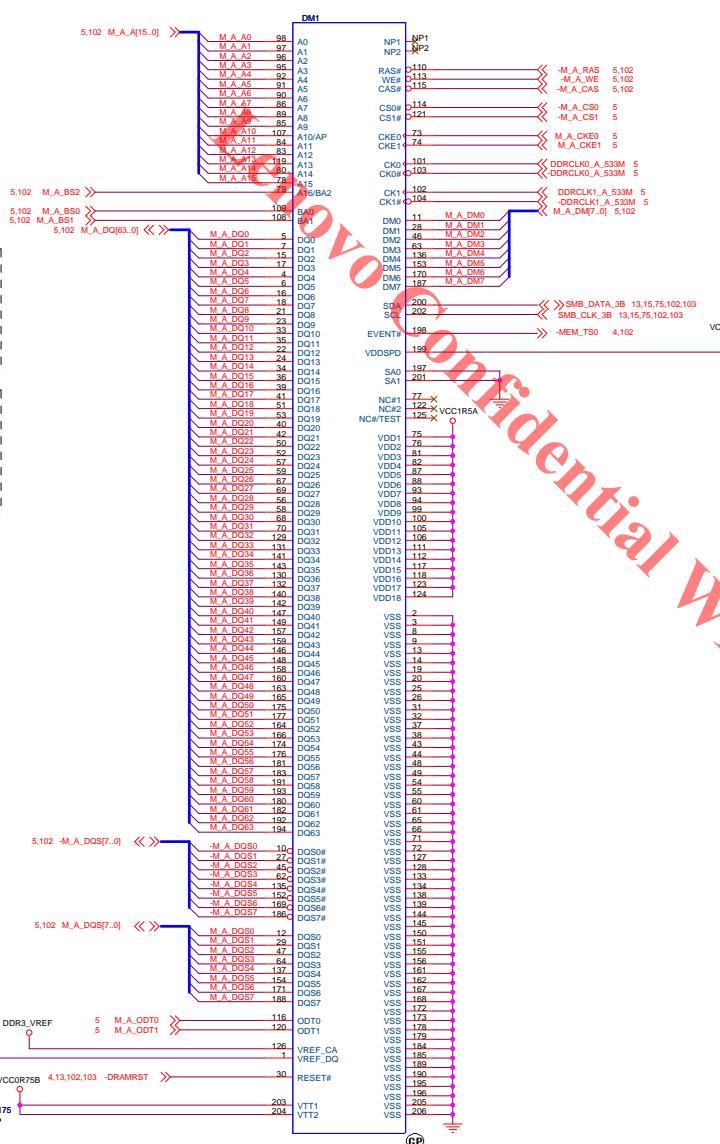
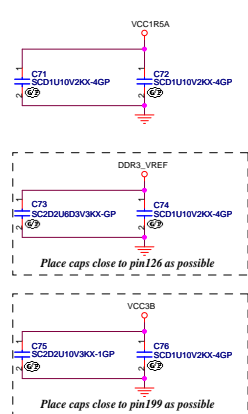
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**XDP CONNECTOR**

File: XDP CONNECTOR  
Size: A4  
Date: Tuesday, May 05, 2009

Document Number: Kendo-1 WS  
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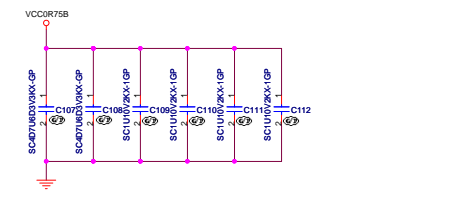
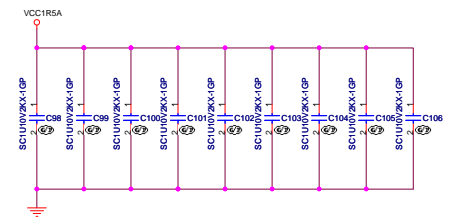
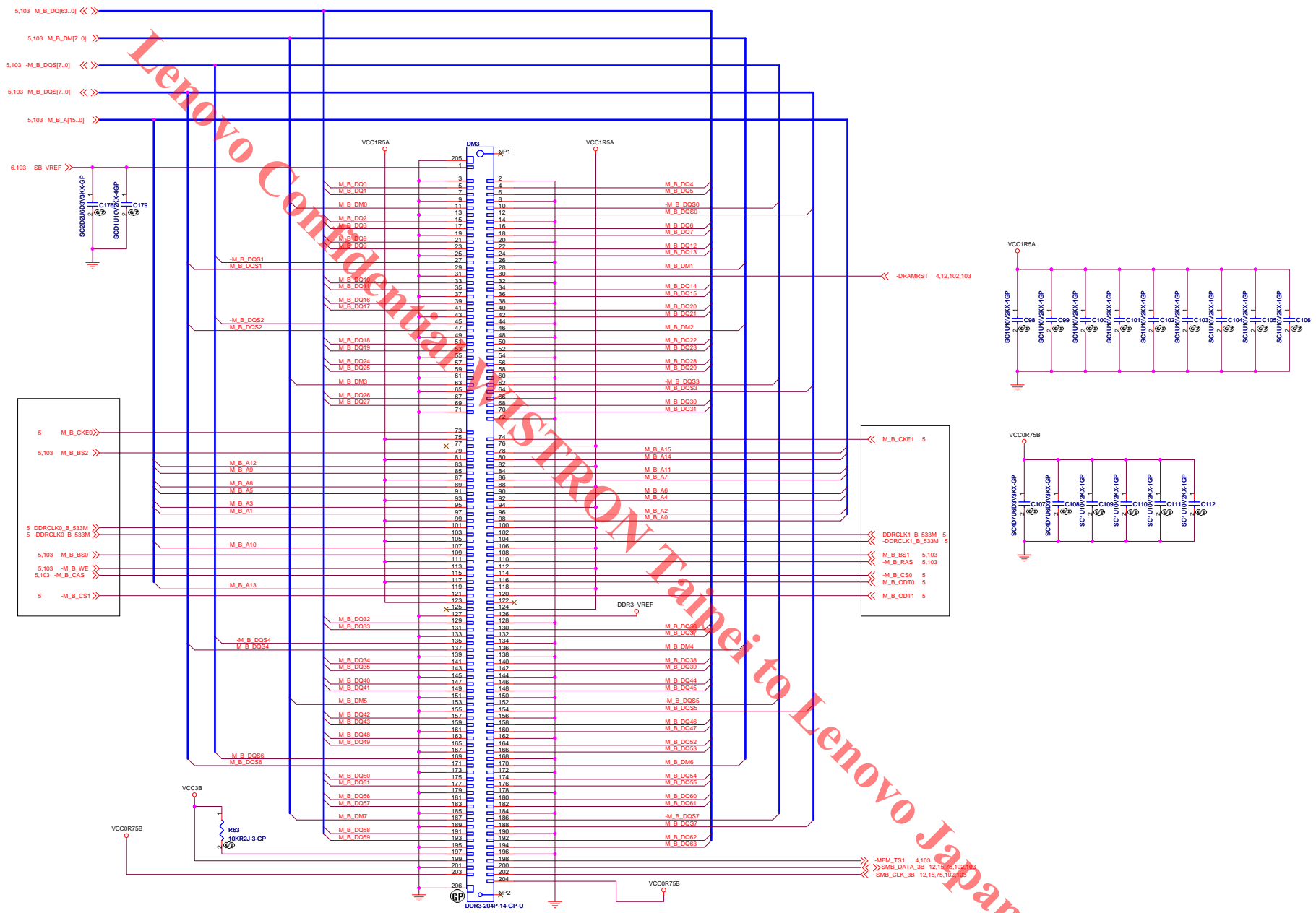
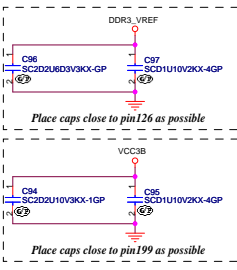
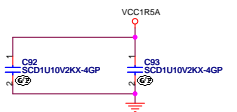
Rev: SC



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SPD Address : 50h

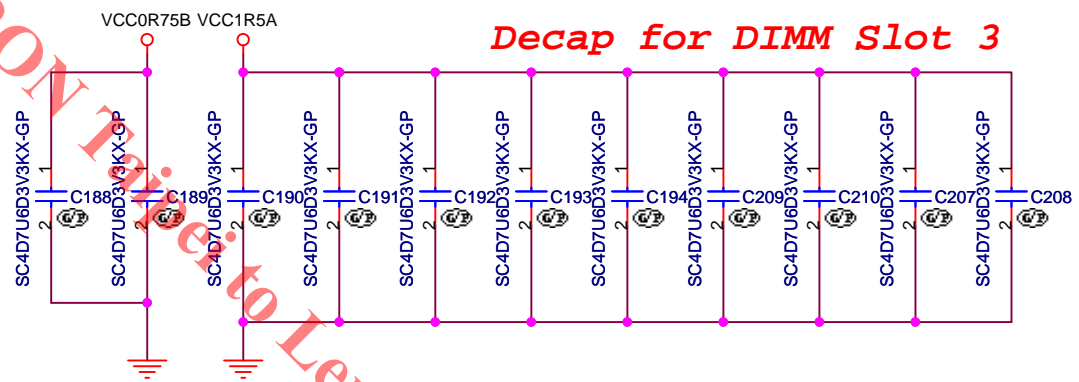
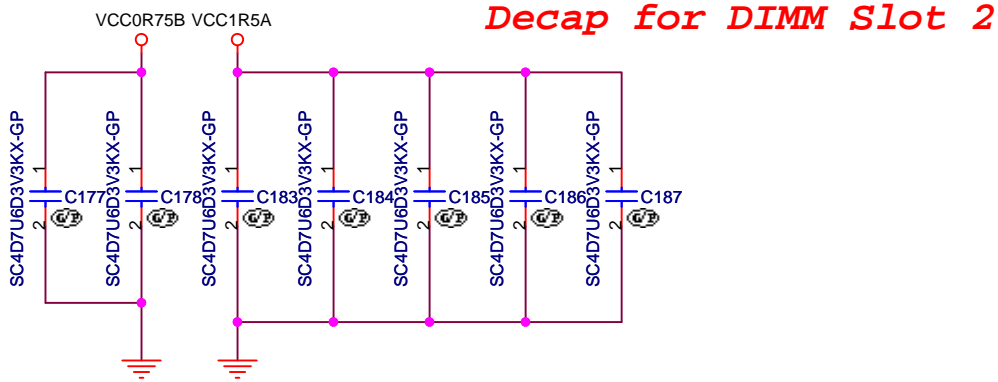
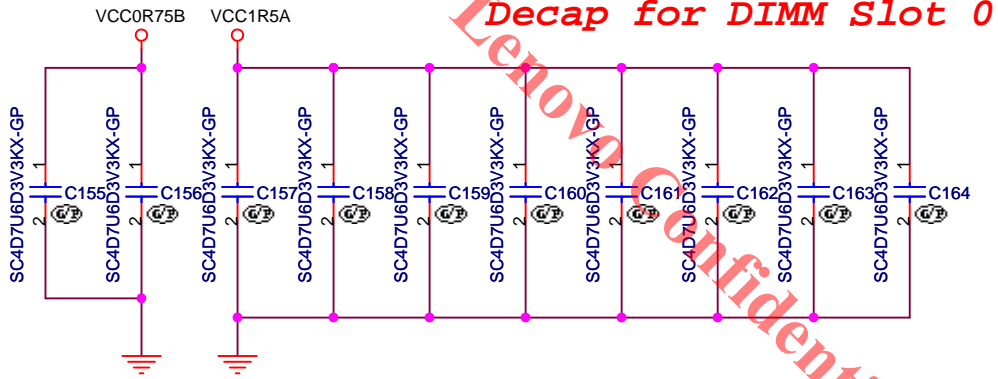
This connector should be placed on far side from CPU.



- 5 M.B\_CKE0
- 5,103 M.B\_BS2
- 5 DDRCLK0\_B\_533M
- 5 -DDRCLK0\_B\_533M
- 5,103 M.B\_BS0
- 5,103 -M.B\_WE
- 5,103 -M.B\_CAS
- 5 -M.B\_CS1

- M.B\_CKE1 5
- DDRCLK1\_B\_533M 5
- DDRCLK1\_B\_533M 5
- M.B\_BS1 5,103
- M.B\_BS0 5,103
- M.B\_CS0 5
- M.B\_CS1 5
- M.B\_ODT0 5
- M.B\_ODT1 5

SPD Address : \$1h  
 This connector should be placed on far side from CPU.



<Variant Name>

緯創資通

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Title  
**DECAP For DIMMs**

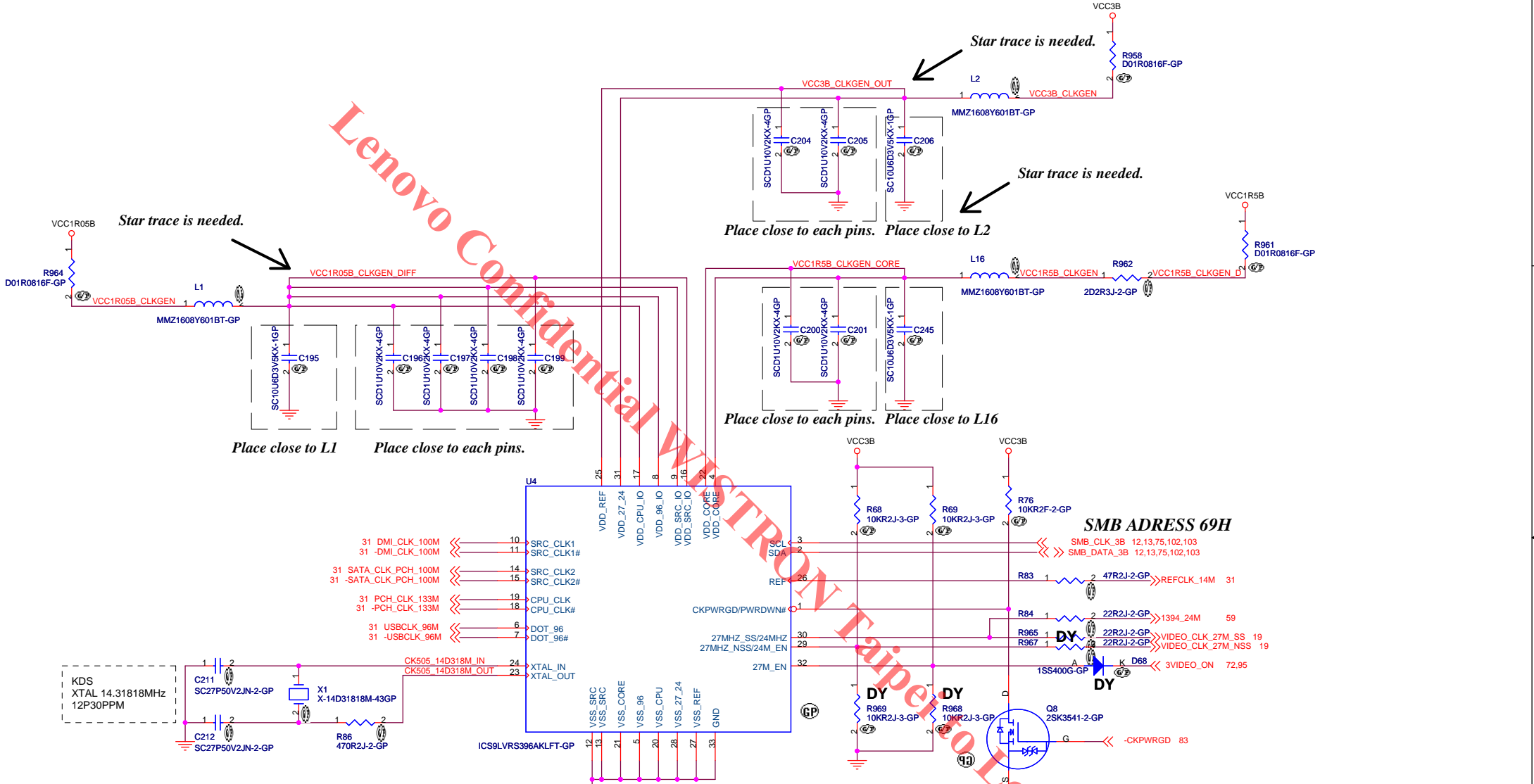
Size A4 Document Number

**Kendo-1 WS**

Rev  
**SC**

Date: Tuesday, May 05, 2009

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[Source Cadidate]		
SILEGO	SLG8LV569VTR	71.08569.003
IDT	IC9LVRS396AKLF-T	71.09396.003
REALTEK	RTM890N-634-GRT	71.00890.003

**KDS Recommended Conditions:**  
 Normal Frequency: 14.318180MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 12pF.  
 Effective Series Resistance: 50-ohm.  
 Effective Shunt Capacitance: 7pF.

**IDT Recommended Conditions:**  
 Normal Frequency: 14.318MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 20pF.  
 Effective Shunt Capacitance: 5pF.

**Realtek Recommended Conditions:**  
 Normal Frequency: 14.318MHz.  
 Frequency Tolerance: +/- 50ppm.  
 Load Frequency: 10 ~ 32 pF.  
 Effective Series Resistance: 40-ohm max.  
 Effective Shunt Capacitance: 7pF.

**Silego Recommended Conditions:**  
 Normal Frequency: 14.31818MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 20 pF.  
 Effective Shunt Capacitance: 5pF.

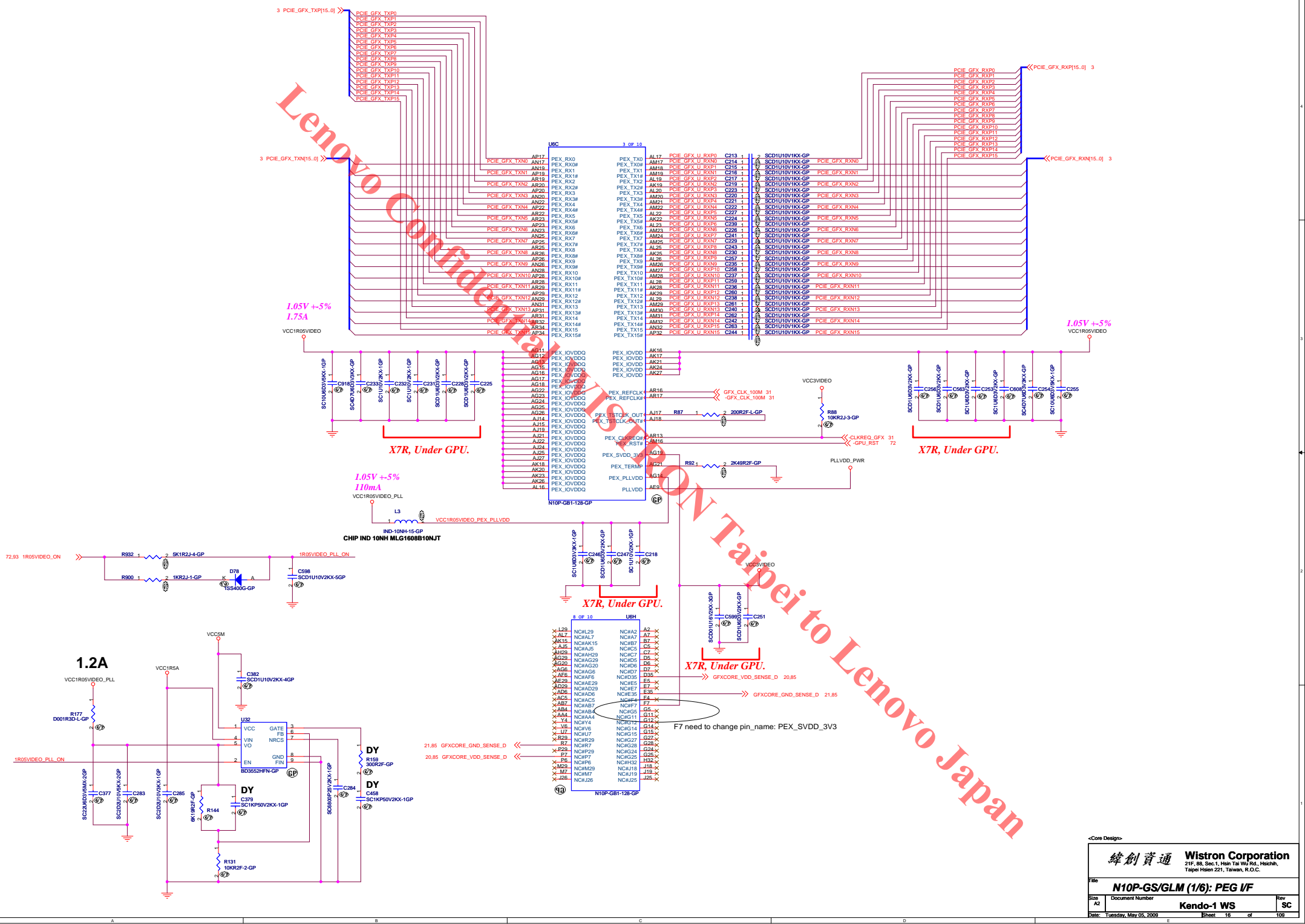
Variant Name:

**緯創資通 Wistron Corporation**  
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 Taipei Hsien 221, Taiwan, R.O.C.

**CLOCK GEN**

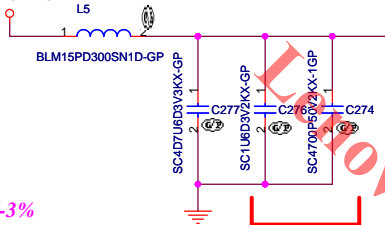
Size A3	Document Number	Rev SC
<b>Kendo-1 WS</b>		
Date: Tuesday, May 05, 2009	Sheet 15	of 109

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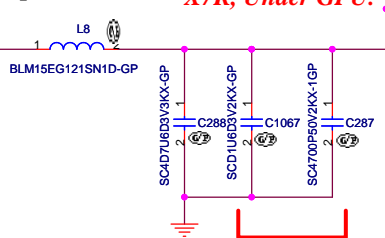


1.8V +/-3%  
200mA  
VCC1R8VIDEO



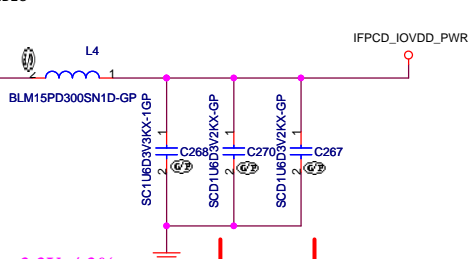
X7R, Under GPU.

1.05V +/-3%  
200mA  
VCC1R05VIDEO\_PLL



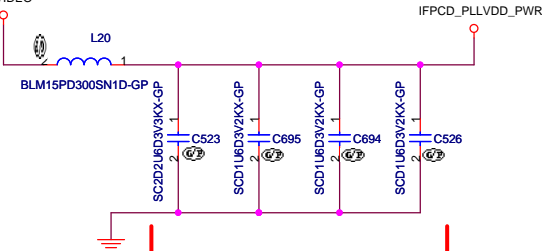
X7R, Under GPU.

1.05V +/-3%  
500mA  
VCC1R05VIDEO

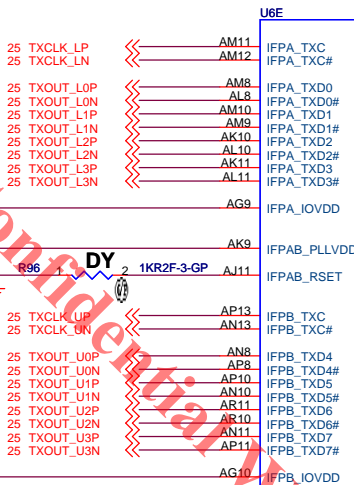


Under GPU.

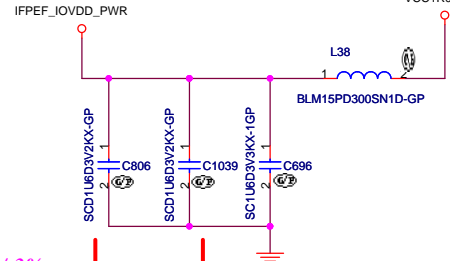
3.3V +/-3%  
200mA  
VCC3VIDEO



Under GPU.

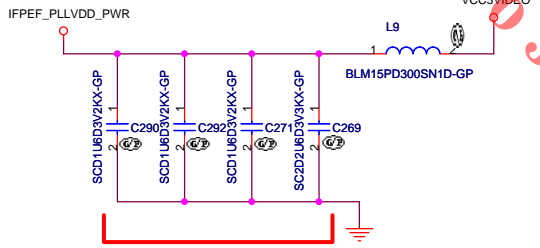


1.05V +/-3%  
500mA  
VCC1R05VIDEO



Under GPU.

3.3V +/-3%  
200mA  
VCC3VIDEO



Under GPU.

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**N10P-GS/GLM(2/6): DIGITAL OUT**

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Date: Tuesday, May 05, 2009  
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I2CA=>CRT, I2CC=>LVDS.

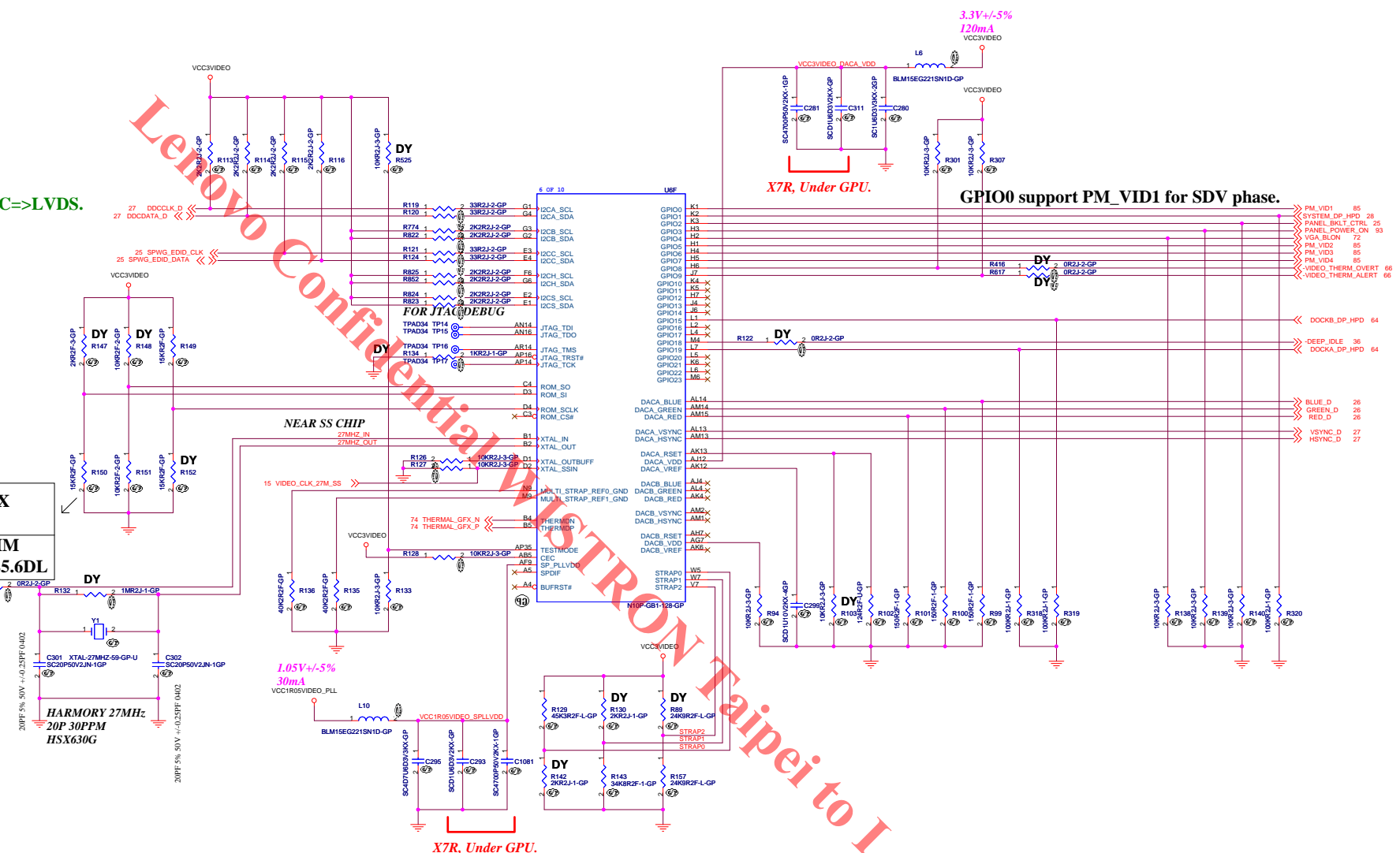


TABLE  
KENDO VIDEO MEMORY

	SAMSUNG 0011	HYNIX 0010
R150	20KOHM 64.20025.6DL	15KOHM 64.15025.6DL

TABLE  
KENDO NIVIDA

	VENDOR P/N	WISTRON P/N
1	N10P-GLM-GB1-128 (40nm) Workstation	71.0N10P.A2U
2	N10P-GS-GB1-128 (40nm) Discrete	71.0N10P.B2U

PCI\_DEVID Straps

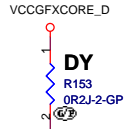
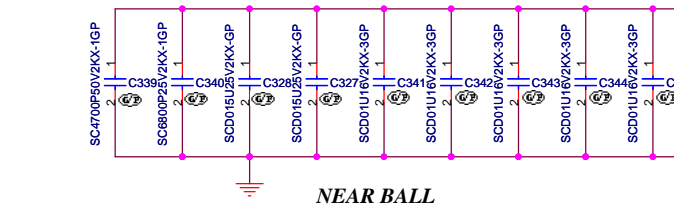
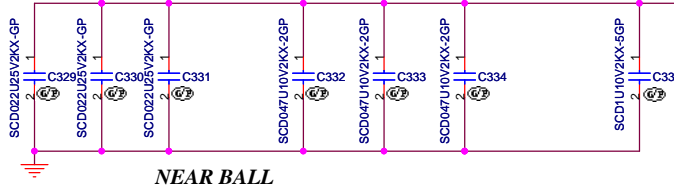
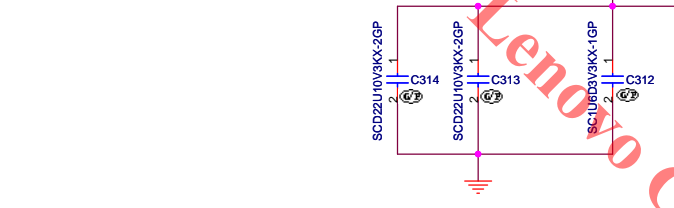
	N10P/M-GS	N10P-GLM
R89	NO_ASM	ASM
R157	ASM	NO_ASM

LOGIC

22A

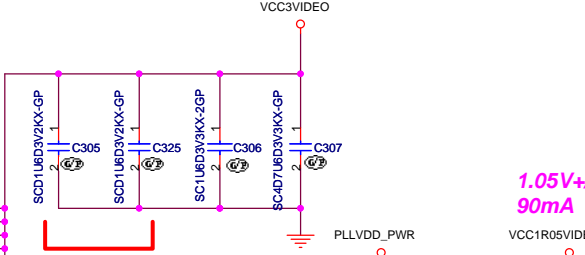
VCCGFXCORE\_D

NEAR GPU



16.85 GFXCORE\_VDD\_SENSE\_D

VCCGFXCORE\_D



1.05V +/- 5% 90mA

X7R, Under GPU.

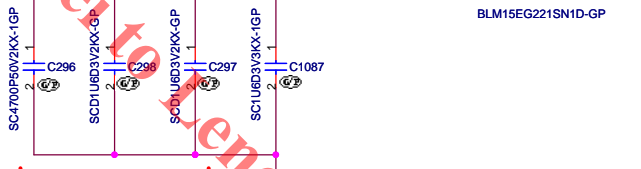
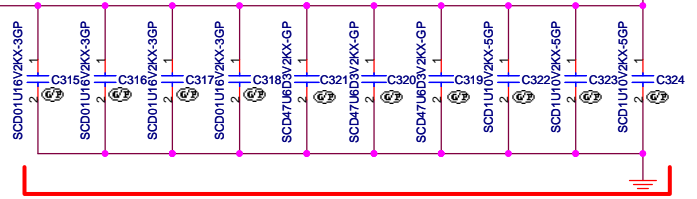
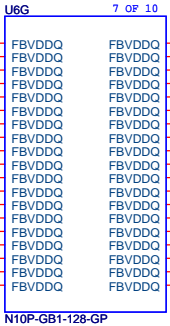
6.5A

VCC1R5VIDEO

X7R, Under GPU.

X7R, Under GPU.

VCC1R5VIDEO



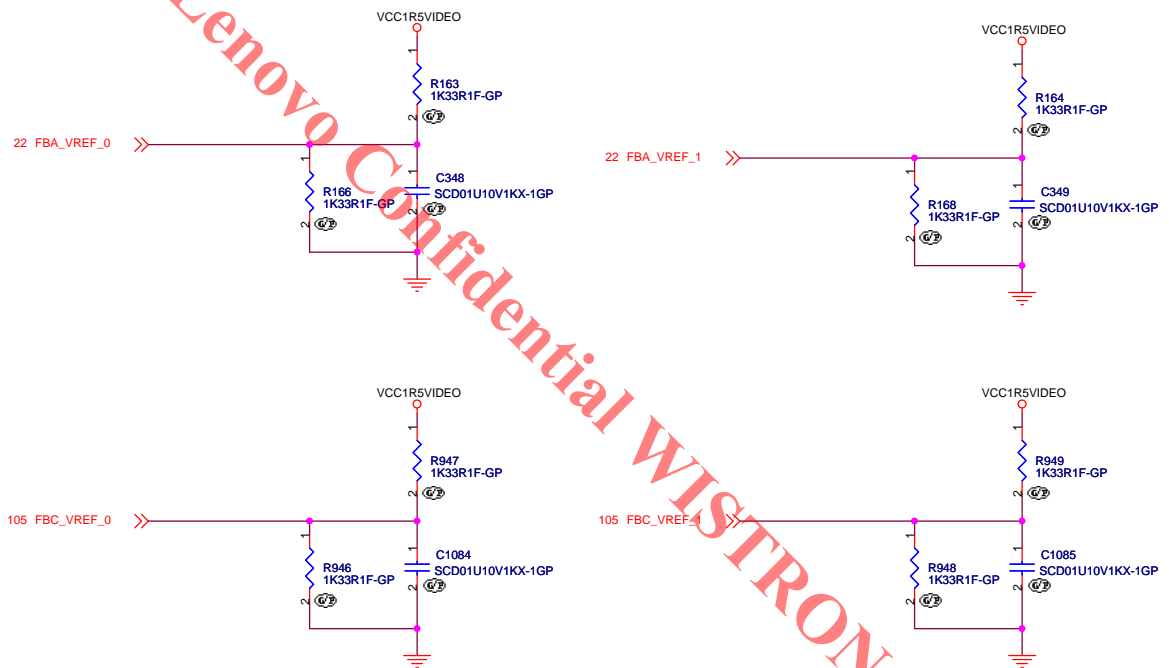
<Core Design>

緯創資通 Wistron Corporation  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title		
<b>N10P-GS/GLM (5/6): POWER</b>		
Size	Document Number	Rev
A3	<b>Kendo-1 WS</b>	<b>SC</b>
Date:	Tuesday, May 05, 2009	Sheet 20 of 109







**FBCLK Termination**

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<Core Design>		
<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>DYNAMIC MEMORY TERMINATION</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 23	of 109

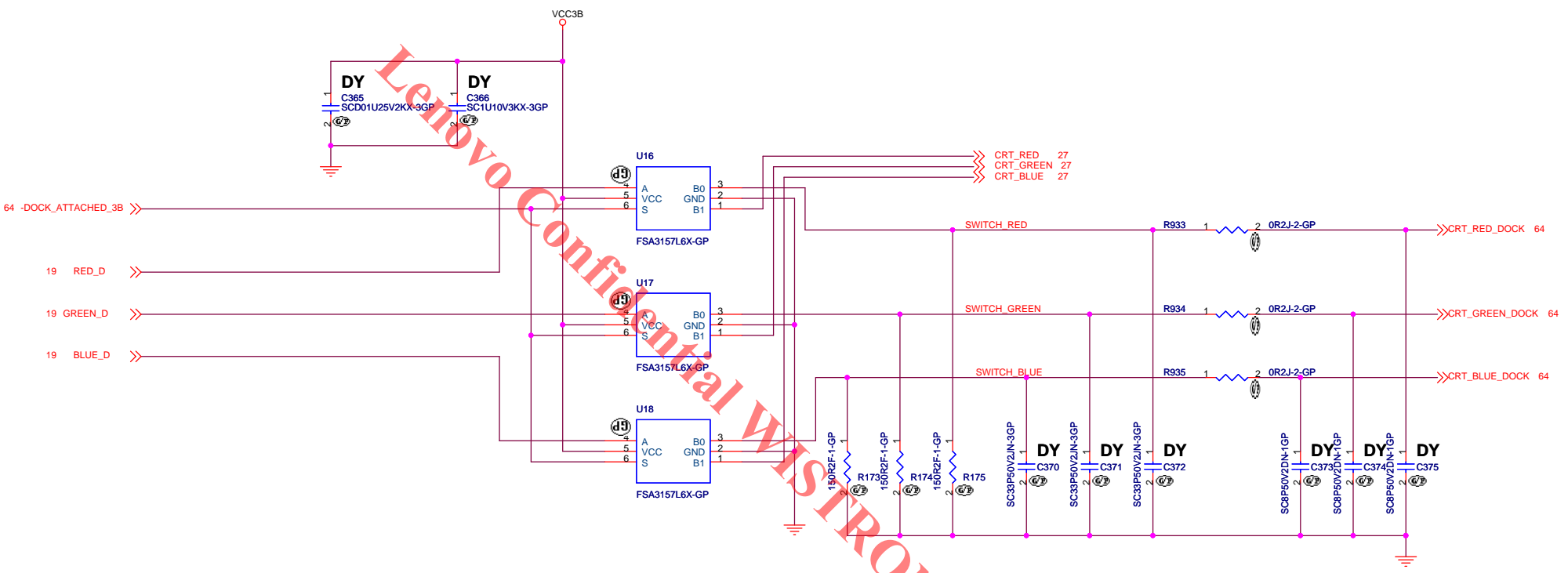
Lenovo Confidential WISTRON Taipei to Lenovo Japan

**BLANK**

-<Variant Name->		
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 24 of 109







	Supplier	Vendo P/N	WISTRON P/N
1	FAIRCHILD	FSA3157L6X-GP	73.03157.G0H
2	ONSEMI	NLASB3157MTR2G	73.03157.003
3	TI	SN74LVC1G3157DRYR	73.03157.H0H

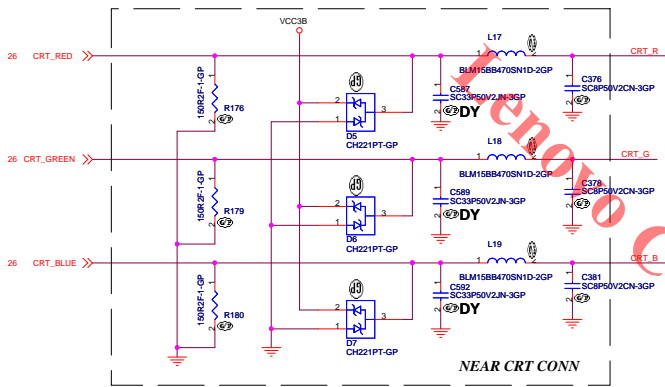
Lenovo Confidential WISTRON Taipei to Lenovo Japan

Variant Name: >

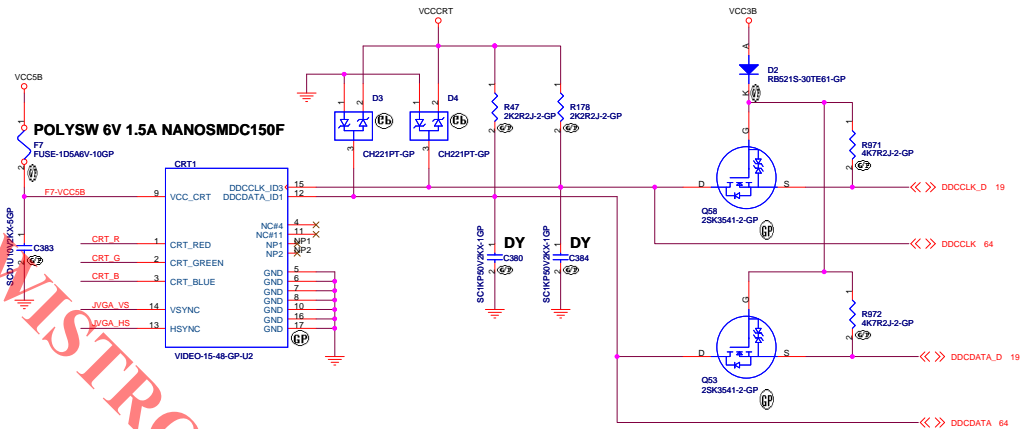
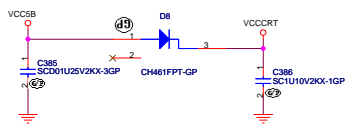
**緯創資通** **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: **RGB SWITCH**

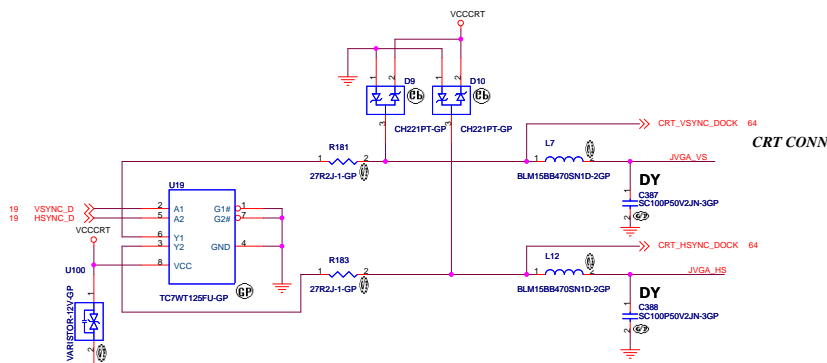
Size A3	Document Number	Rev SC
<b>Kendo-1 WS</b>		
Date: Tuesday, May 05, 2009	Sheet 26	of 109



NEAR CRT CONN



To avoid leak current from a monitor.



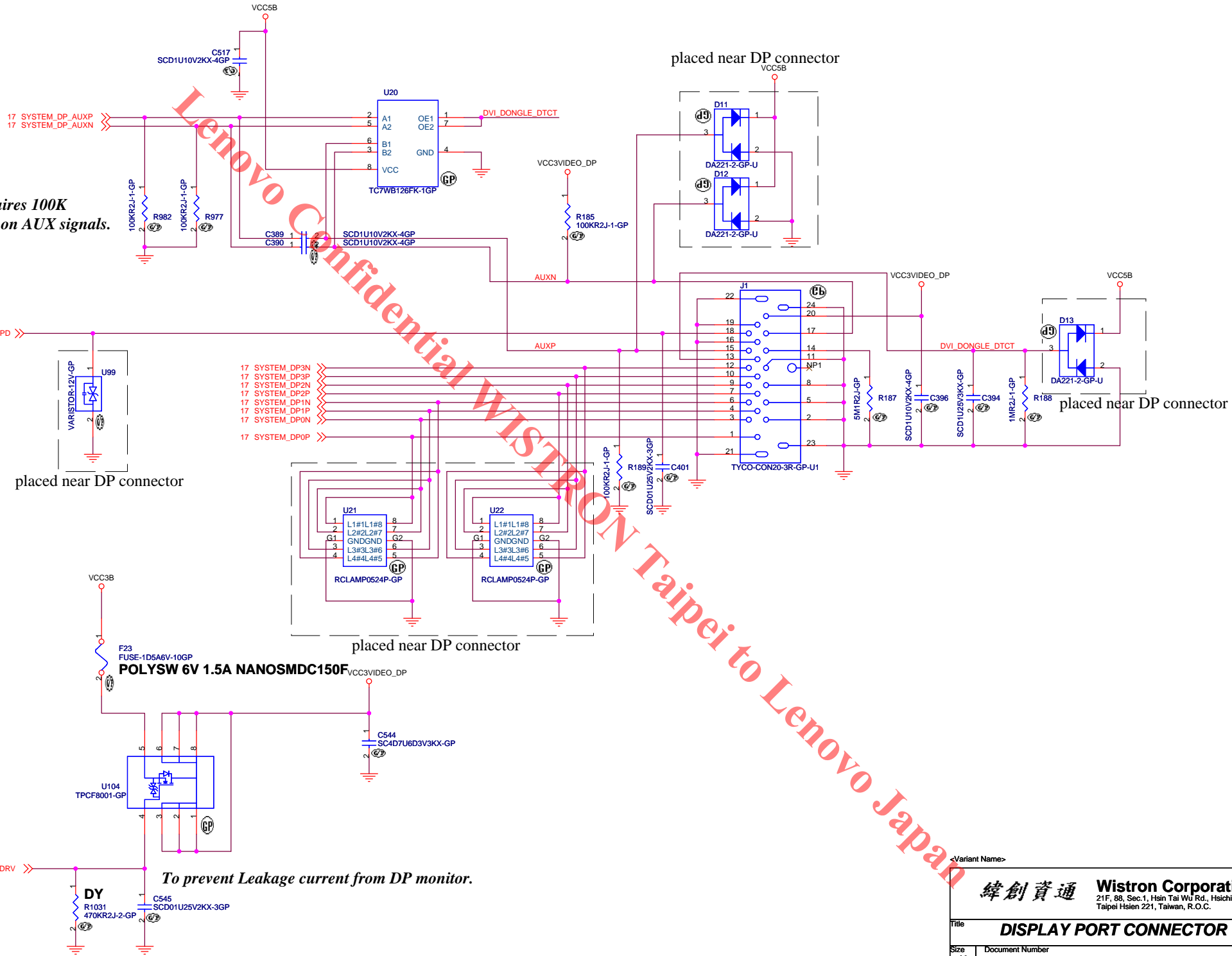
CRT CONN

	Supplier	Vendo P/N	WISTRON P/N
1	TOSHIBA	TC7WT125FU-GP	73.7W125.007
2	TI	SN74LVC2G125DCTR	73.2G125.007

<-Variant Name>

**N10X requires 100K pull-down on AUX signals.**

placed near DP connector



placed near DP connector

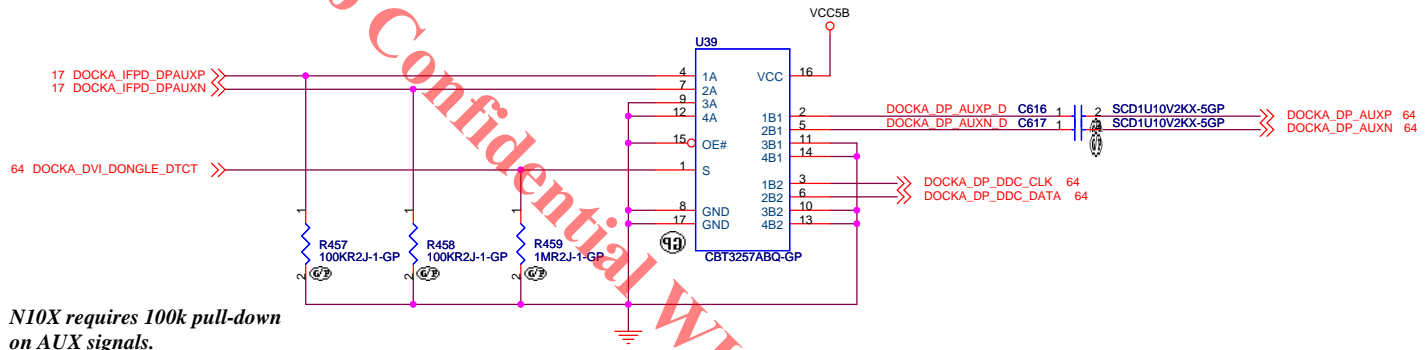
placed near DP connector

placed near DP connector

To prevent Leakage current from DP monitor.

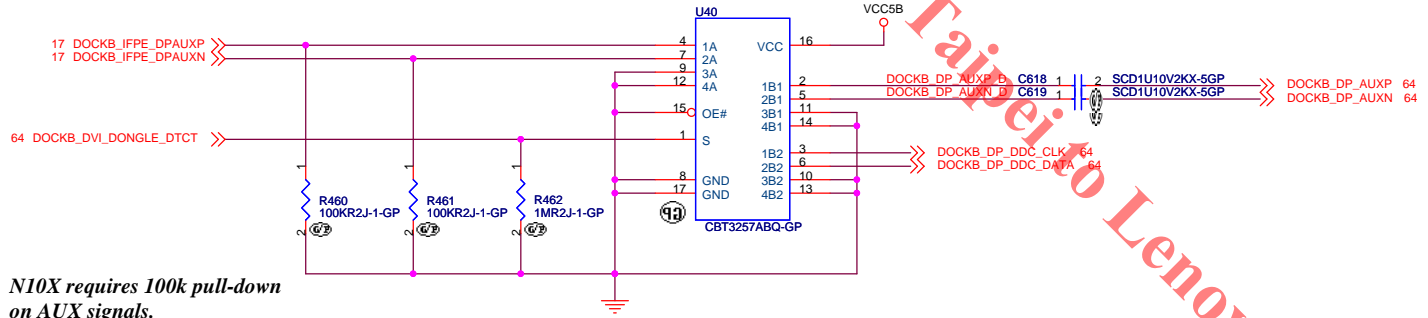
Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>DISPLAY PORT CONNECTOR</b>	
Size A3	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Rev SC
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N10X requires 100k pull-down on AUX signals.

		U27, U39, U40	Wistron part number
1	NXP	CBT3257ABQ-GP	73.03257.A03
2	OnSemi	74FST3257MNTWG-GP	73.5A121.00H



N10X requires 100k pull-down on AUX signals.

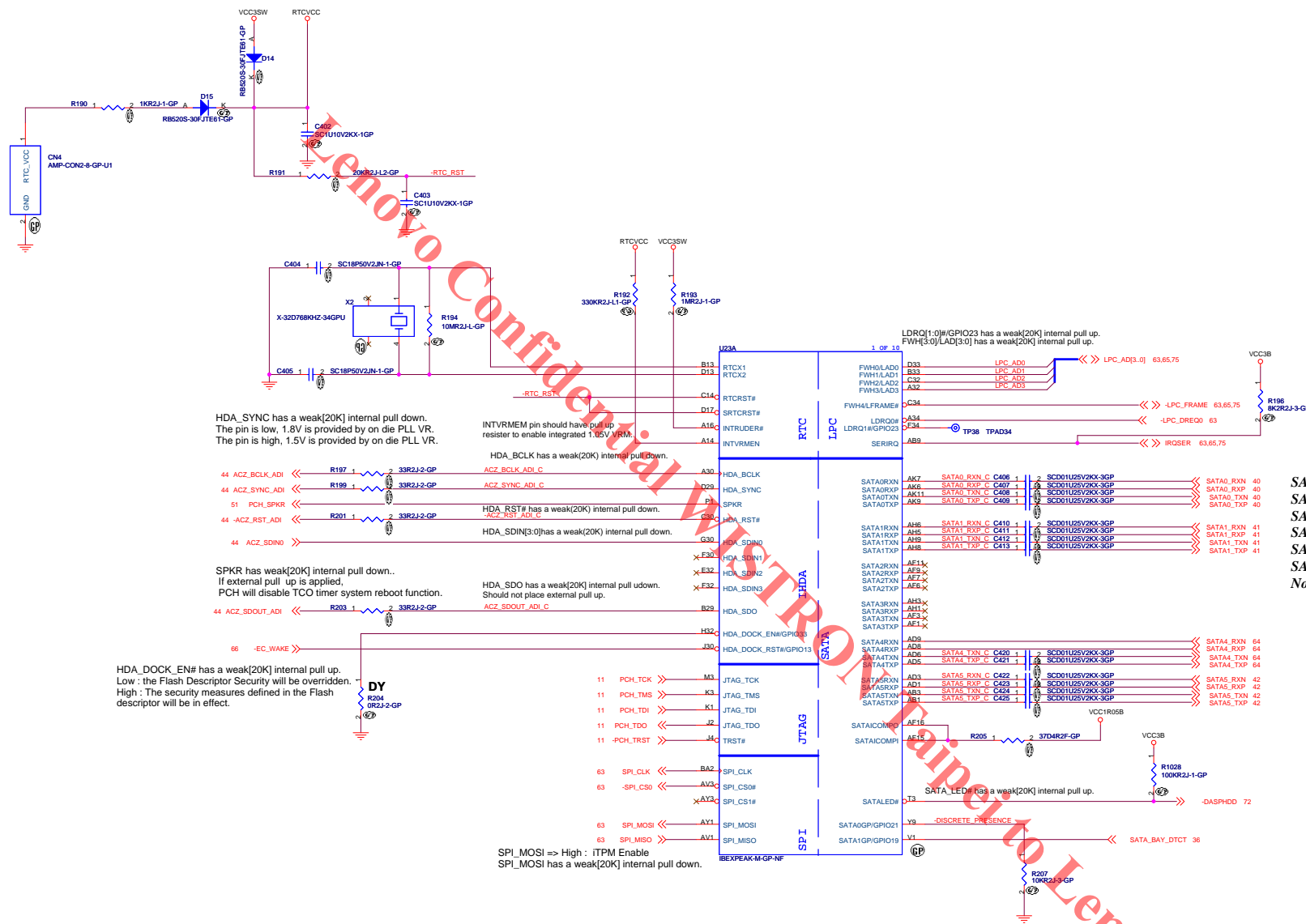
INPUTS	INPUT/OUTPUT		FUNCTION	
	OE#	S		A
L	L		B1	A port = B1 port
L	H		B2	A port = B2 port
H	X		Z	Disconnect

Variant Name: **緯創資通 Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: **DISPLAY PORT MUX**

Size: A3 | Document Number: **Kendo-1 WS** | Rev: **SC**

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HDA\_SYNC has a weak[20K] internal pull down.  
The pin is low, 1.8V is provided by on die PLL VR.  
The pin is high, 1.5V is provided by on die PLL VR.

INTVREM pin should have pull up resistor to enable integrated 1.05V VRM.

HDA\_BCLK has a weak(20K) internal pull down.

HDA\_RST# has a weak(20K) internal pull down.

HDA\_SDIN[3:0] has a weak(20K) internal pull down.

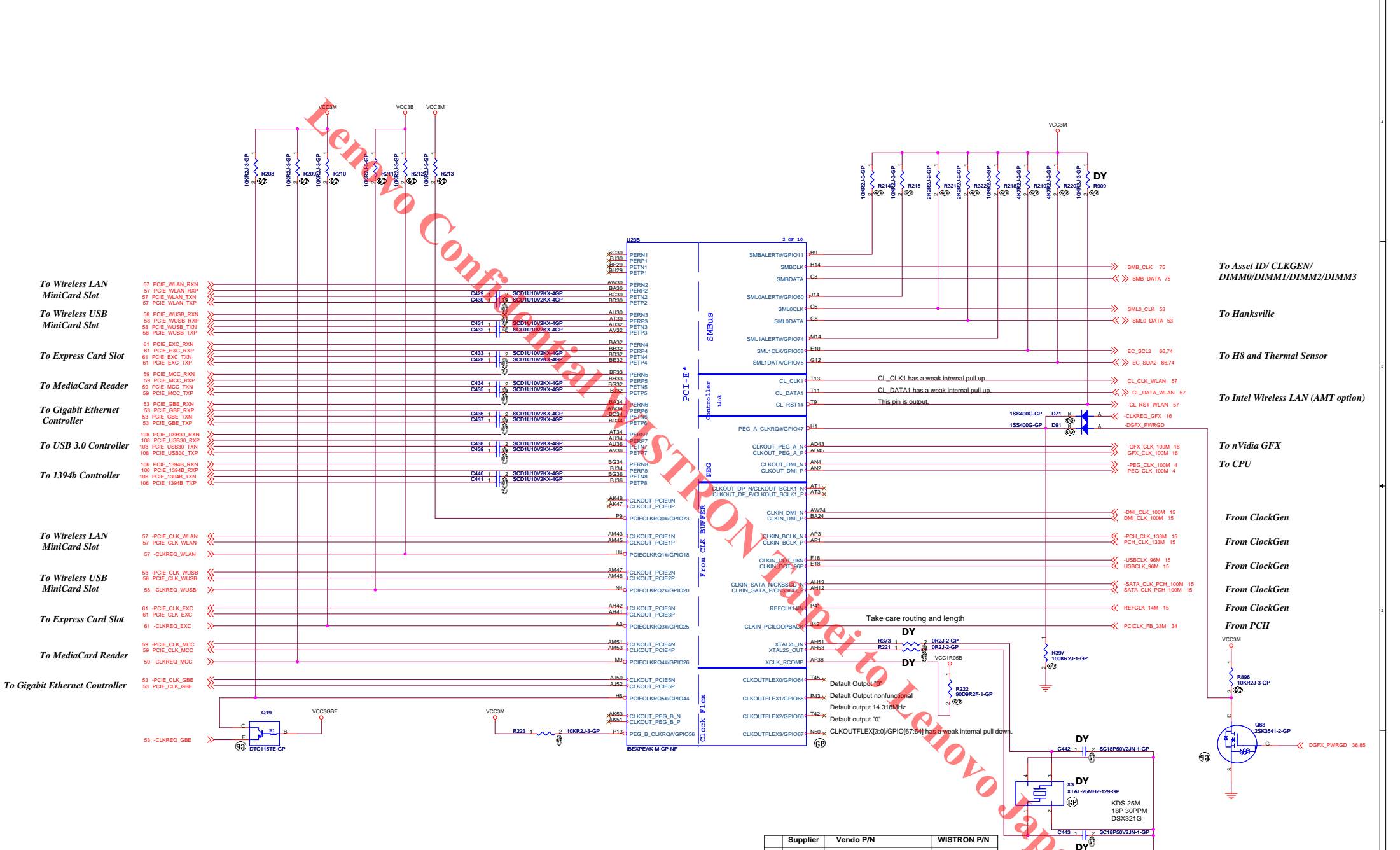
HDA\_SDO has a weak[20K] internal pull down.  
Should not place external pull up.

SPKR has weak[20K] internal pull down..  
If external pull up is applied,  
PCH will disable TCO timer system reboot function.

HDA\_DOCK\_EN# has a weak[20K] internal pull up.  
Low : The Flash Descriptor Security will be overridden.  
High : The security measures defined in the Flash descriptor will be in effect.

SPI\_MOSI => High : ITPM Enable  
SPI\_MOSI has a weak[20K] internal pull down.

SATA0: To HDD Bay  
SATA1: To ODD Bay  
SATA2: Reserved  
SATA3: Reserved  
SATA4: To Docking Connector  
SATA5: To eSATA Combo Connector  
Note: SATA4 and SATA5 support Multiplier



- To Wireless LAN MiniCard Slot
- To Wireless USB MiniCard Slot
- To Express Card Slot
- To MediaCard Reader
- To Gigabit Ethernet Controller
- To USB 3.0 Controller
- To 1394b Controller
- To Wireless LAN MiniCard Slot
- To Wireless USB MiniCard Slot
- To Express Card Slot
- To MediaCard Reader
- To Gigabit Ethernet Controller

- To Asset ID/CLKGEN/DIMM0/DIMM1/DIMM2/DIMM3
- To Hanksville
- To H8 and Thermal Sensor
- To Intel Wireless LAN (AMT option)
- To nVidia GFX
- To CPU
- From ClockGen
- From ClockGen
- From ClockGen
- From ClockGen
- From ClockGen
- From PCH

Supplier	Vendo P/N	WISTRON P/N
1	KDS DSX321G 25M 18P 30PPM	82.30020.B11
2	H.ELE HSX321S 25M 18P 30PPM	82.30020.B21

KDS Recommended Conditions: Normal Frequency: 25MHz, Frequency Tolerance: +/- 30ppm, Load Frequency: 18pF, Effective Series Resistance: 50-ohm, Effective Shunt Capacitance: 2pF.

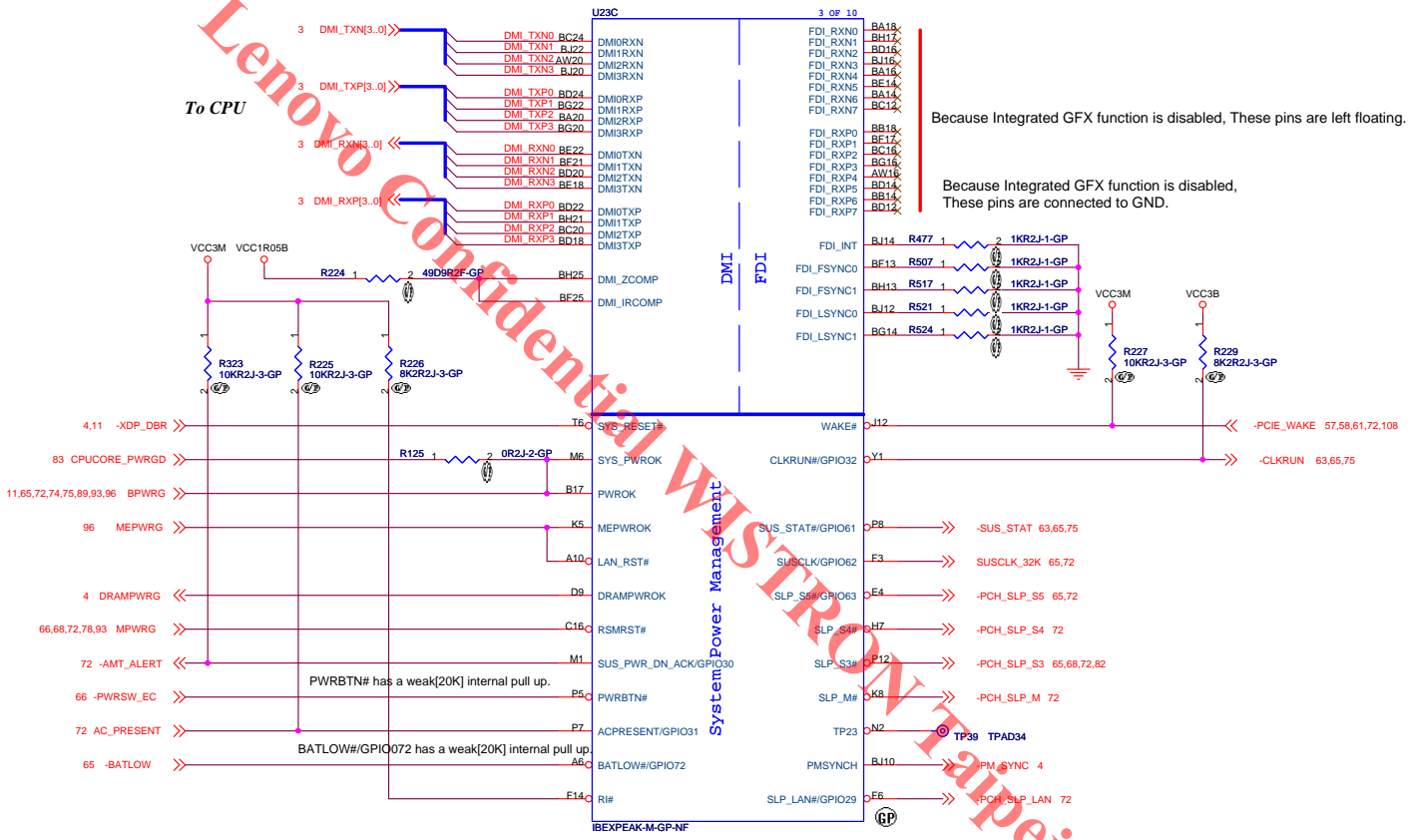
HELE Recommended Conditions: Normal Frequency: 25MHz, Frequency Tolerance: +/- 30ppm, Load Frequency: 18pF, Effective Series Resistance: 50-ohm, Effective Shunt Capacitance: 2pF.

Intel Recommended Conditions: Normal Frequency: 25MHz, Frequency Tolerance: +/- 30ppm, Load Frequency: 18pF, Effective Series Resistance: 50-ohm, Effective Shunt Capacitance: 6pF.

Wistron Corporation  
21F, 8th, Sec.1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsin 221, Taiwan, R.O.C.

File: PCH(2/8):PCIE/SMBUS/CLK  
Size: A4  
Document Number: Kendo-1 WS  
Date: Tuesday, May 05, 2009  
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Because Integrated GFX function is disabled, These pins are left floating.

Because Integrated GFX function is disabled, These pins are connected to GND.

<Variant Name>

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichia, Taipei Hsien 221, Taiwan, R.O.C.	
<b>Title</b> <b>PCH (3/8):DMI/FDI/PM</b>	
<b>Size</b> Custom	<b>Document Number</b> <b>Kendo-1 WS</b>
<b>Date:</b> Tuesday, May 05, 2009	<b>Rev</b> <b>SC</b>
<b>Sheet</b> 32	<b>of</b> 109



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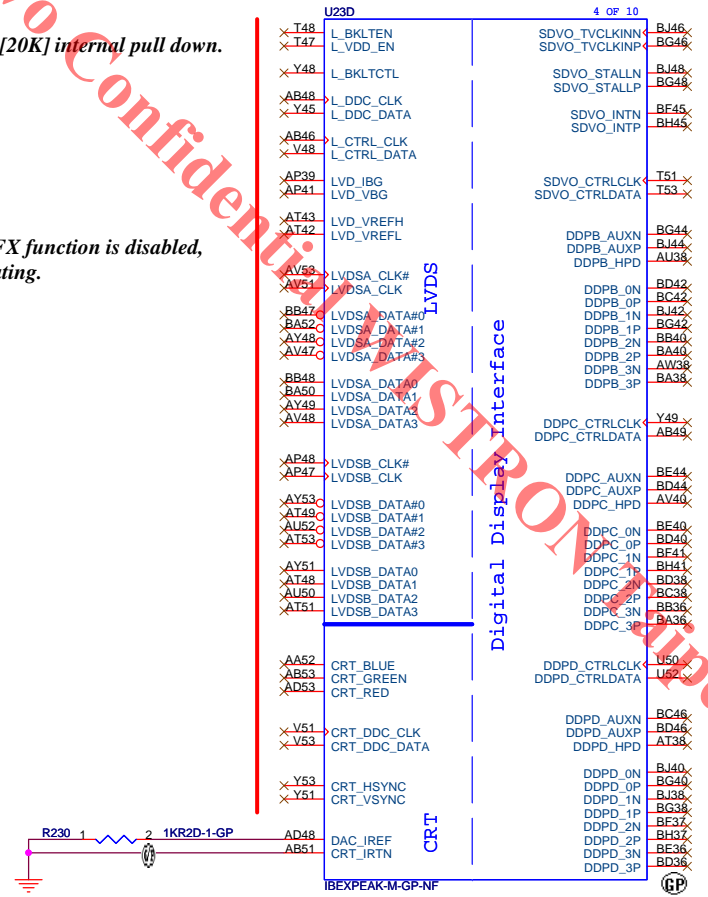
*L\_DDC\_DATA has a weak[20K] internal pull down.*

*Because Integrated GFX function is disabled,  
These pins are left floating.*

*SDVO\_CTRLDATA/SDVO\_CTRLCLK has a weak[20K] internal pull down.*

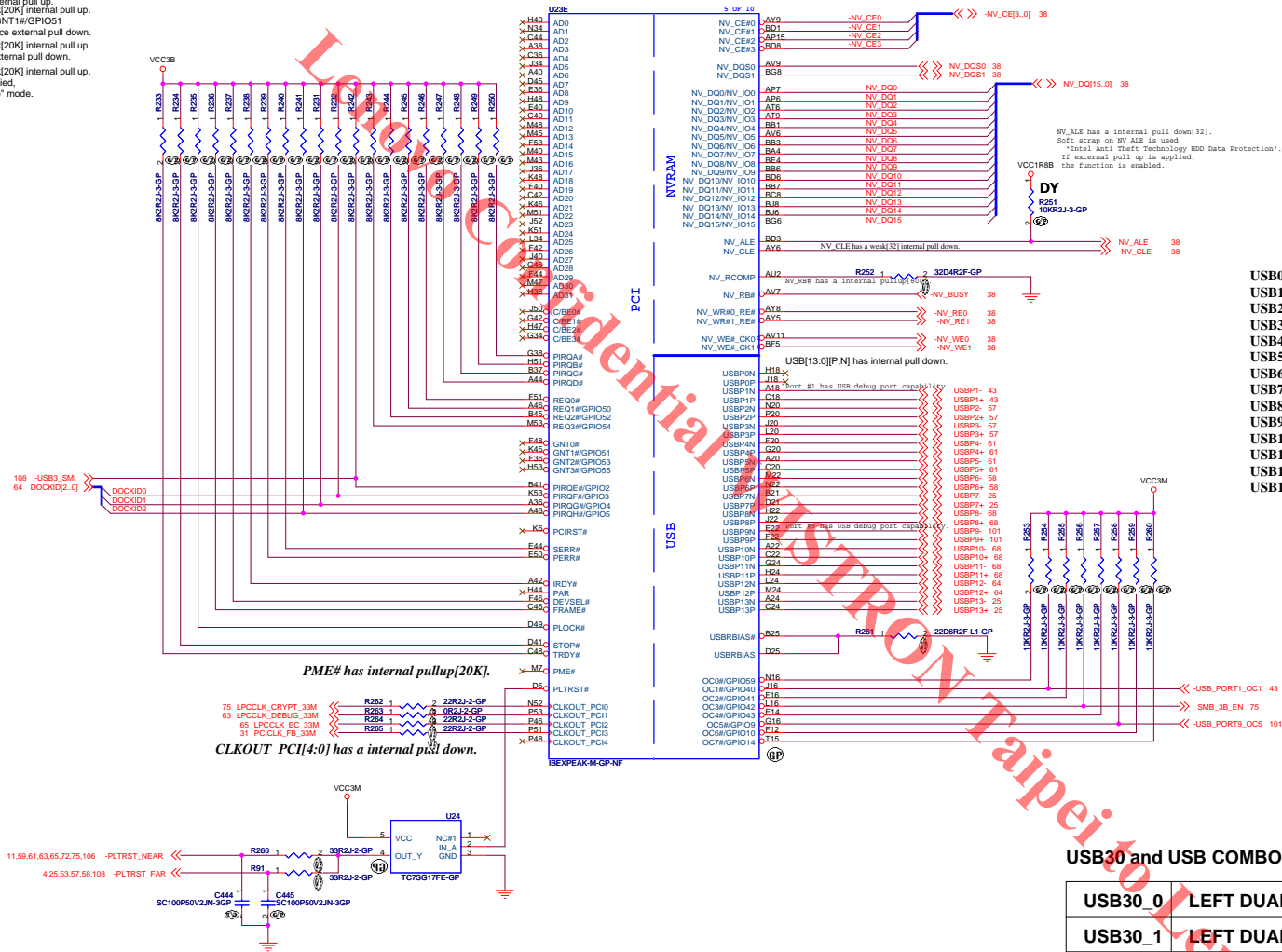
*Because Integrated GFX function is disabled,  
These pins are left floating.*

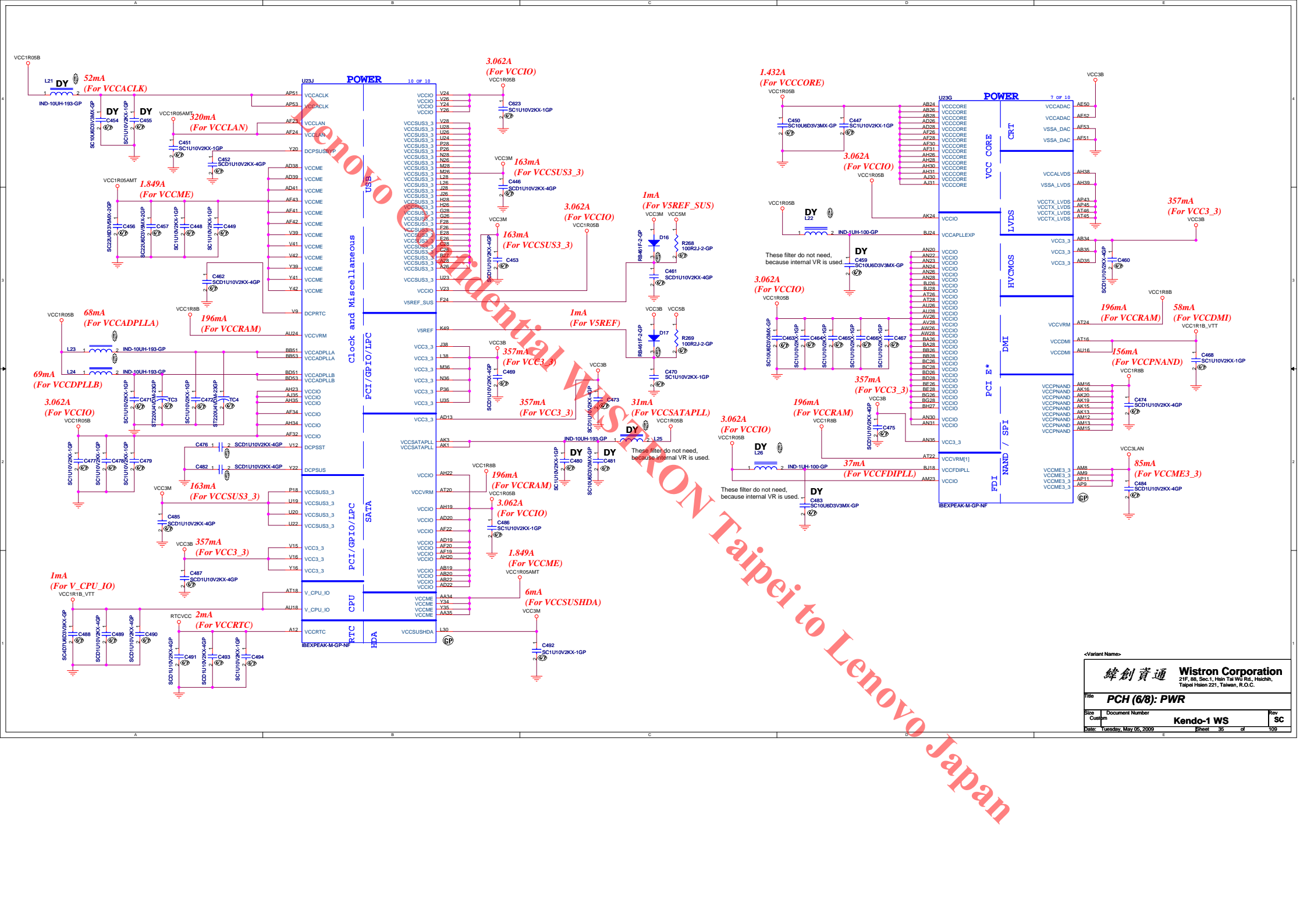
*DDP[D:C]\_CTRLCLK has a weak[10K] internal pull down.  
DDP[D:C]\_CTRLDATA has a weak[20K] internal pull down.*



Because PCI IF is not used, ADx.C/BEx and GNTx are left as NC.

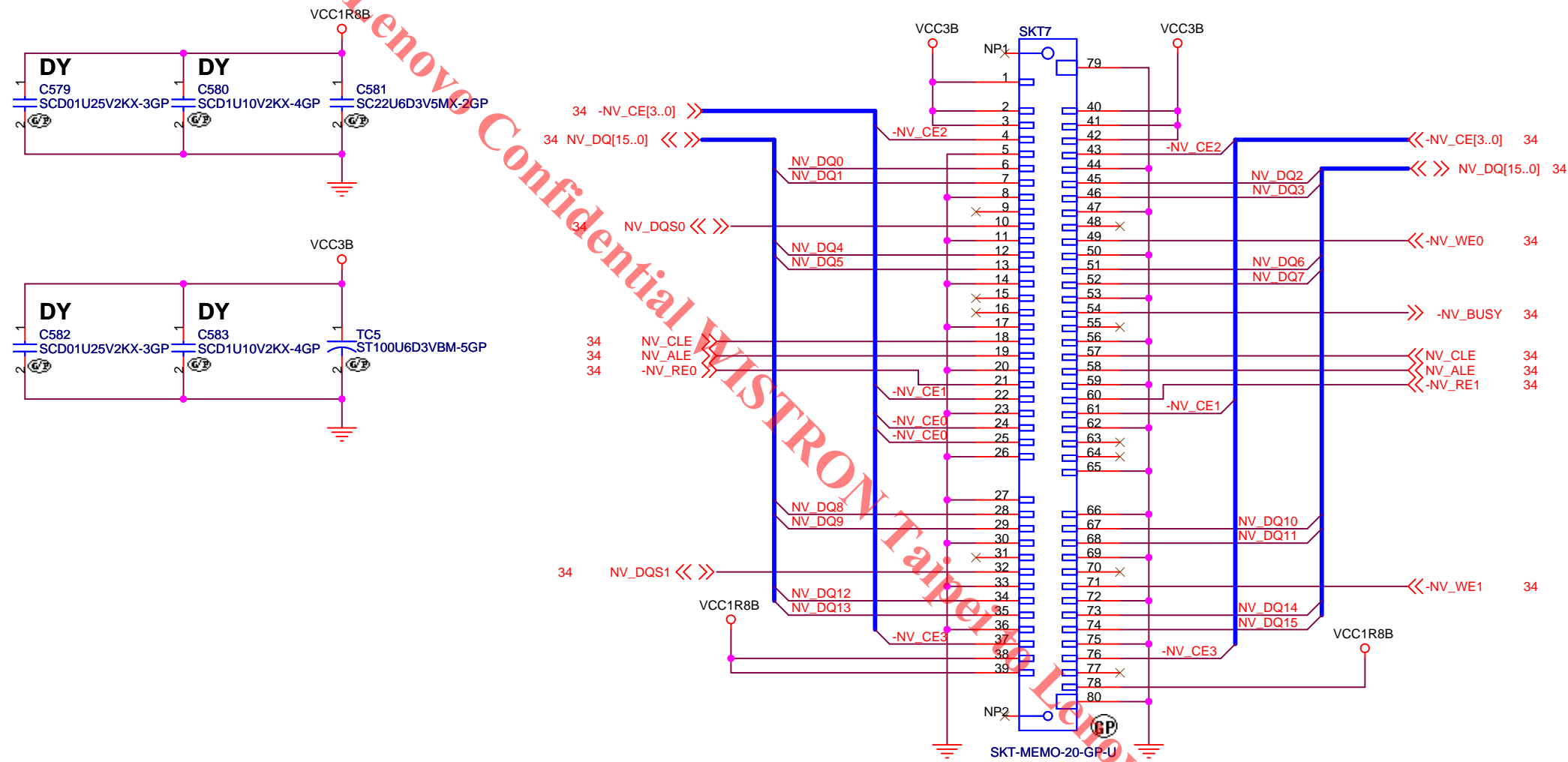
GNT0# has a weak[20K] internal pull up.  
 GNT1#/GPIO51 has a weak[20K] internal pull up.  
 To use SPI IF flash BIOS, GNT1#/GPIO51 and GNT0# should not place external pull down.  
 GNT2#/GPIO53 has a weak[20K] internal pull up.  
 This pin should not have external pull down.  
 GNT3#/GPIO55 has a weak[20K] internal pull up.  
 If external pull down is applied, PCH will be "topblock swap" mode.












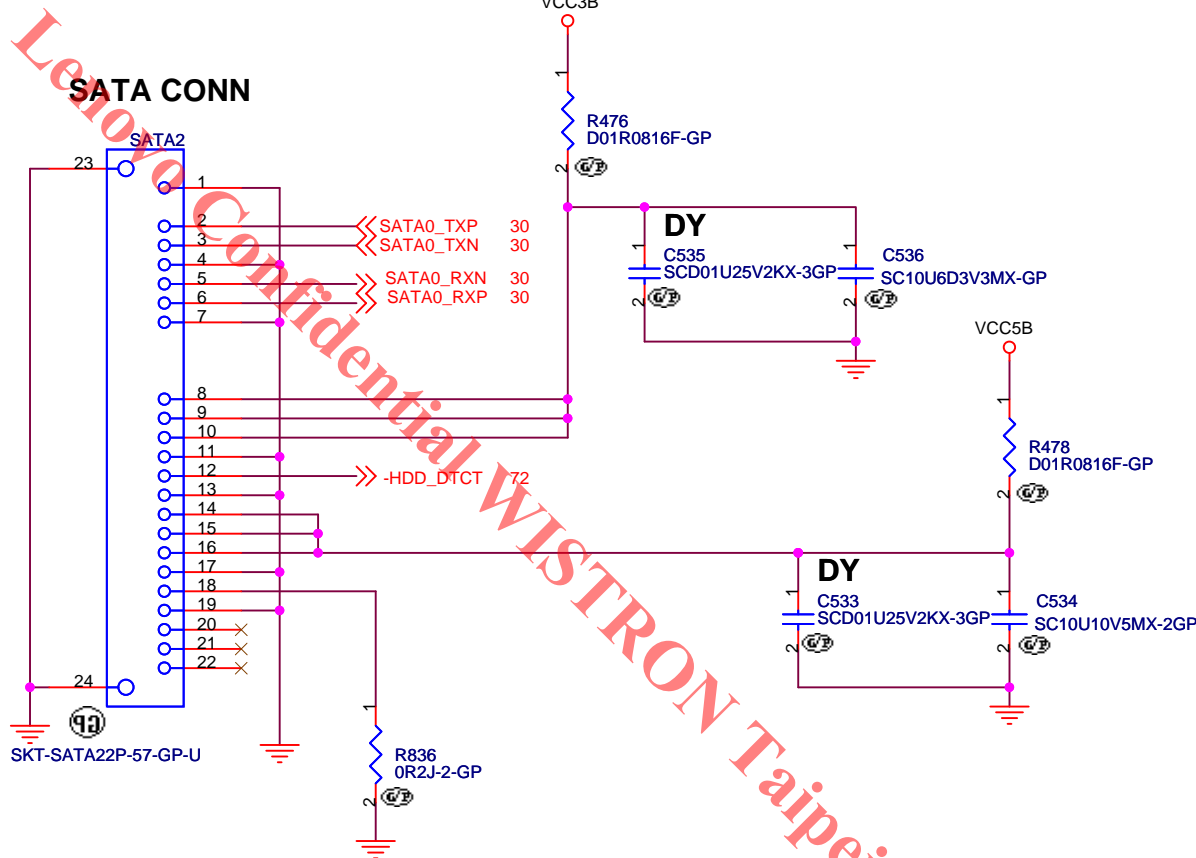
<Variant Name>

 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
<b>TURBO MEMORY SLOT</b>		
Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date Tuesday, May 05, 2009	Sheet 38	of 109


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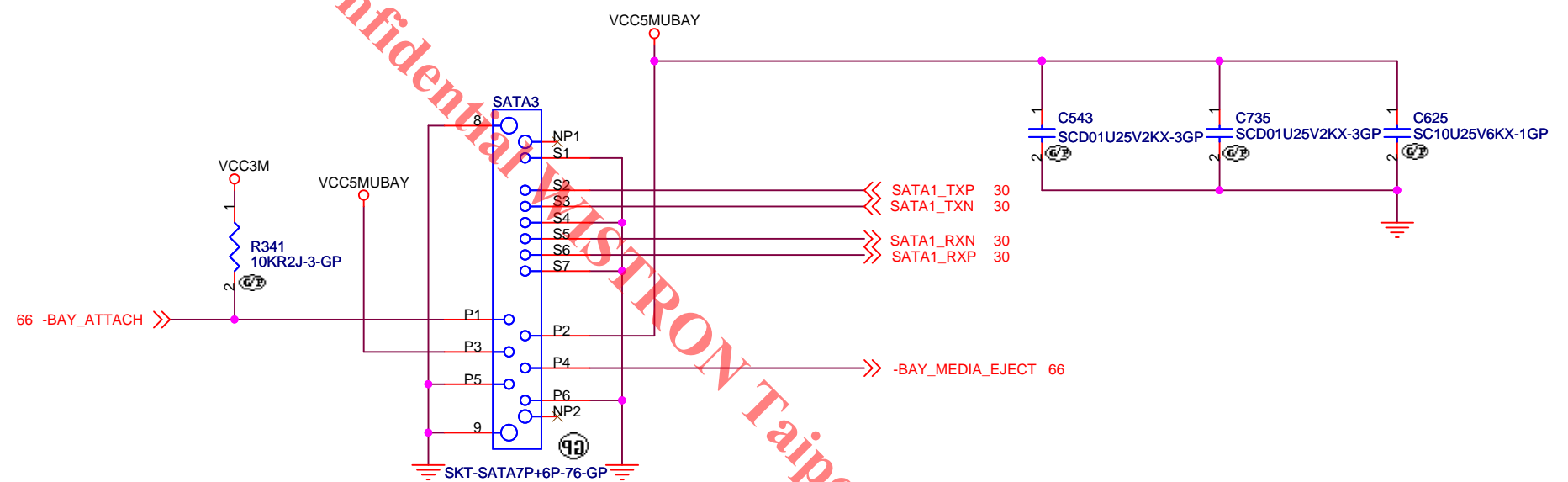
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Size	Document Number	Rev	
A3	<b>Kendo-1 WS</b>	SC	
Date:	Tuesday, May 05, 2009	Sheet	39 of 109



<Variant Name>

 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>Title</b> <b>SATA HDD CONN</b>	
<b>Size</b> A4	<b>Document Number</b> <b>Kendo-1 WS</b>
<b>Date</b> Tuesday, May 05, 2009	<b>Rev</b> <b>SC</b>
<b>Sheet 40 of 109</b>	





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<Variant Name>

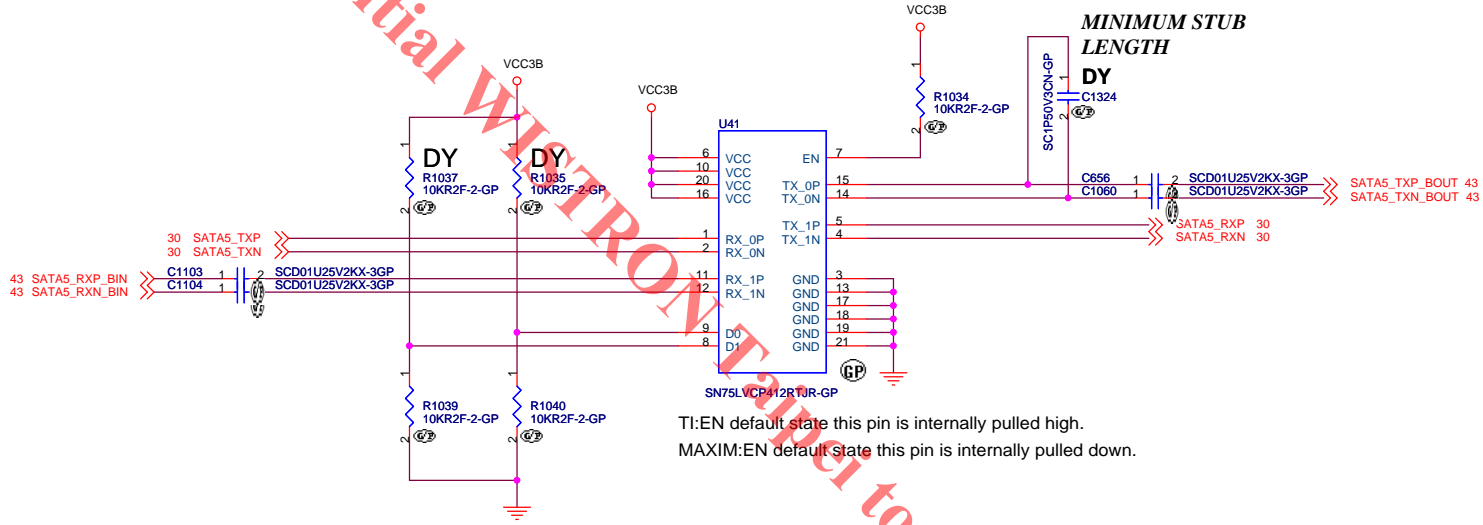
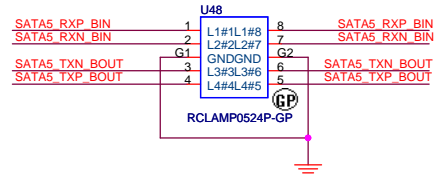
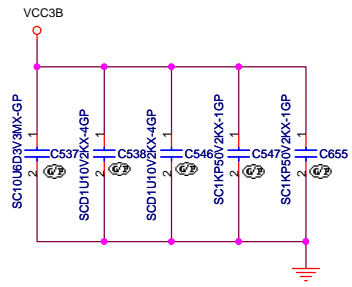
**Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

Title **SATA BAY I/F CONN**

Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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TABLE

EN	D0	D1	CH - 0	CH - 1
0	X	X	STANDBY	STANDBY
1	0	0	STANDARD	STANDARD
1	1	0	BOOST	STANDARD
1	0	1	STANDARD	BOOST
1	1	1	BOOST	BOOST

← LOGIC

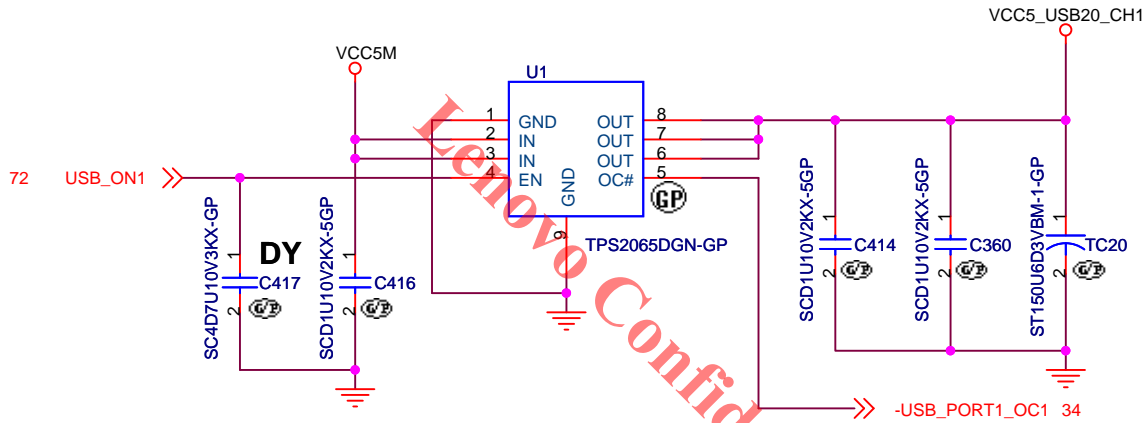
		U41	Wistron part number
1	TI	SN75LVCP412RTJR-GP	71.75412.003
2	MAXIM	MAX4951CTP-T-GP	71.04951.A03

<Core Design>

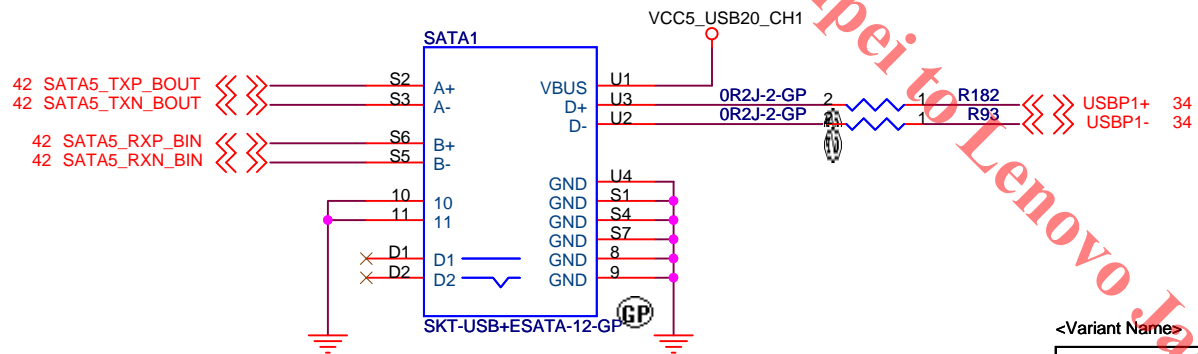
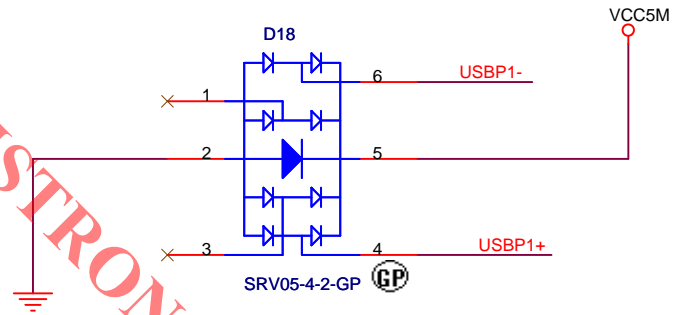
**Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

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**Title** ESATA CONNECTOR  
**Size** A3  
**Document Number** Kendo-1 WS  
**Date** Tuesday, May 05, 2009  
**Sheet** 42 of 109  
**Rev** SC



	Supplier	Vendo P/N	WISTRON P/N
1	TI	TPS2065DGN-GP	74.02065.079
2	TI	TPS2065DGN-1-GP	74.02065.A79
3	ROHM	BD8014FVJ-GP	74.08014.07G



### eSATA Combo

<Variant Name>

**緯創資通** **Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

Title **USB POWER/ CONN**

Size A4 Document Number **Kendo-1 WS** Rev **SC**

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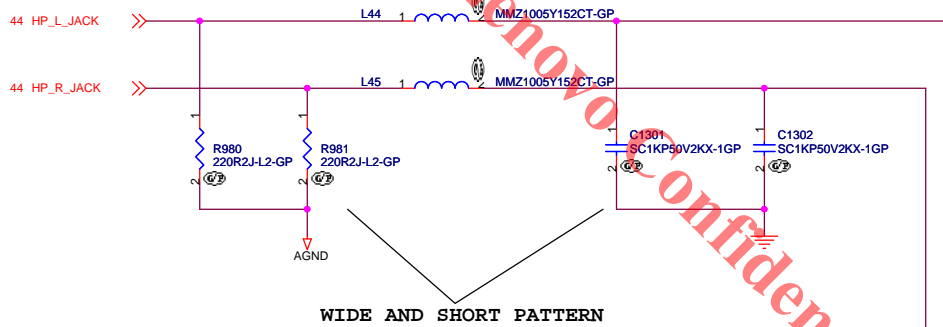


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-<Variant Name->		
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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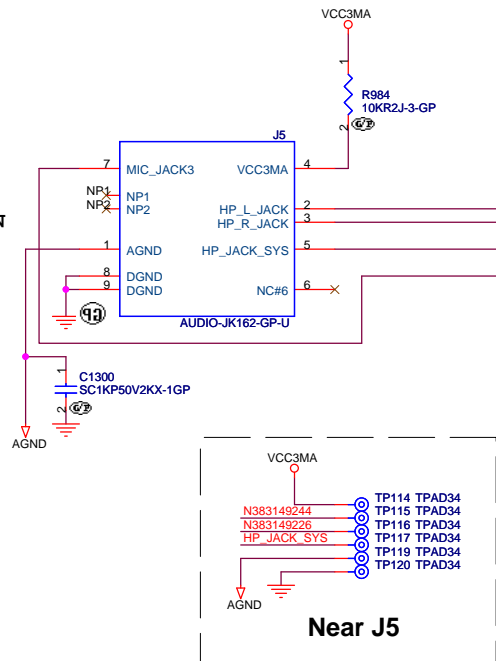
NEAR HEADPHONE CONN



WIDE AND SHORT PATTERN

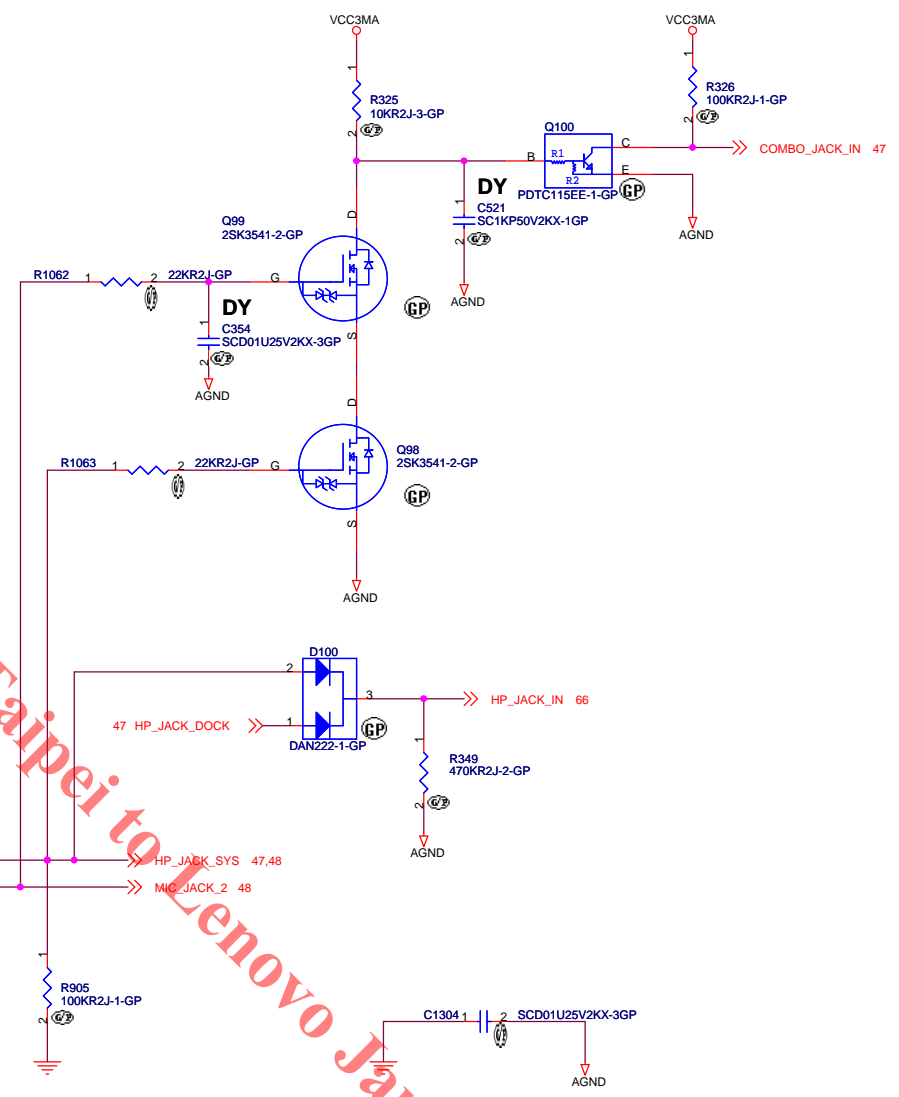
ESD Protection diodes will be change to other parts from SIV phase if it failed.

WIDE PATTERN



Near J5

	Supplier	Vendo P/N	WISTRON P/N
1	OnSemi	ESD5B5D0ST1G-GP	83.ESD5B.0AF
2	ROHM	RSB5.6S	83.RSB56.0AF



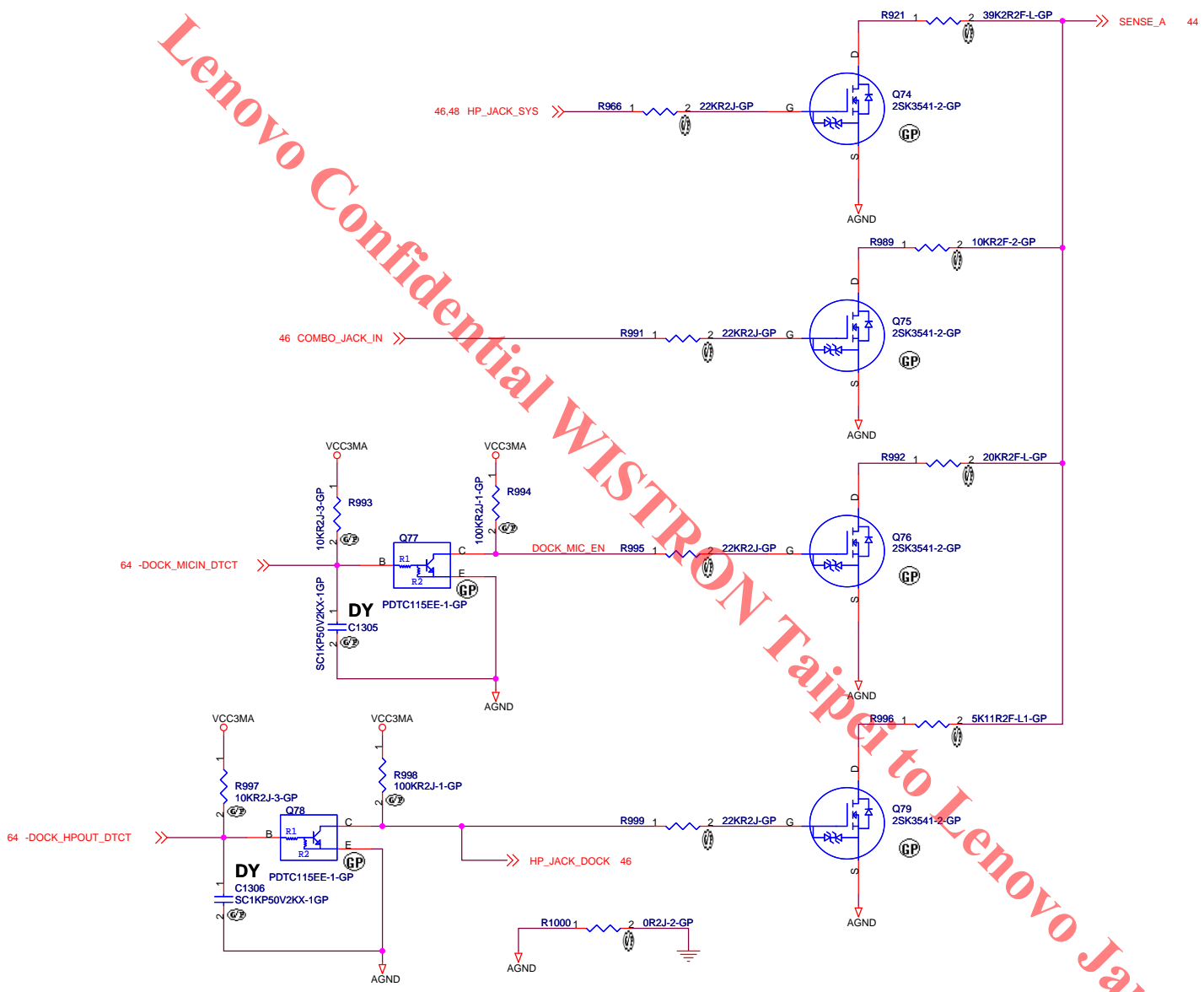
<Core Design>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title **AUDIO CONNECTOR**

Size A3 Document Number **Kendo-1 WS** Rev **SC**  
Date: Tuesday, May 05, 2009 Sheet 46 of 109

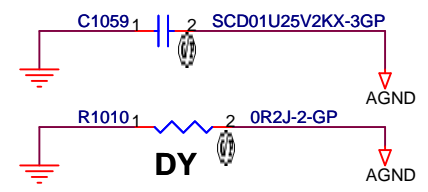
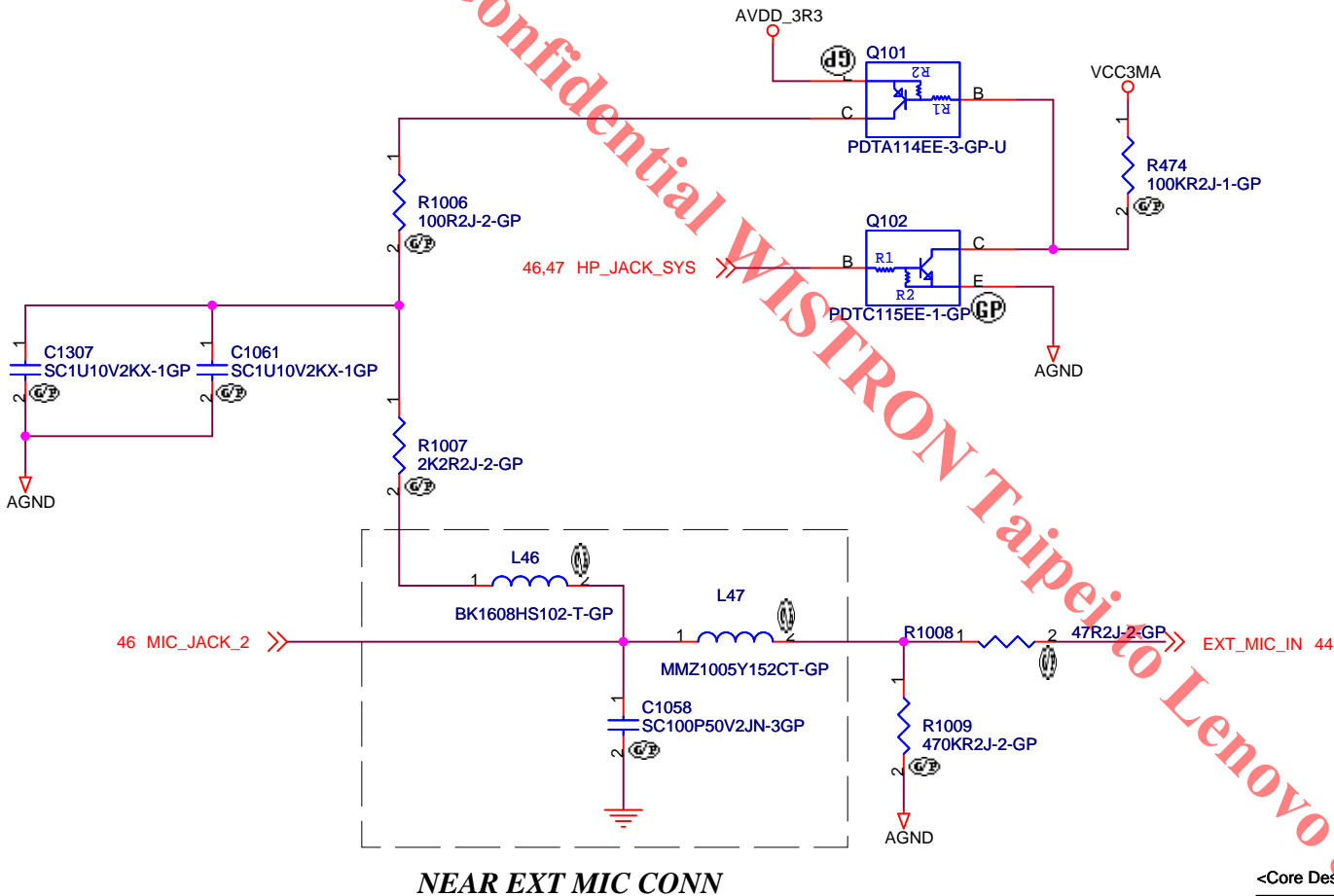
Lenovo Confidential WISTRON Taipei to Lenovo Japan



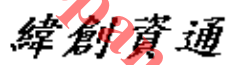
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<b>緯創資通</b>		<b>Wistron Corporation</b>	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>AUDIO JACK SENSE</b>			
Title			
Size	Document Number	Rev	SC
A3	<b>Kendo-1 WS</b>		
Date:	Tuesday, May 05, 2009	Sheet	47 of 109

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<Core Design>

 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>AUDIO EXT MIC I/F</b>	
Size A4	Document Number <b>Kendo-1 WS</b>
Date Tuesday, May 05, 2009	Rev <b>SC</b>
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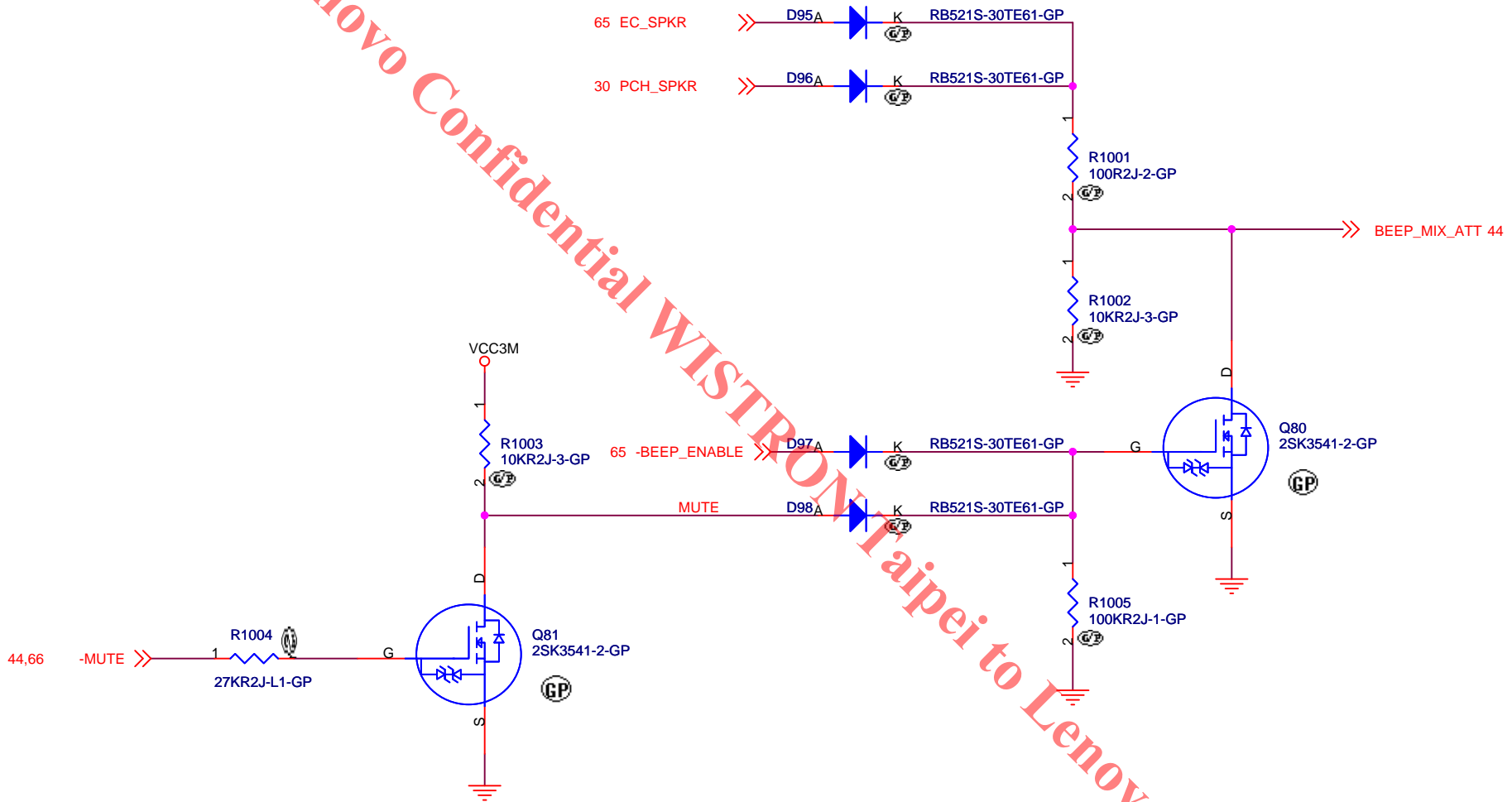
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Title		
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Size	Document Number	Rev
A3	<b>Kendo-1 WS</b>	<b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 49 of 109

Lenovo Confidential  
WISTRON Taipei to Lenovo Japan

**BLANK**

<Variant Name>		
<b>緯創資通</b>		<b>Wistron Corporation</b> <small>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</small>
<b>Title</b> <b>BLANK</b>		
<b>Size</b> A3	<b>Document Number</b> <b>Kendo-1 WS</b>	<b>Rev</b> <b>SC</b>
<b>Date:</b> Tuesday, May 05, 2009		<b>Sheet</b> 50 <b>of</b> 109

Lenovo Confidential WISTRON Taipei to Lenovo Japan



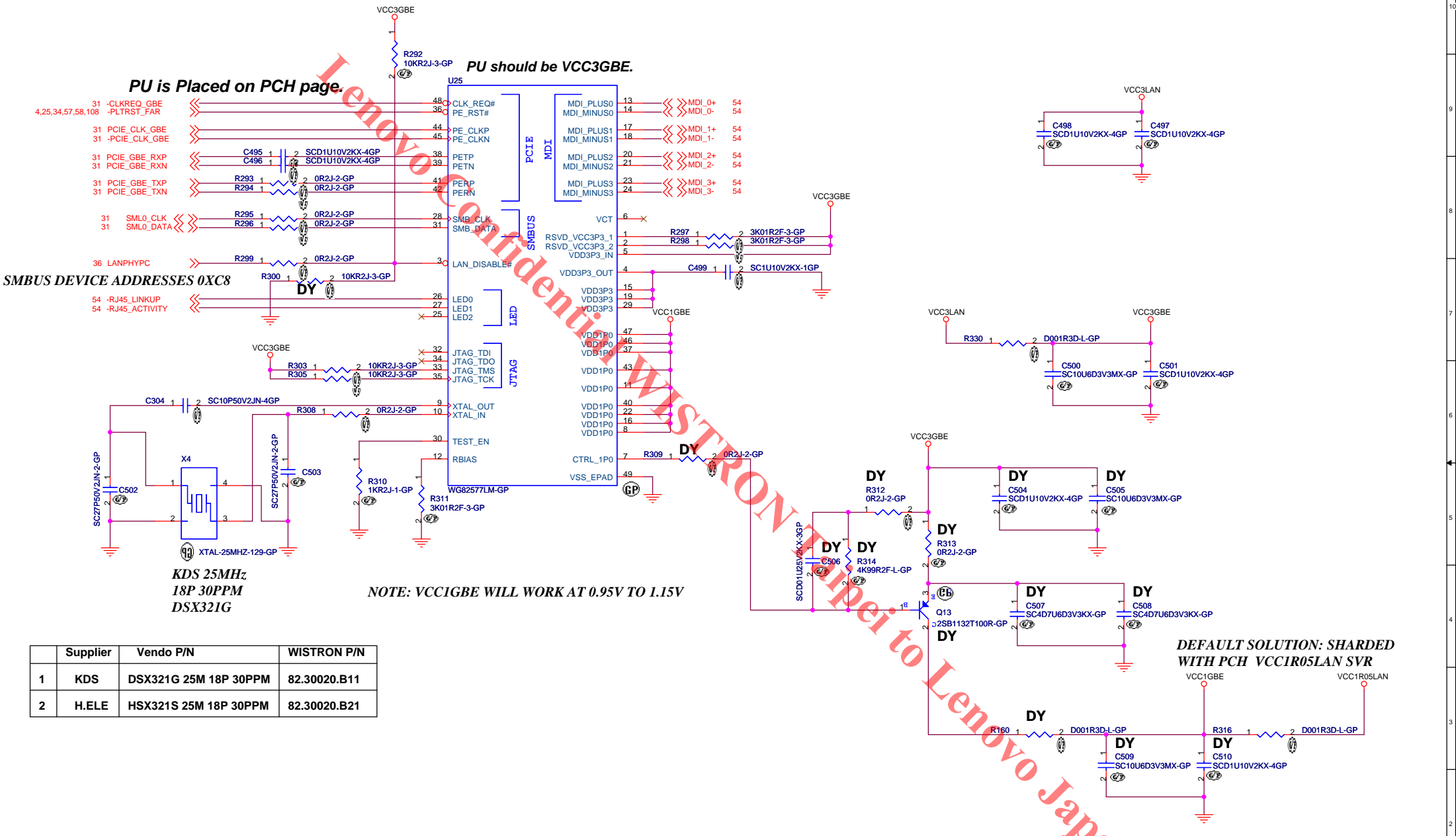
<Core Design>

<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>Title</b> <b>BEEP CONTROL</b>	
Size A4	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Rev <b>SC</b>
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Lenovo Confidential WISTRON Taipei to Lenovo Japan

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<b>緯創資通</b>		<b>Wistron Corporation</b> <small>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</small>
Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 52 of 109



PU is Placed on PCH page.

PU should be VCC3GBE.

SMBUS DEVICE ADDRESSES 0XC8

KDS 25MHz  
18P 30PPM  
DSX321G

NOTE: VCC1GBE WILL WORK AT 0.95V TO 1.15V

Supplier	Vendo P/N	WISTRON P/N
1	KDS DSX321G 25M 18P 30PPM	82.30020.B11
2	H.ELE HSX321S 25M 18P 30PPM	82.30020.B21

KDS Recommended Conditions: Normal Frequency: 25MHz. Frequency Tolerance: +/- 30ppm. Load Frequency: 18pF. Effective Series Resistance: 50-ohm. Effective Shunt Capacitance: 2pF.	HELE Recommended Conditions: Normal Frequency: 25MHz. Frequency Tolerance: +/- 30ppm. Load Frequency: 18pF. Effective Series Resistance: 50-ohm. Effective Shunt Capacitance: 2pF.	Intel Recommended Conditions: Normal Frequency: 25MHz. Frequency Tolerance: +/- 30ppm. Load Frequency: 18pF. Effective Series Resistance: 50-ohm. Effective Shunt Capacitance: 6pF.
--	---	--

WISTRON Taipei to Lenovo Japan

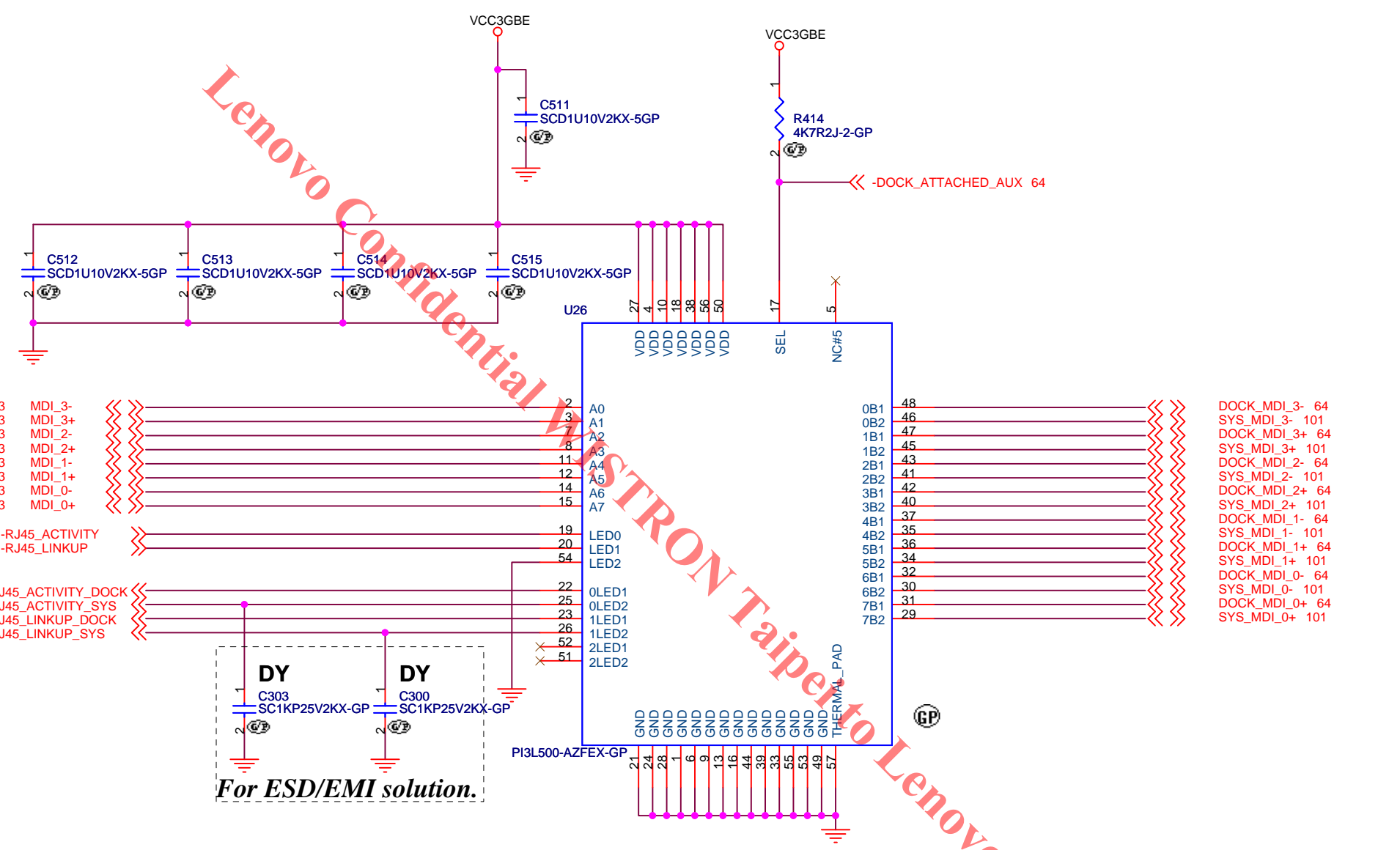
Variant Name:

**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

**GBE HANKSVILLE**

File: Kendo-1 WS Rev SC

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53 MDI\_3-  
53 MDI\_3+  
53 MDI\_2-  
53 MDI\_2+  
53 MDI\_1-  
53 MDI\_1+  
53 MDI\_0-  
53 MDI\_0+

53 -RJ45\_ACTIVITY  
53 -RJ45\_LINKUP

64 -RJ45\_ACTIVITY\_DOCK  
101 -RJ45\_ACTIVITY\_SYS  
64 -RJ45\_LINKUP\_DOCK  
101 -RJ45\_LINKUP\_SYS

DOCK\_MDI\_3- 64  
SYS\_MDI\_3+ 101  
DOCK\_MDI\_3+ 64  
SYS\_MDI\_3+ 101  
DOCK\_MDI\_2- 64  
SYS\_MDI\_2- 101  
DOCK\_MDI\_2+ 64  
SYS\_MDI\_2+ 101  
DOCK\_MDI\_1- 64  
SYS\_MDI\_1- 101  
DOCK\_MDI\_1+ 64  
SYS\_MDI\_1+ 101  
DOCK\_MDI\_0- 64  
SYS\_MDI\_0- 101  
DOCK\_MDI\_0+ 64  
SYS\_MDI\_0+ 101

**DY**  
 C303  
 SC1KP25V2KX-GP  
**DY**  
 C300  
 SC1KP25V2KX-GP  
**For ESD/EMI solution.**

[Source Cadidate]				
1st	Pericom	PI3L500AZFEX	73.3L500.003	41R0539AA
2nd	TI	TS3L500AERHUR	73.3L500.A0V	41R0539BA

<Variant Name>

<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>Title</b> <span style="font-size: 1.2em; font-weight: bold;">GBE LAN SW</span>		
Size A4	Document Number <span style="font-size: 1.2em; font-weight: bold;">Kendo-1 WS</span>	Rev <span style="font-size: 1.2em; font-weight: bold;">SC</span>
Date: Tuesday, May 05, 2009		Sheet 54 of 109

Lenovo Confidential WISTRON Taipei to Lenovo Japan

**BLANK**

Title		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		<b>BLANK</b>	
Size	Document Number	Rev	
A3	<b>Kendo-1 WS</b>	SC	
Date:	Tuesday, May 05, 2009	Sheet	55 of 109

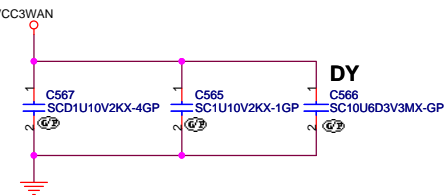
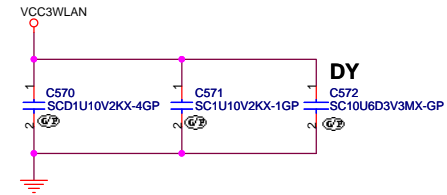
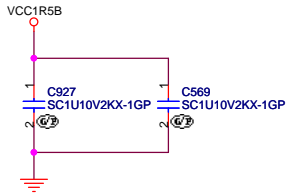
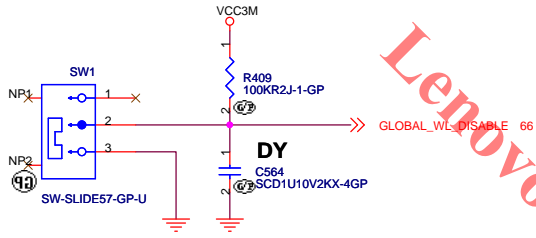
Lenovo Confidential WISTRON Taipei to Lenovo Japan

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<Variant Name>		
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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**WIRELESS DISABLE SWITCH**



Always ON	YES	NO
U33	ASM	No_ASM
R410	No_ASM	ASM
R411	No_ASM	ASM

↑  
LOGIC

USB_SEL_ALWAYS_ON	USB_WWAN
L	USBP3
H	USBAO

*On WWAN Always on regular mode,  
This component have to be assembled,  
instead of above D+/D- jumper.*

34 USBP3-  
34 USBP3+

61 USBAO-  
61 USBAO+

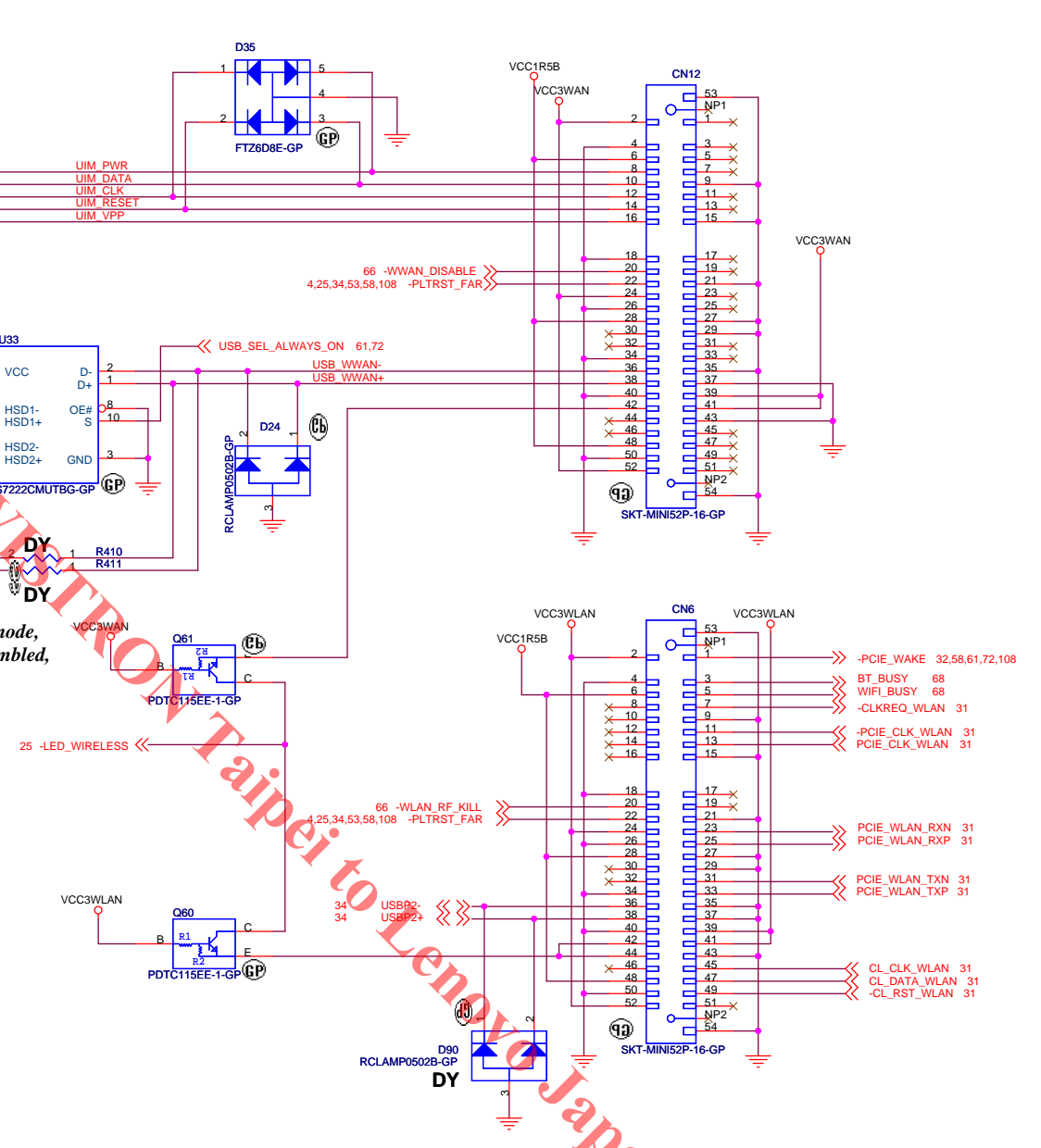
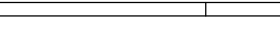
25 -LED\_WIRELESS



66 -WWAN\_DISABLE  
4,25,34,53,58,108 -PLTRST\_FAR



66 -WLAN\_RF\_KILL  
4,25,34,53,58,108 -PLTRST\_FAR



-Variant Name-

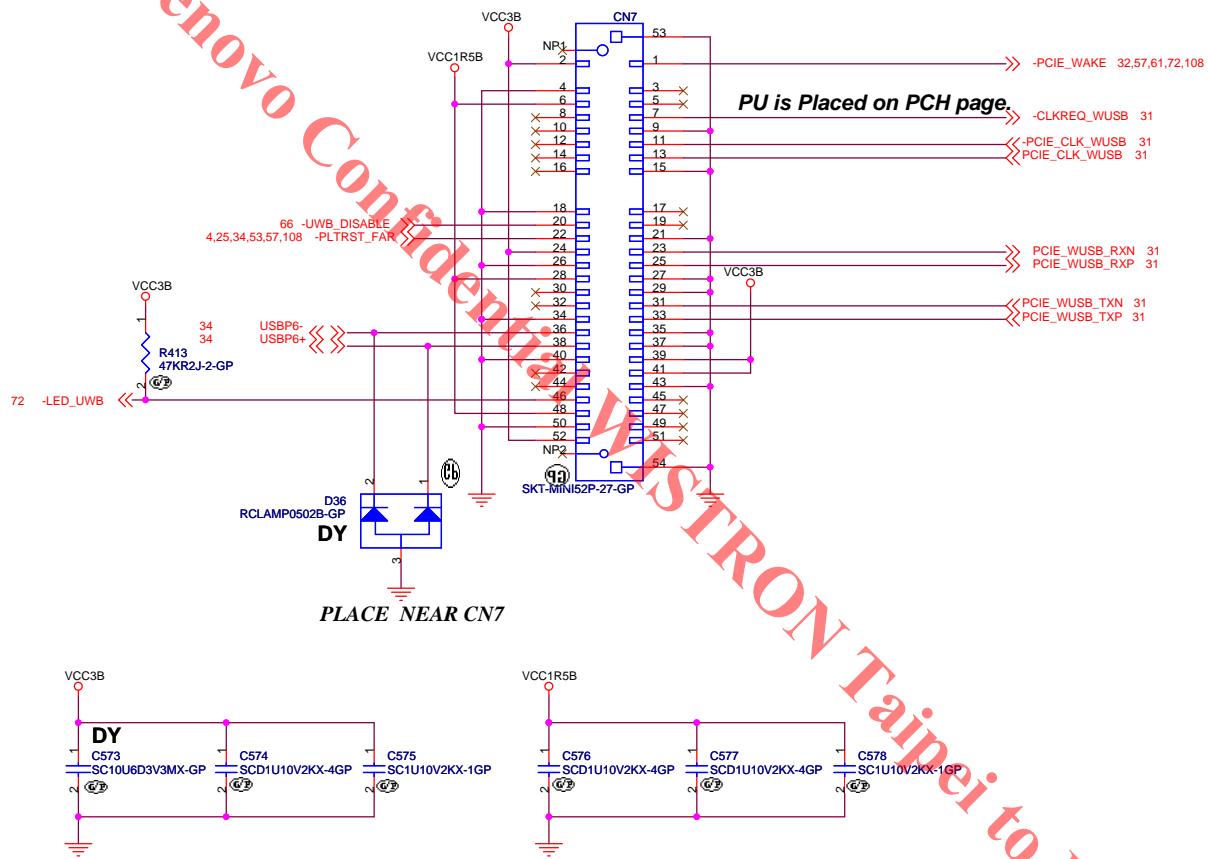
**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

**Title** PCIE MINI CARD SLOT (1/2)

<b>Size</b> A3	<b>Document Number</b> Kendo-1 WS	<b>Rev</b> SC
<b>Date:</b> Tuesday, May 05, 2009		
Sheet 57		of 109

# HALF MINI CARD FOR WUSB

## 7.0H CONNECTOR



<Variant Name>

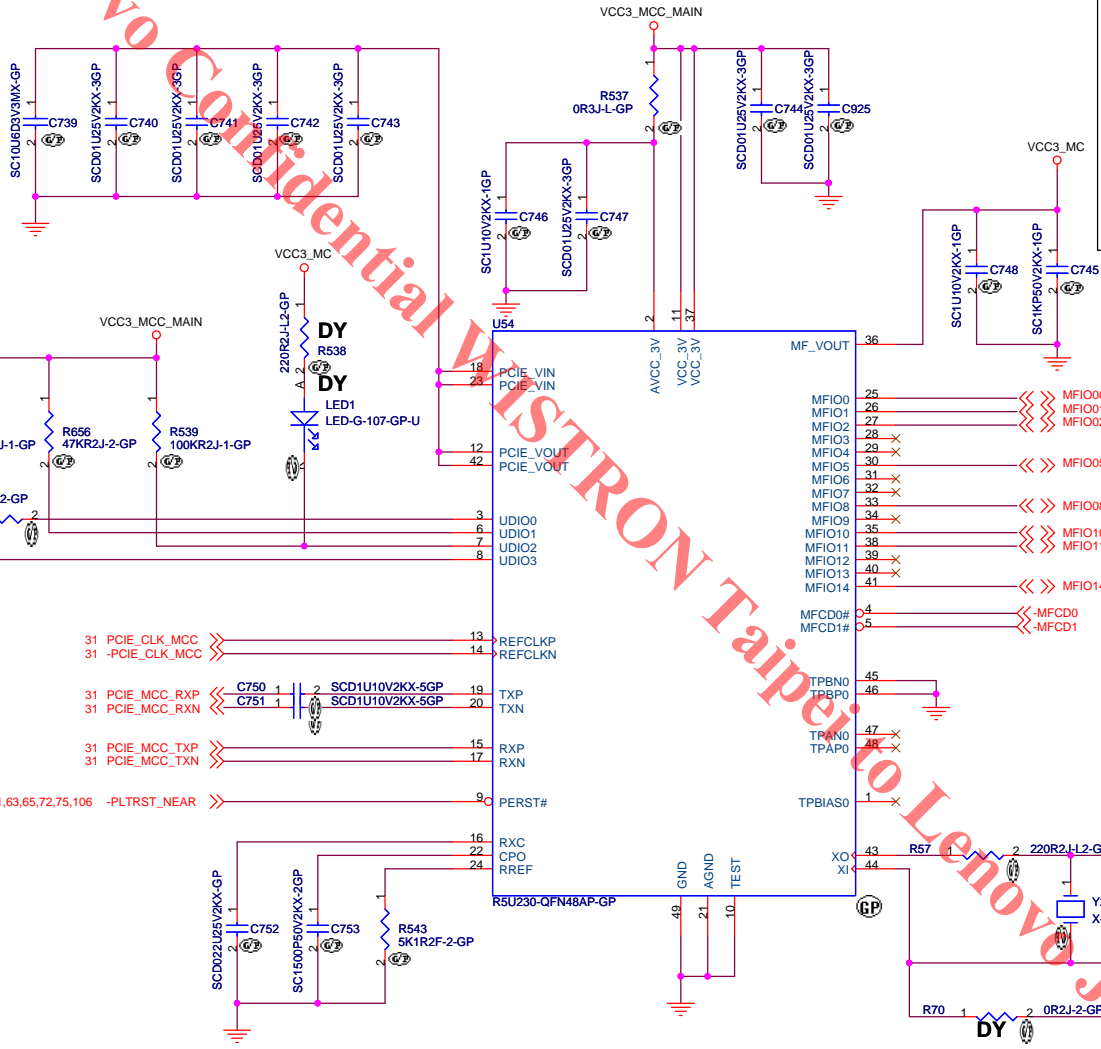
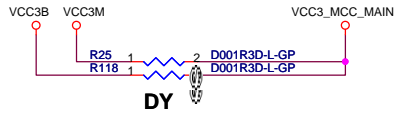
緯創資通 **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title **PCIE MINI CARD SLOT (2/2)**

Size A3 Document Number **Kendo-1 WS** Rev **SC**

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	Vendor P/N	Wistron P/N
LED1	LTST-S270KGKT	83.00270.B70
	SML-A10MTT86	83.00A10.070



MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MS_D0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDCD#		XDCD0#
MFCD1#		MSINS#	XDCD1#

PU is Placed on PCH page.  
31 -CLKREQ\_MCC <<

- 31 PCIE\_CLK\_MCC <<>> 13 REFCLKP
- 31 -PCIE\_CLK\_MCC <<>> 14 REFCLKN
- 31 PCIE\_MCC\_RXP <<>> 19 TXP
- 31 PCIE\_MCC\_RXN <<>> 20 TXN
- 31 PCIE\_MCC\_TXP <<>> 15 RXP
- 31 PCIE\_MCC\_TXN <<>> 17 RXN
- 11,34,61,63,65,72,75,106 -PLTRST\_NEAR <<>> 9 PERST#

**KDS 24.576MHz**  
**12P 50PPM**  
**HCX-6F**

Richo Recommended Conditions:  
Normal Frequency: 24.576MHz  
Frequency Tolerance: +/- 50ppm.  
Load Capacitance: 10pF.  
Effective Series Resistance: 50-ohm.  
Effective Shunt Capacitance: 7pF.

KDS Recommended Conditions:  
Normal Frequency: 24.576MHz  
Frequency Tolerance: +/- 30ppm.  
Load Capacitance: 12pF+/-0.2.  
Effective Series Resistance: 50-ohm.  
Effective Shunt Capacitance: 7pF.

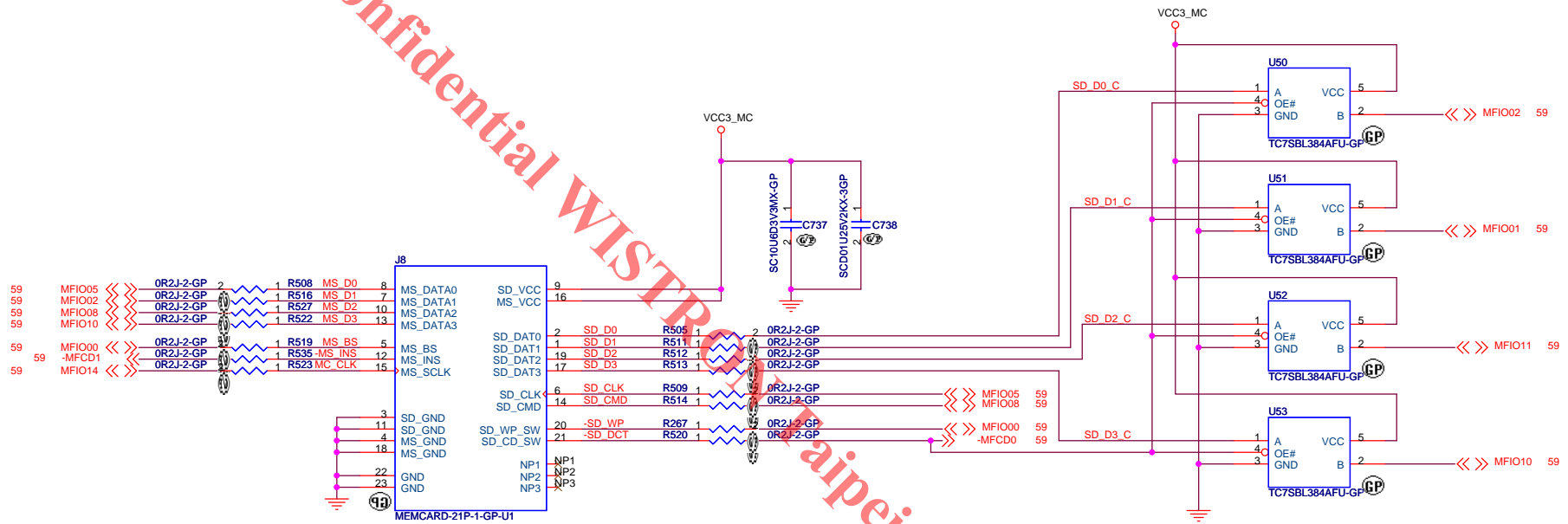
Variant Name: **緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: **1394/MEDIA CARD CONTROLLER**

Size A3 Document Number: **Kendo-1 WS** Rev **SC**

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		U50, U51, U52, U53	Wistron part number
1	TOSHIBA	TC7SBL384AFU-GP	73.7S384.007
2	NXP	74LVC1G384GW	73.1G384.AHH
3	TI	TS5A3167DCKR	74.53167.A9F

<Variant Name>

**緯創資通** Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title  
**MEDIA CARD INTERFACE**

Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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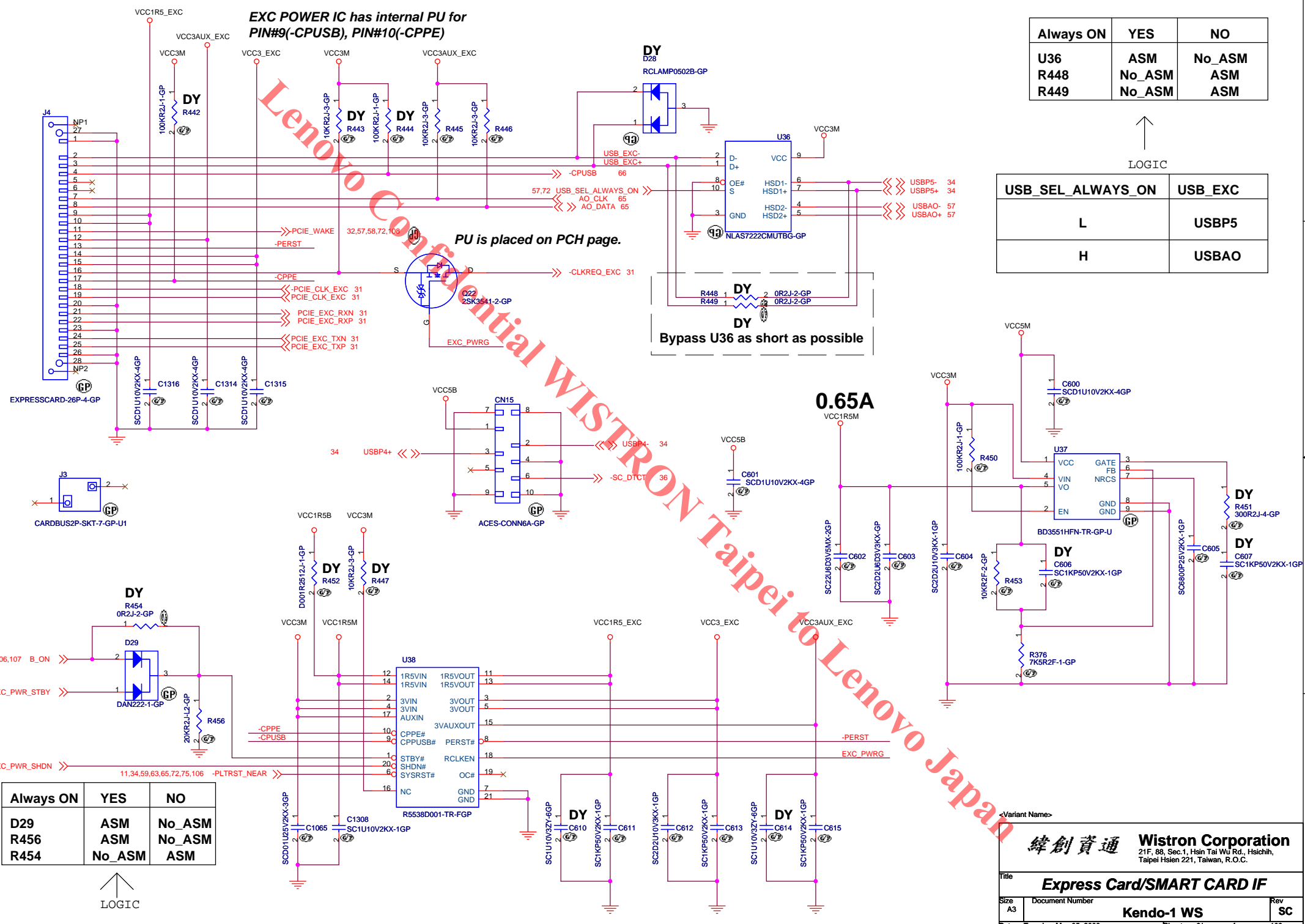
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**EXC POWER IC has internal PU for PIN#9(-CPUSB), PIN#10(-CPPE)**

Always ON	YES	NO
U36	ASM	No_ASM
R448	No_ASM	ASM
R449	No_ASM	ASM

↑  
LOGIC

USB_SEL_ALWAYS_ON	USB_EXC
L	USBP5
H	USBAO



Always ON	YES	NO
D29	ASM	No_ASM
R456	ASM	No_ASM
R454	No_ASM	ASM

↑  
LOGIC

<Variant Name>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

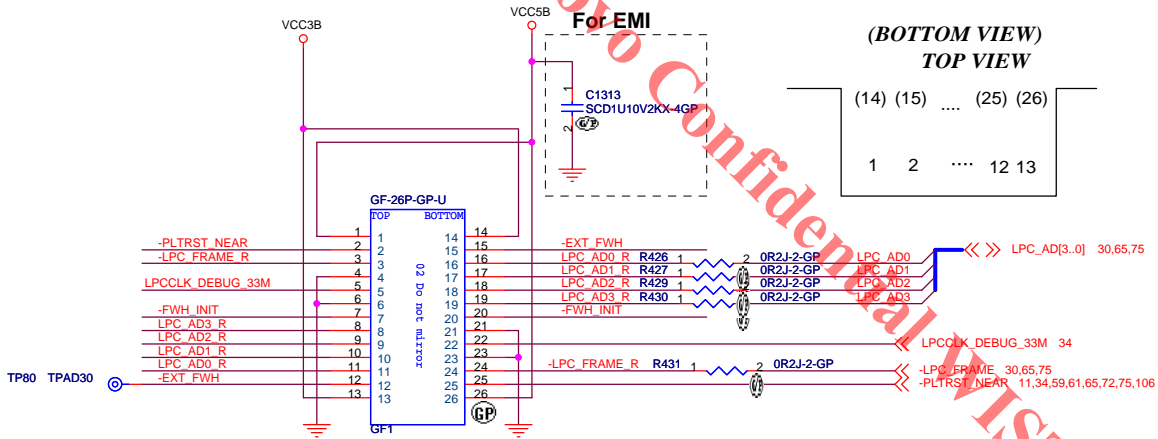
Title <b>Express Card/SMART CARD IF</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 61 of 109	

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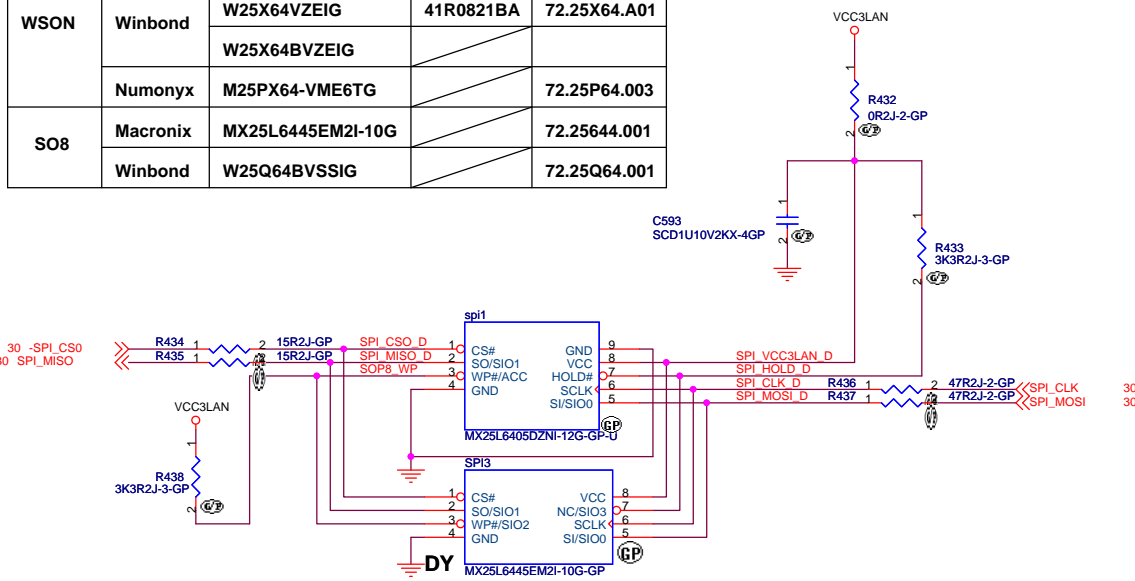
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<b>緯創資通</b>		<b>Wistron Corporation</b> <small>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</small>
Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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# Golden Finger for Debug Board



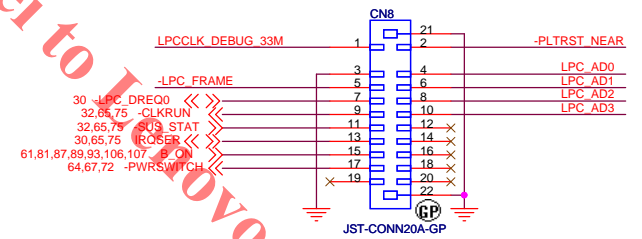
64Mbit SPI FLASH :

Package	Supplier	Vendor P/N	Lenovo P/N	Wistron P/N
WS0N	Macronix	MX25L6405DZNI-12G	41R0821AA	72.25640.B01
	Winbond	W25X64VZEIG	41R0821BA	72.25X64.A01
		W25X64BVZEIG		
	Numonyx	M25PX64-VME6TG		72.25P64.003
SO8	Macronix	MX25L6445EM2I-10G		72.25644.001
	Winbond	W25Q64BVSSIG		72.25Q64.001



Dual foot print for WS0N and SO8.

# Lenovo Debug Tool IF.



Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

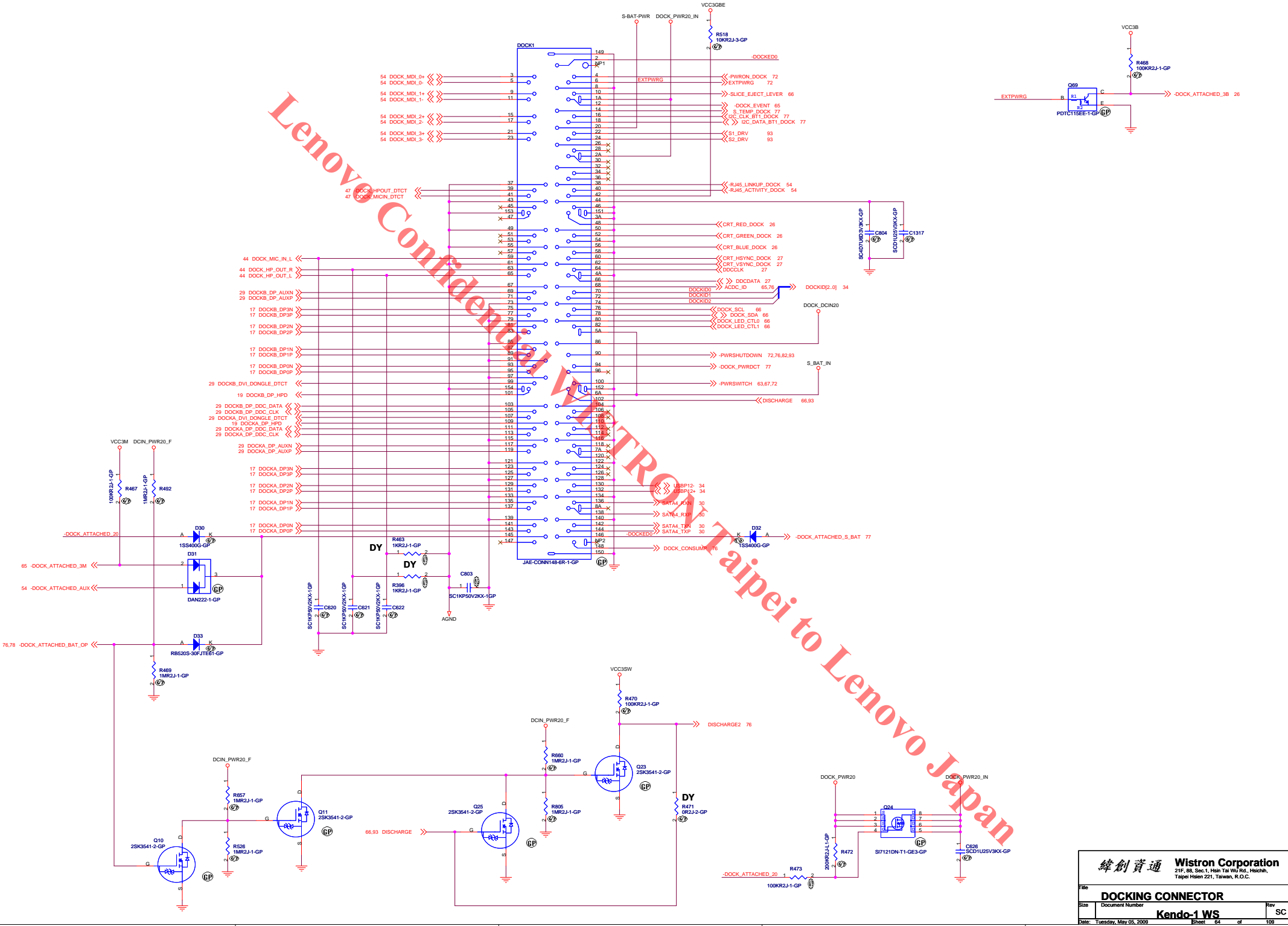
**SPI FLASH**

Variant Name: \_\_\_\_\_

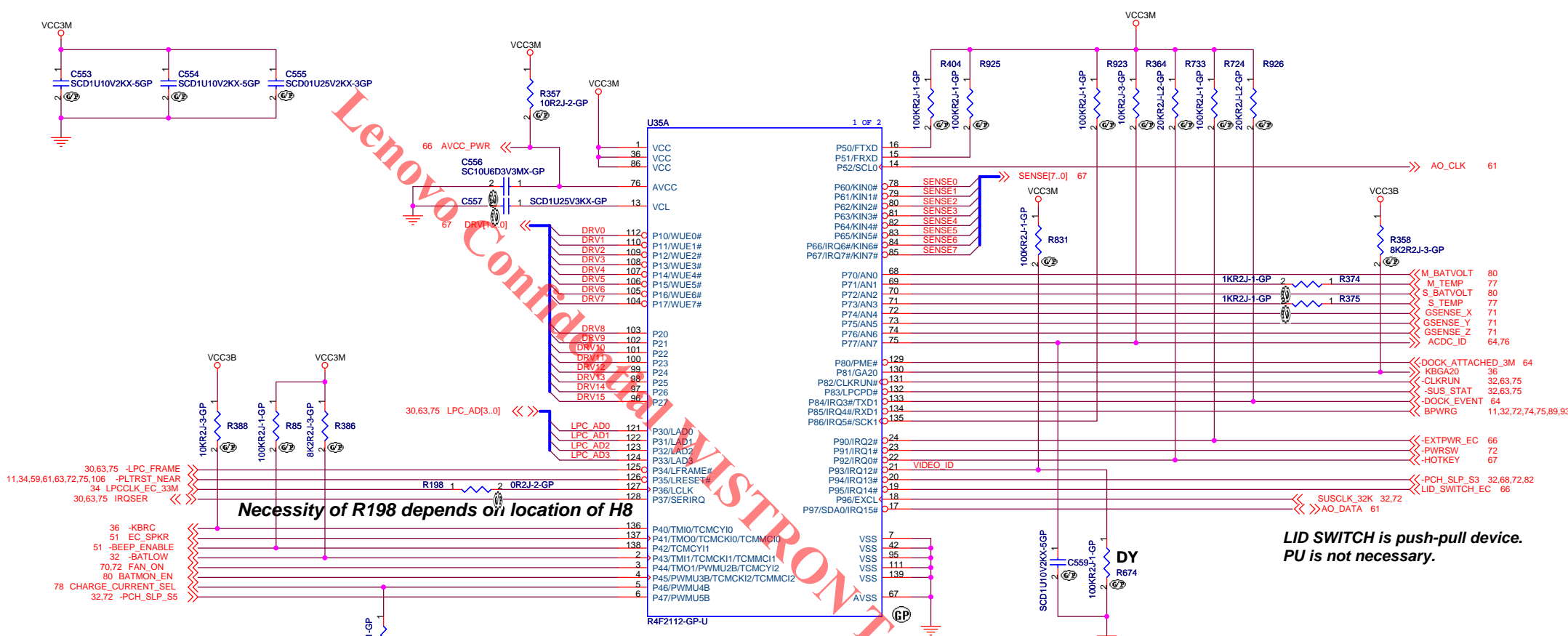
Size A3 Document Number **Kendo-1 WS** Rev **SC**

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Wistron to Lenovo Japan





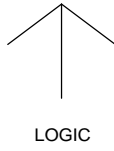


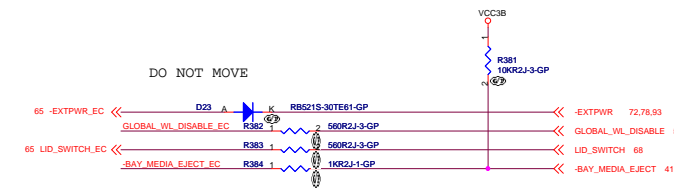
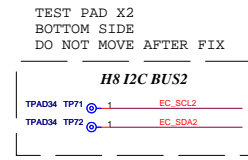
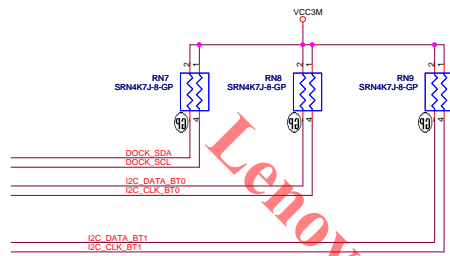
Necessity of R198 depends on location of H8

LID SWITCH is push-pull device.  
PU is not necessary.

Table

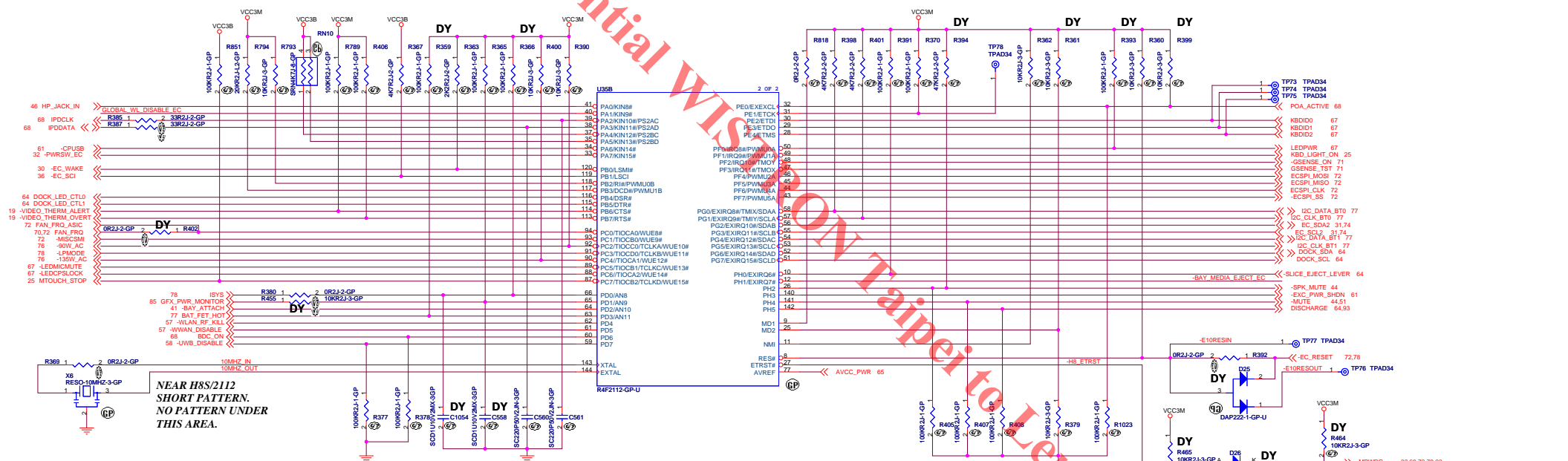
GFX	EXT.	INT.
R831	ASM	NO_ASM
R674	NO_ASM	ASM





Power rail for RN10 can be either VCC3B or VCC5B.

EXC Power IC has internal PU for PIN#20(-EXC\_PWR\_SHDN)



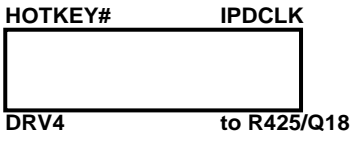
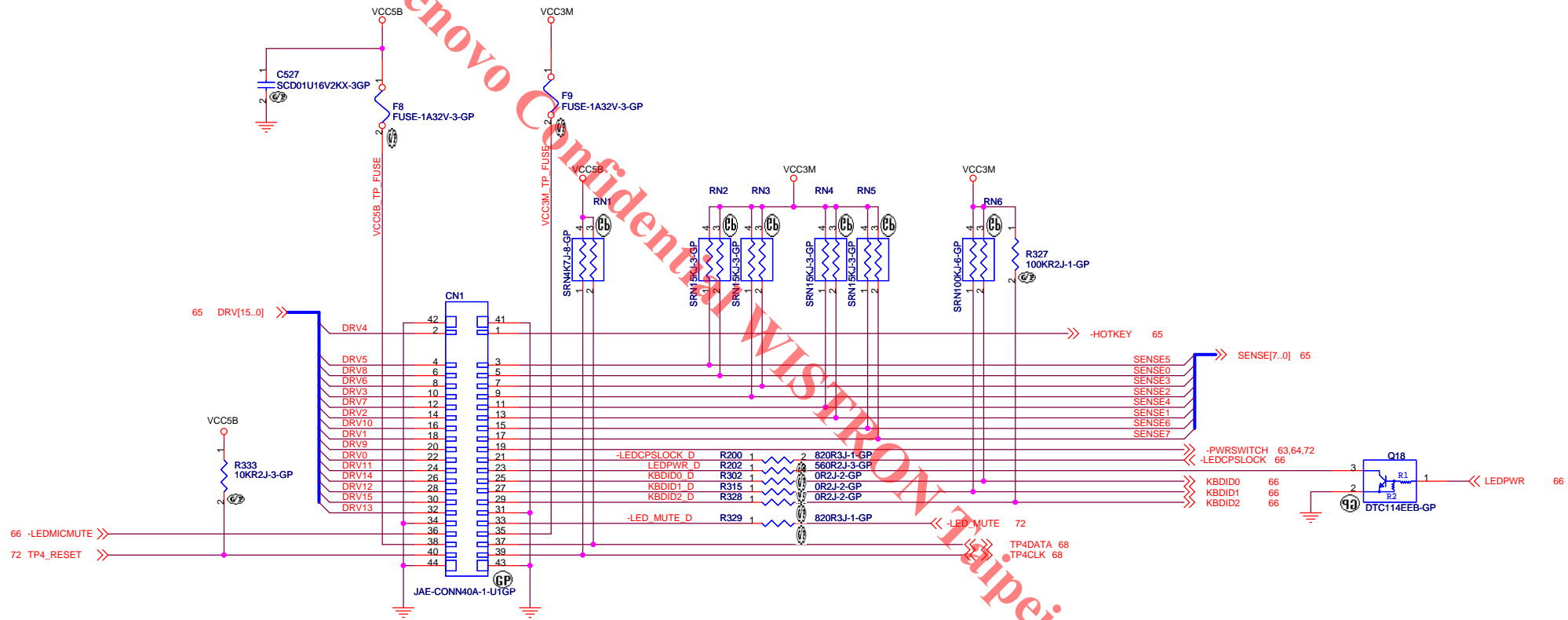
NEAR H8S2112 SHORT PATTERN. NO PATTERN UNDER THIS AREA.

pls. modify this logic table

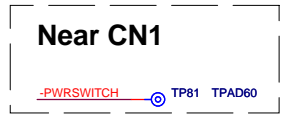
E10A Debug I/F	Enable	Disable
R392	NO ASM	ASM
R466	NO ASM	ASM
D25	ASM	NO ASM
D26	ASM	NO ASM
D20	ASM	NO ASM
R360	ASM	ASM
R361	ASM	NO ASM
R464	ASM	NO ASM
R465	ASM	NO ASM
R379	NO ASM	ASM

↑  
LOGIC

# Keyboard Connector



Keyboard Connector Top View

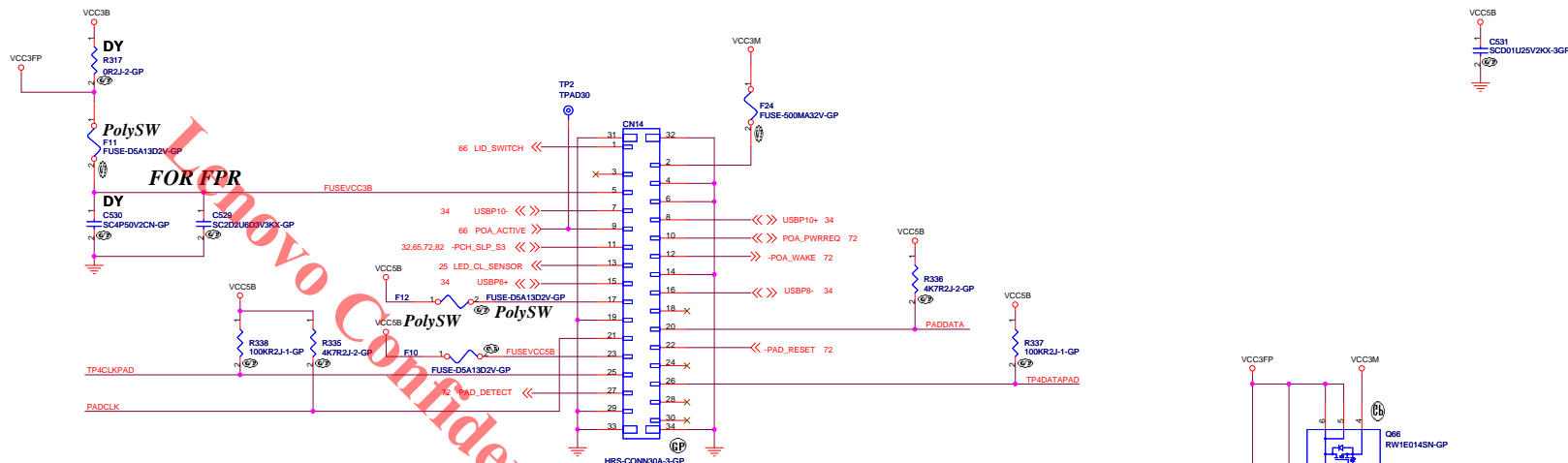


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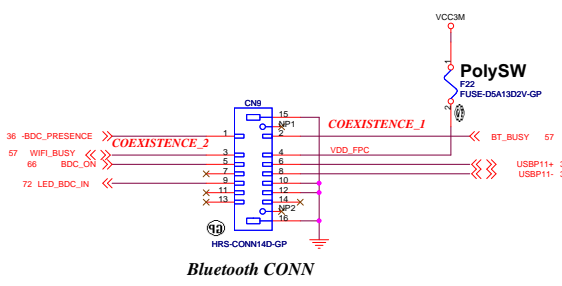
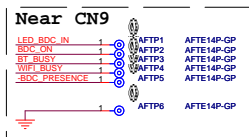
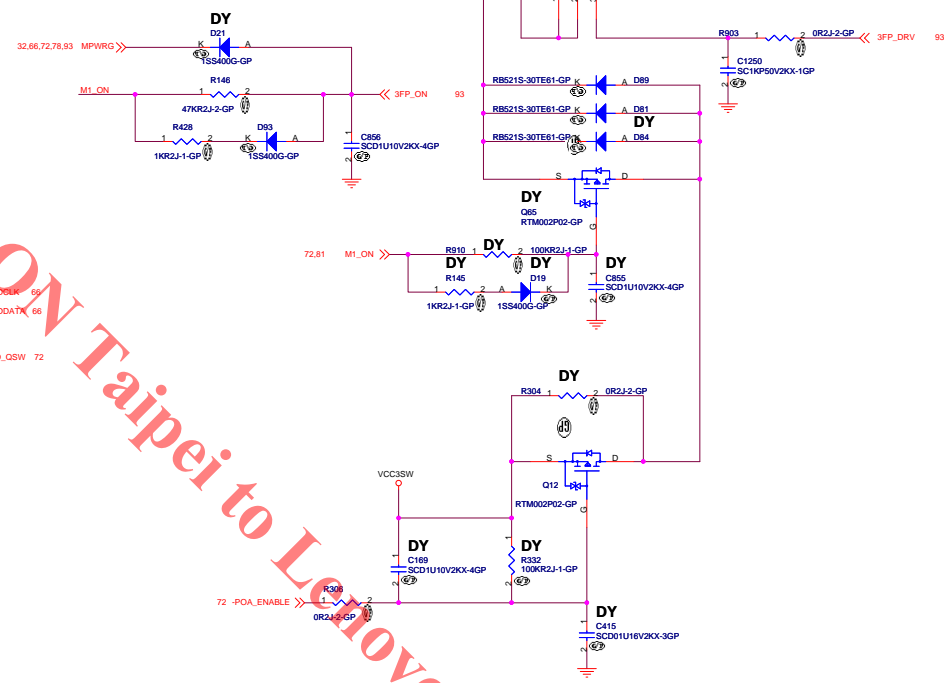
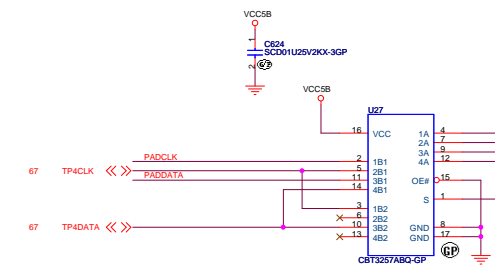
Title		
<b>KEYBOARD CONN</b>		
Size	Document Number	Rev
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Kendo-1 WS		
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**Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

緯創資通



Need to use 30pin connector

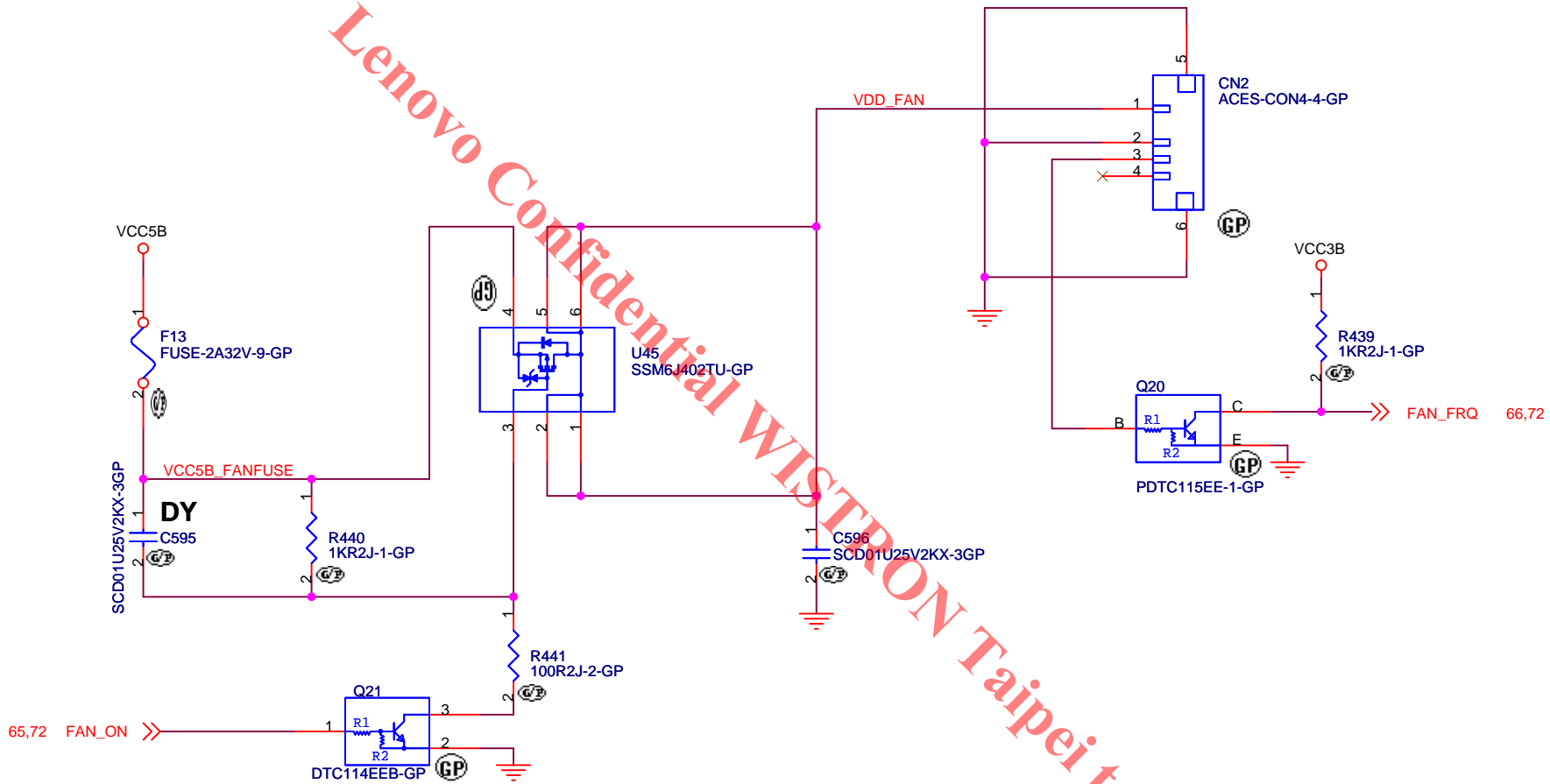


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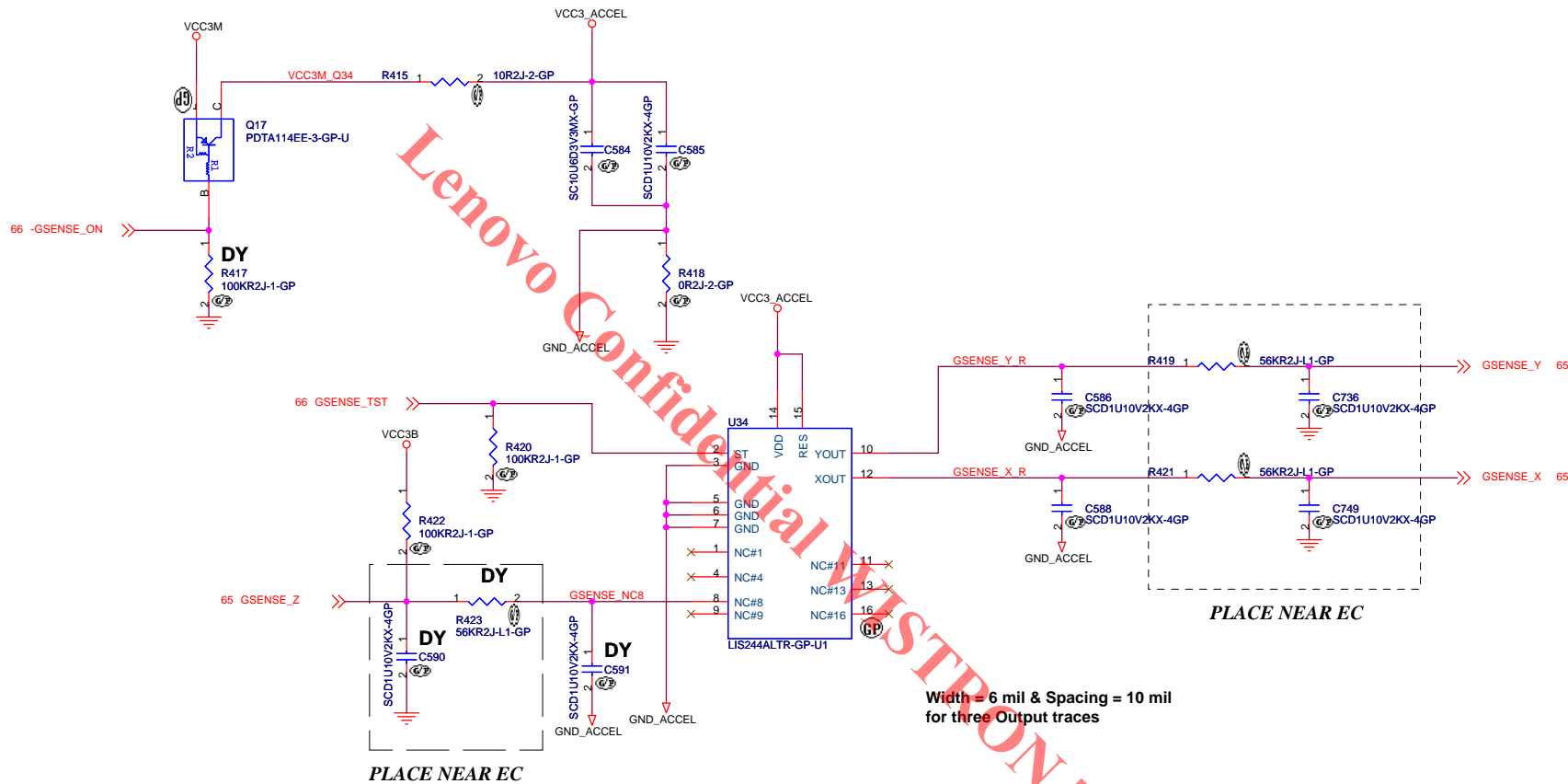
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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Lenovo Confidential WISTRON Taipei to Lenovo Japan



<Core Design>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>FAN CONTROL</b>			
Size A4	Document Number <b>Kendo-1 WS</b>		Rev <b>SC</b>
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Width = 6 mil & Spacing = 10 mil  
for three Output traces

PLACE NEAR EC

PLACE NEAR EC

LIS244AL		NO ACC.	
LIS34AL			
R417	NO-ASM	ASM	
R420	ASM	ASM	
U34	ASM	NO-ASM	
Q17	ASM	NO-ASM	
R415	10-OHM	NO-ASM	
C585	ASM	NO-ASM	
C584	ASM	NO-ASM	
C586	ASM	NO-ASM	
C588	ASM	NO-ASM	
R419	56K	NO-ASM	
C736	ASM	NO-ASM	
R421	56K	NO-ASM	
C749	ASM	NO-ASM	
C591	NO-ASM	NO-ASM	
R423	NO-ASM	NO-ASM	
C590	NO-ASM	NO-ASM	
R422	ASM	ASM	

Layout Comment :  
 (1) Place C586, C588, Q17, R415, R417, C584, C585, R420 close to U34.  
 (2) Avoid routing under DCDC switching area.

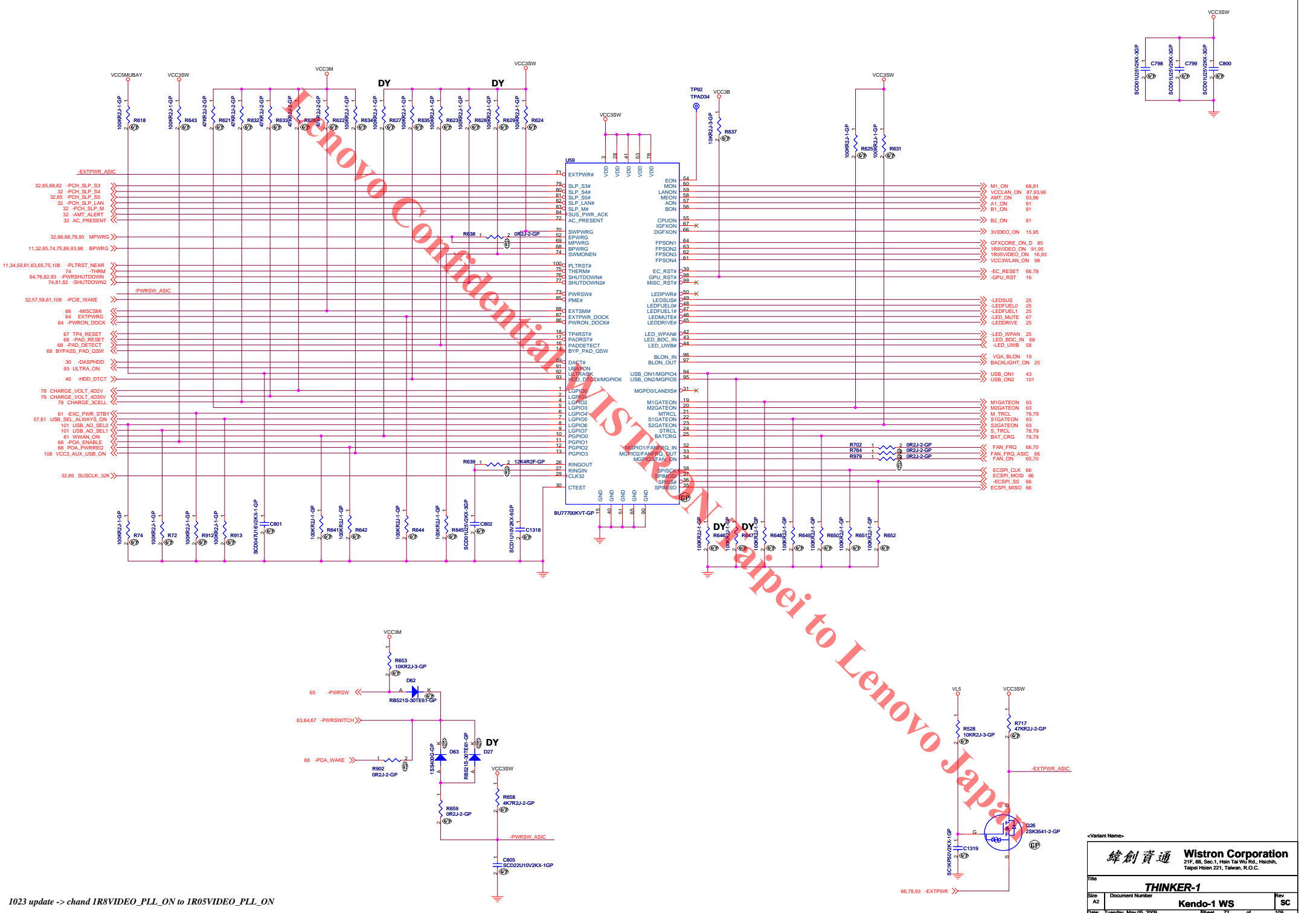
<Core Design>

**緯創資通 Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: **G-SENSOR**

Size: A3 Document Number: **Kendo-1 WS** Rev: **SC**

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1023 update -> chand 1R8VIDEO\_PLL\_ON to 1R05VIDEO\_PLL\_ON

Variant Name: **THINKER-1**

File: **THINKER-1**

Size: A2 Document Number: **Kendo-1 WS** Rev: **SC**

Date: Tuesday, May 05, 2009 Sheet: 72 of 109

緯創資通 **Wistron Corporation**  
 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

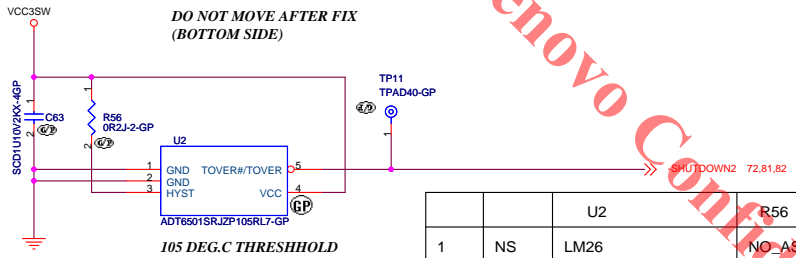


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<Variant Name>		
緯創資通		<b>Wistron Corporation</b> <small>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</small>
Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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# Thermal Sensor for CPU



		U2	R56
1	NS	LM26	NO_ASM
2	MAXIM	MAX6519	NO_ASM
3	ADI	ADT6501SRJZP105RL7	ASM
4	MAXIM	MAX6501UKP105+1	ASM
5	ROHM	BDE1055G	ASM

THESE CAPS MUST BE PLACED AS CLOSE AS POSSIBLE TO MAX6519

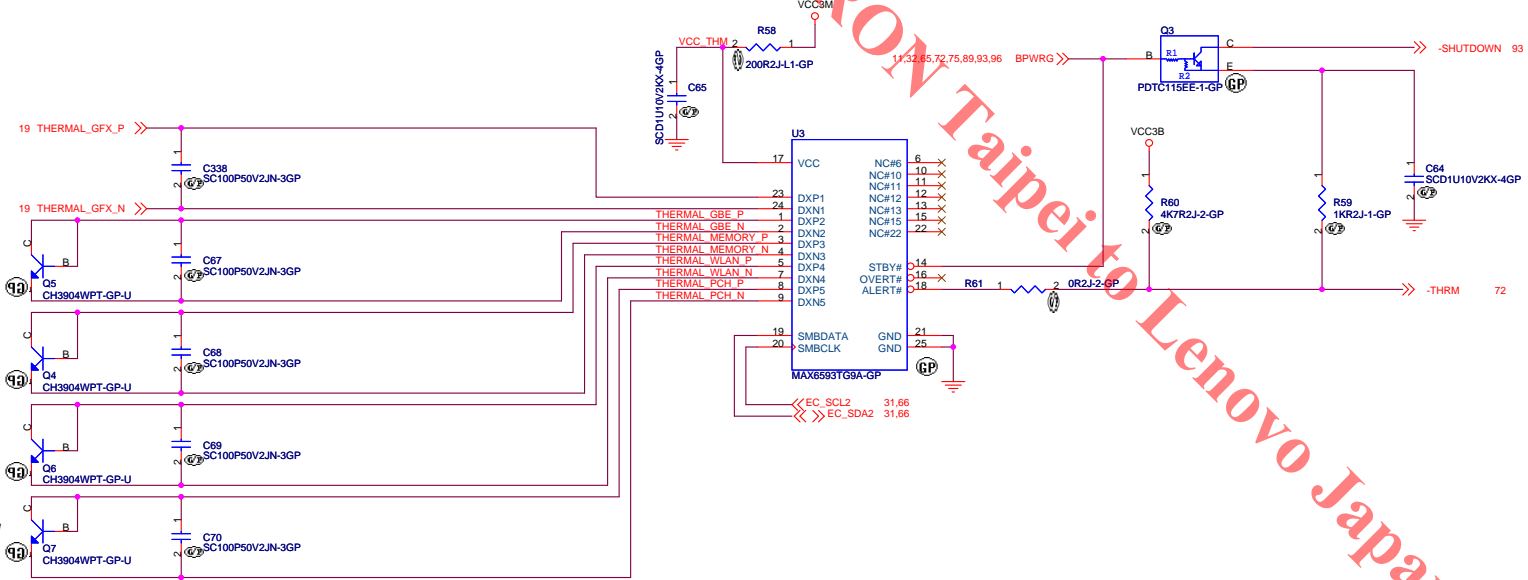
TO Discrete GFX

TO GBE

TO MEMORY TOP

TO WLAN

TO MEMORY BOTTOM



MAX6593TG9A+  
 Remote 1: Discrete GFX Address 01h.  
 Remote 2: GigaBitEthernet Address 02h.  
 Remote 3: SODIMM Top Address 03h.  
 Remote 4: Wireless LAN Slot Address 04h.  
 Remote 5: SODIMM Bottom Address 07h.  
 Local: BASE Cover (Under PCH) Address 05h.

H8 I2C Bus 2 ADDRESS : 4DH  
 TEST PAD FOR BOARD MFG TEST

Layout Comment :  
 (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.  
 (2) Also, it should not be overlapped with large amplitude trace lines either.

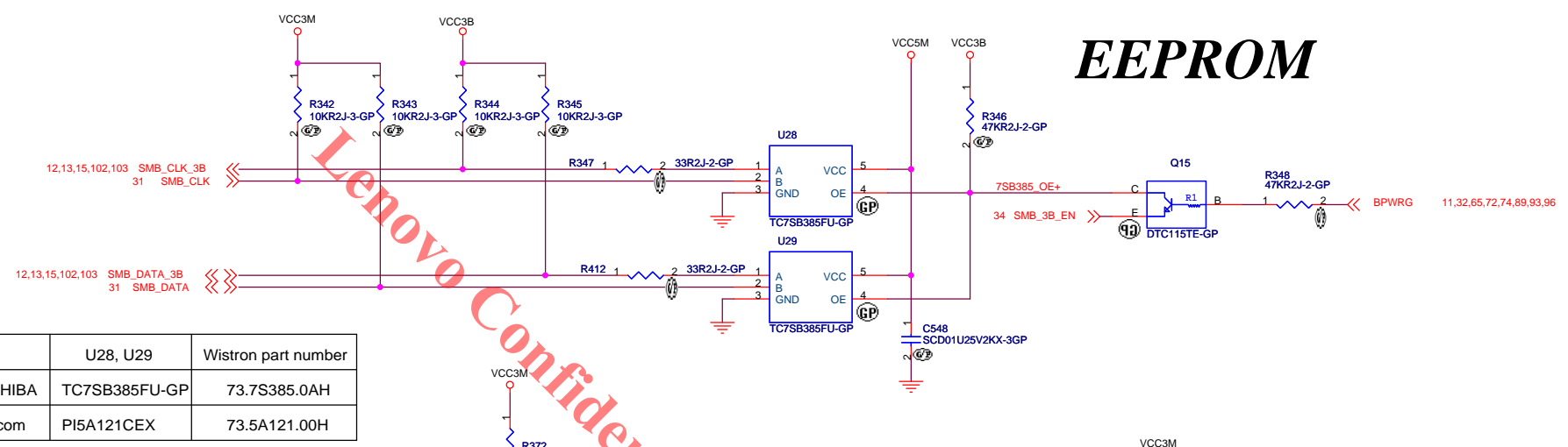
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緯創資通 Wistron Corporation  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

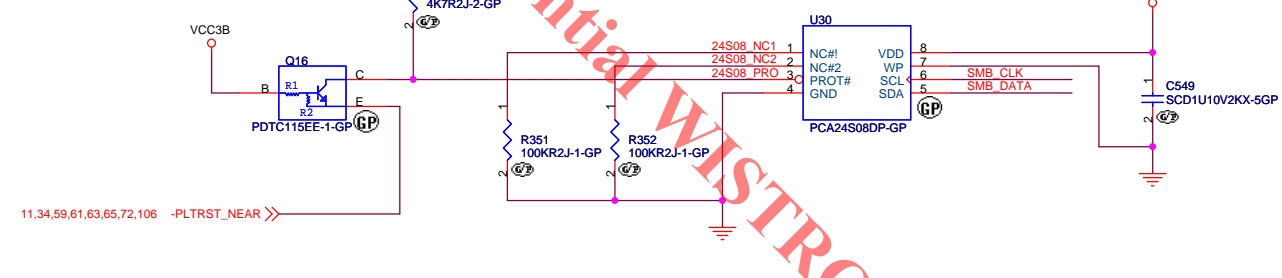
Title: **THERMAL SENSOR**

Size: Custom Document Number: Kendo-1 WS Rev: SC  
 Date: Tuesday, May 05, 2009 Sheet: 74 of 109

# EEPROM



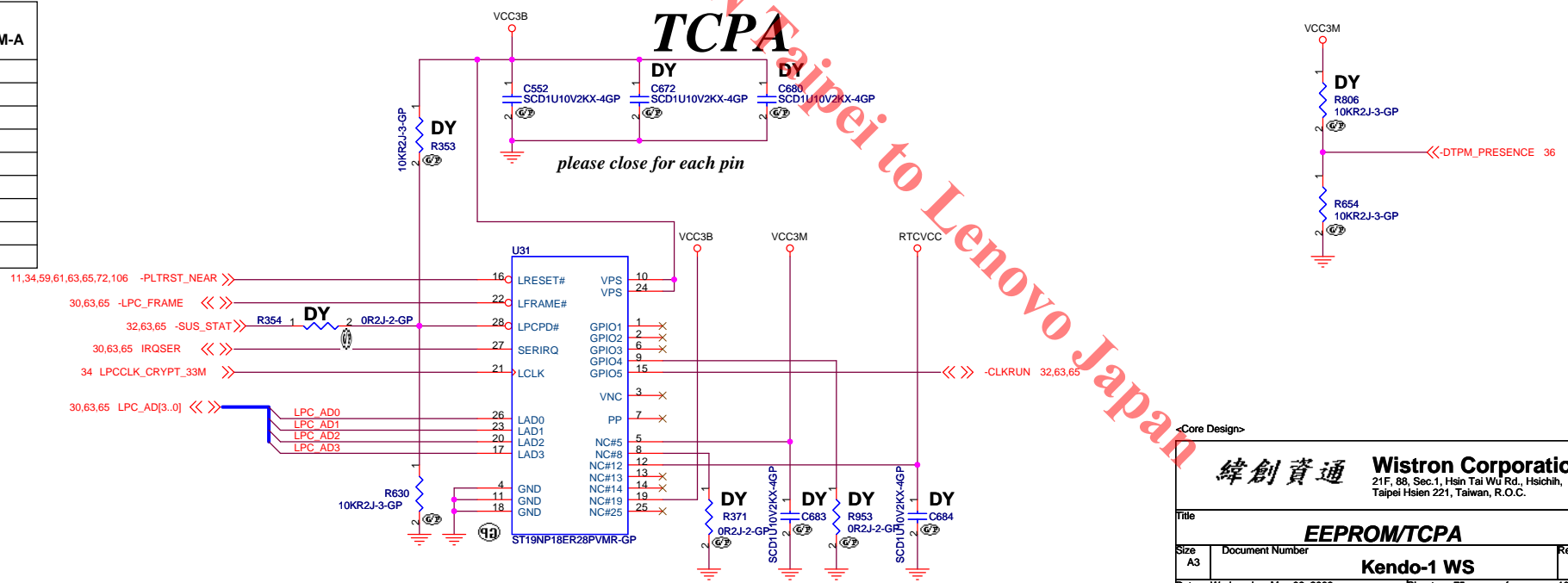
		U28, U29	Wistron part number
1	TOSHIBA	TC7SB385FU-GP	73.7S385.0AH
2	Pericom	PI5A121CEX	73.5A121.00H



		U30	Wistron part number
1	NXP	PCA24S08DP-GP	72.24S08.00Q
2	ROHM	BUL08-1FVJ-W	72.BUL08.00Q

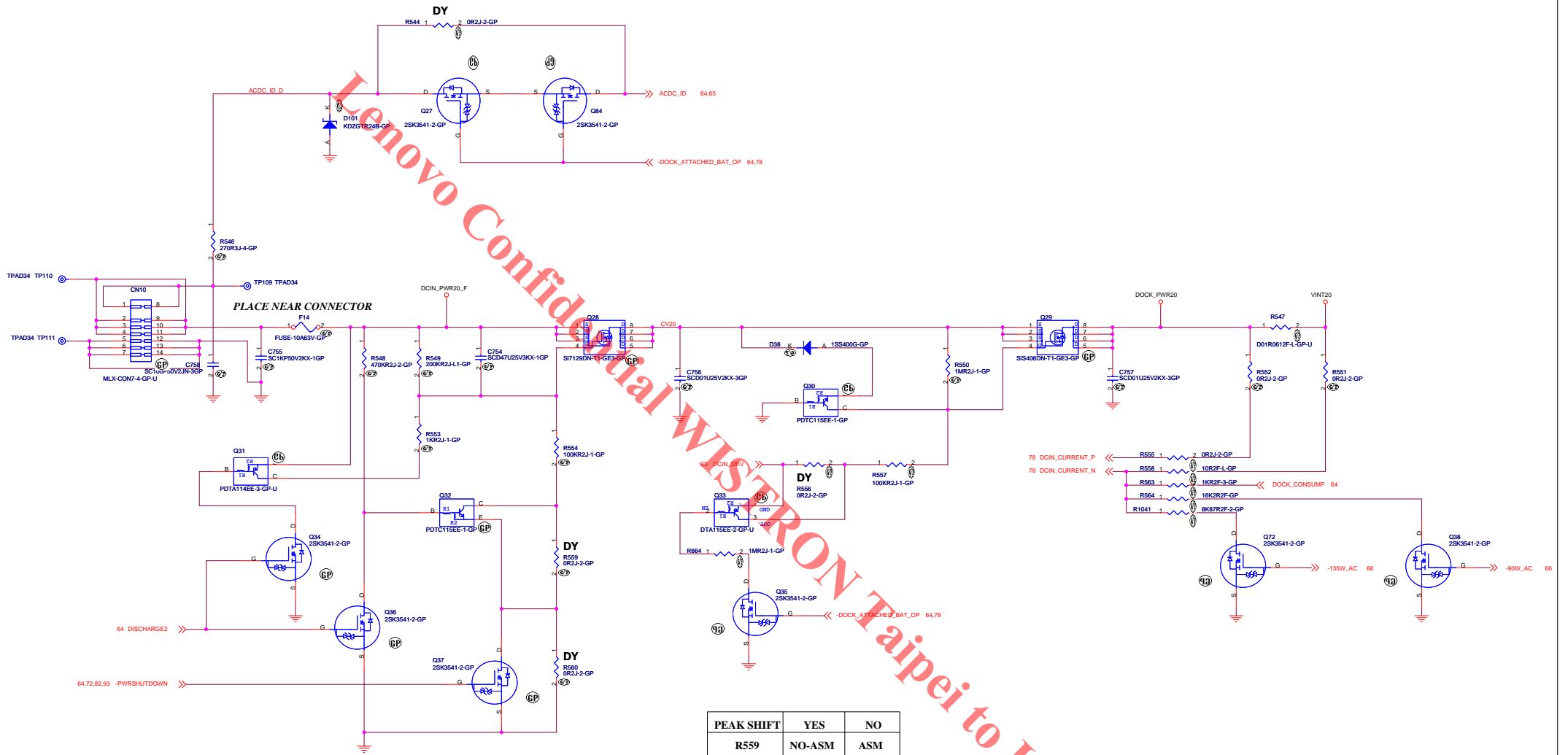
	NO TPM	ST Micro ST19NP18-TPM-A
U31	NO_ASM	ASM
C552	NO_ASM	ASM
C672	NO_ASM	NO_ASM
R354	NO_ASM	ASM
R371	NO_ASM	NO_ASM
R953	NO_ASM	NO_ASM
R353	NO_ASM	NO_ASM
R806	ASM	NO_ASM
R654	NO_ASM	ASM

# TCPA



**Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: **EEPROM/TCPA**  
 Size: A3 Document Number: **Kendo-1 WS** Rev: **SC**  
 Date: Wednesday, May 06, 2009 Sheet 75 of 109



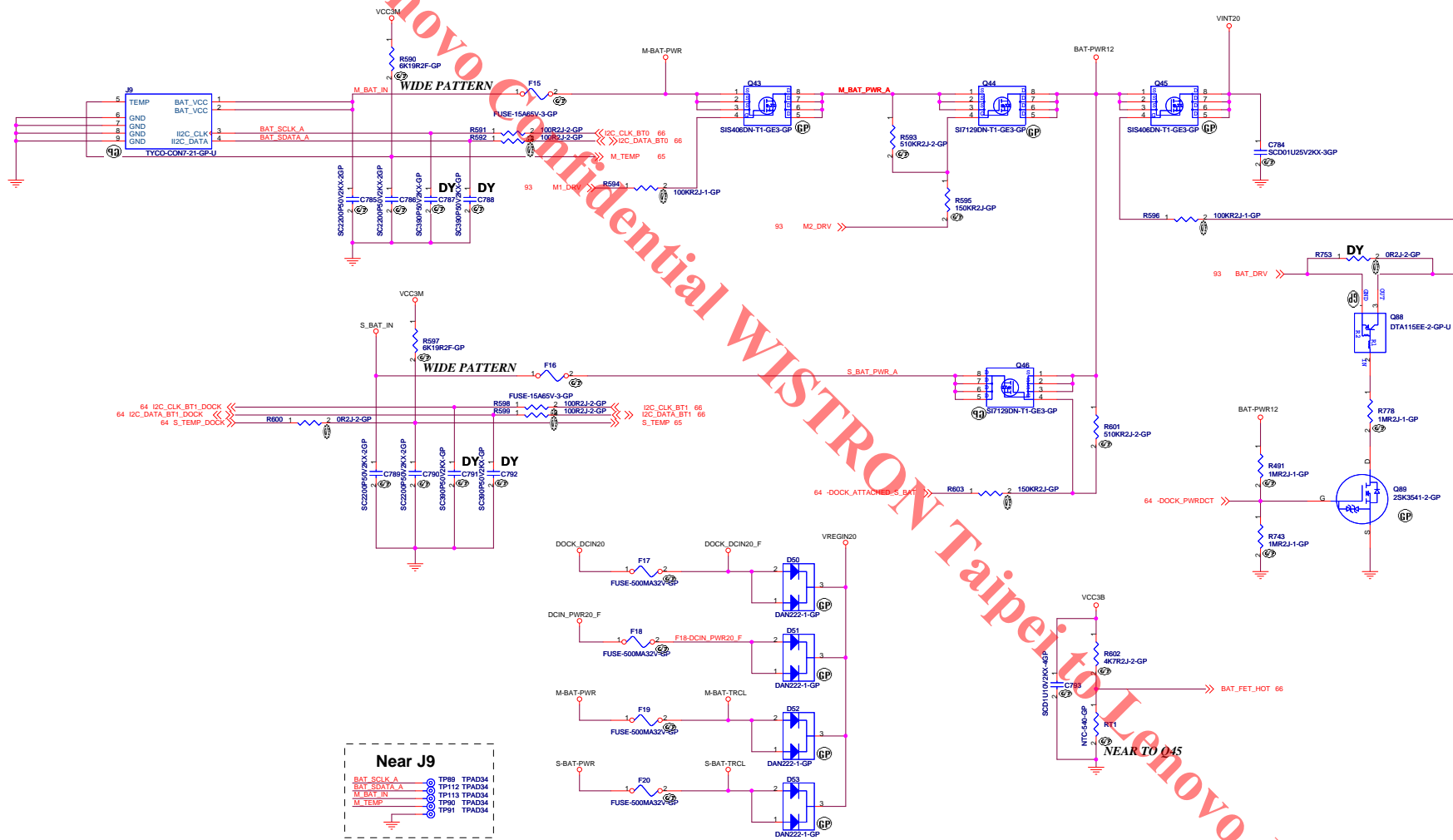
PEAK SHIFT	YES	NO
R559	NO-ASM	ASM
R548	ASM	NO-ASM
Q32	ASM	NO-ASM
Q36	ASM	NO-ASM

↑  
LOGIC

AC-135W AC TABLE

AC Adapter	#135W_AC	#90W_AC
135W	L	L
90W	H	L
65W	H	H

↑  
LOGIC

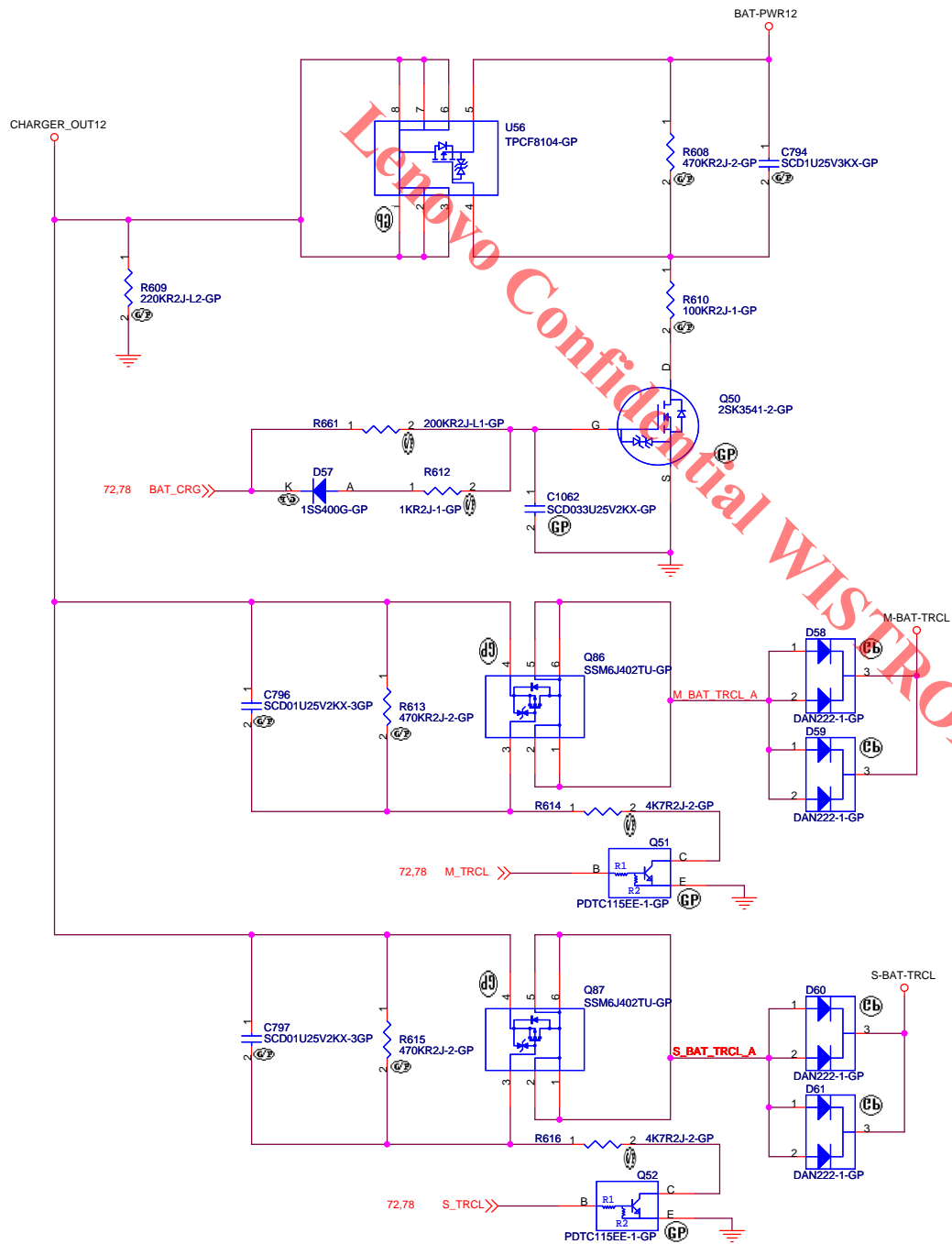


<Core Design>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,  
Taipei Hsien 221, Taiwan, R.O.C.

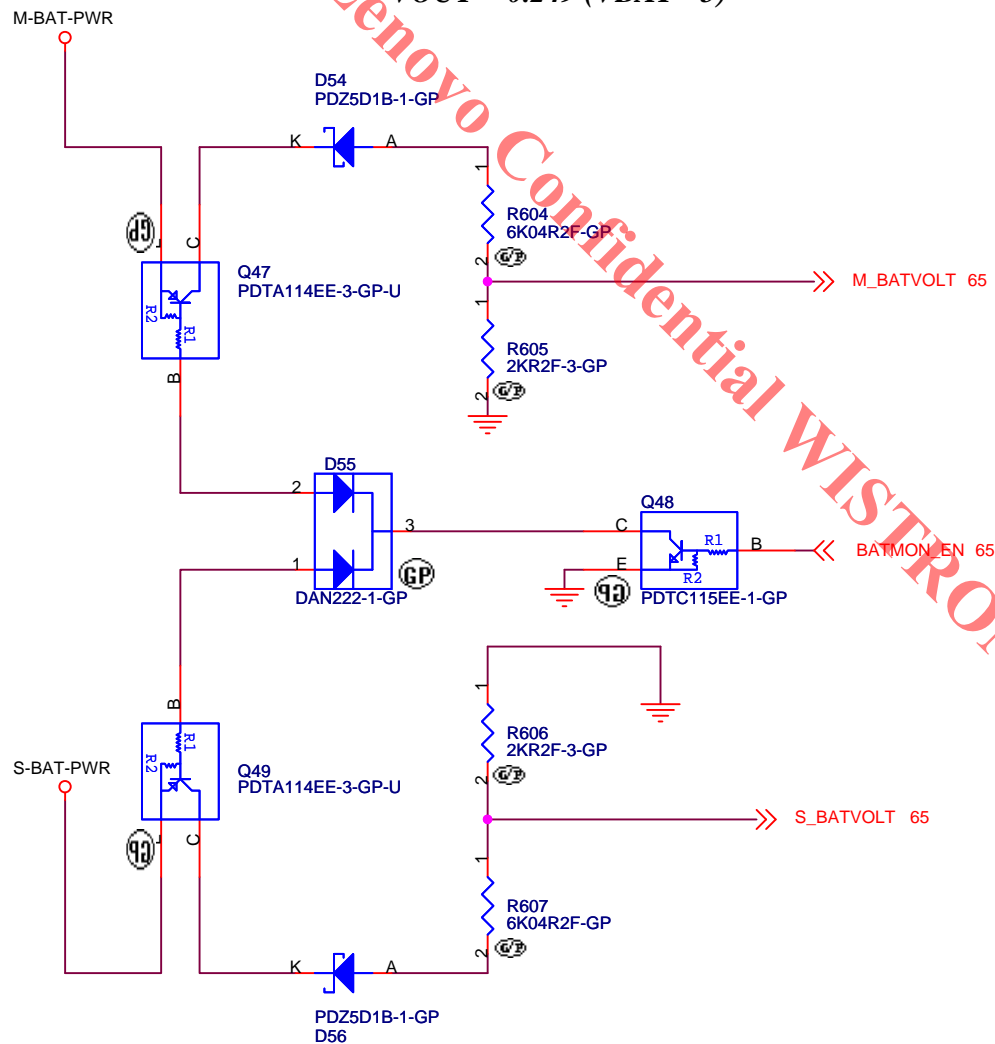
File	BATTERY INPUT		
Size	Document Number	Kendo-1 WS	Rev SC
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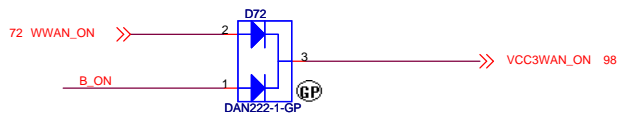
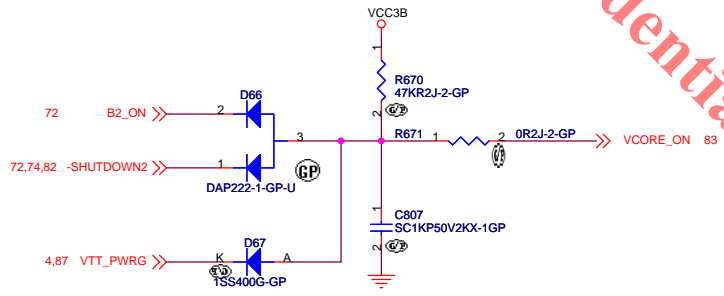
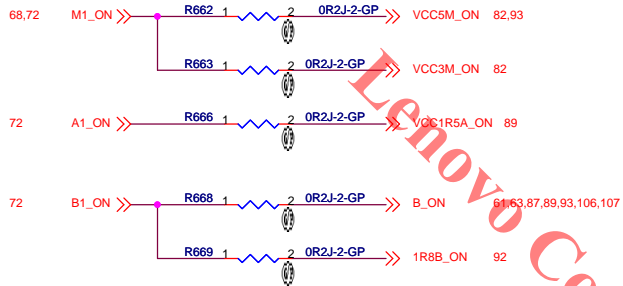
$V_{OUT} = 0.249 (V_{BAT} - 5)$



<Core Design>

<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>BATTERY MONITOR</b>		
Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 80 of 109





**Table**

ALWAYS ON	YES	NO
D72	ASM	NO_ASM

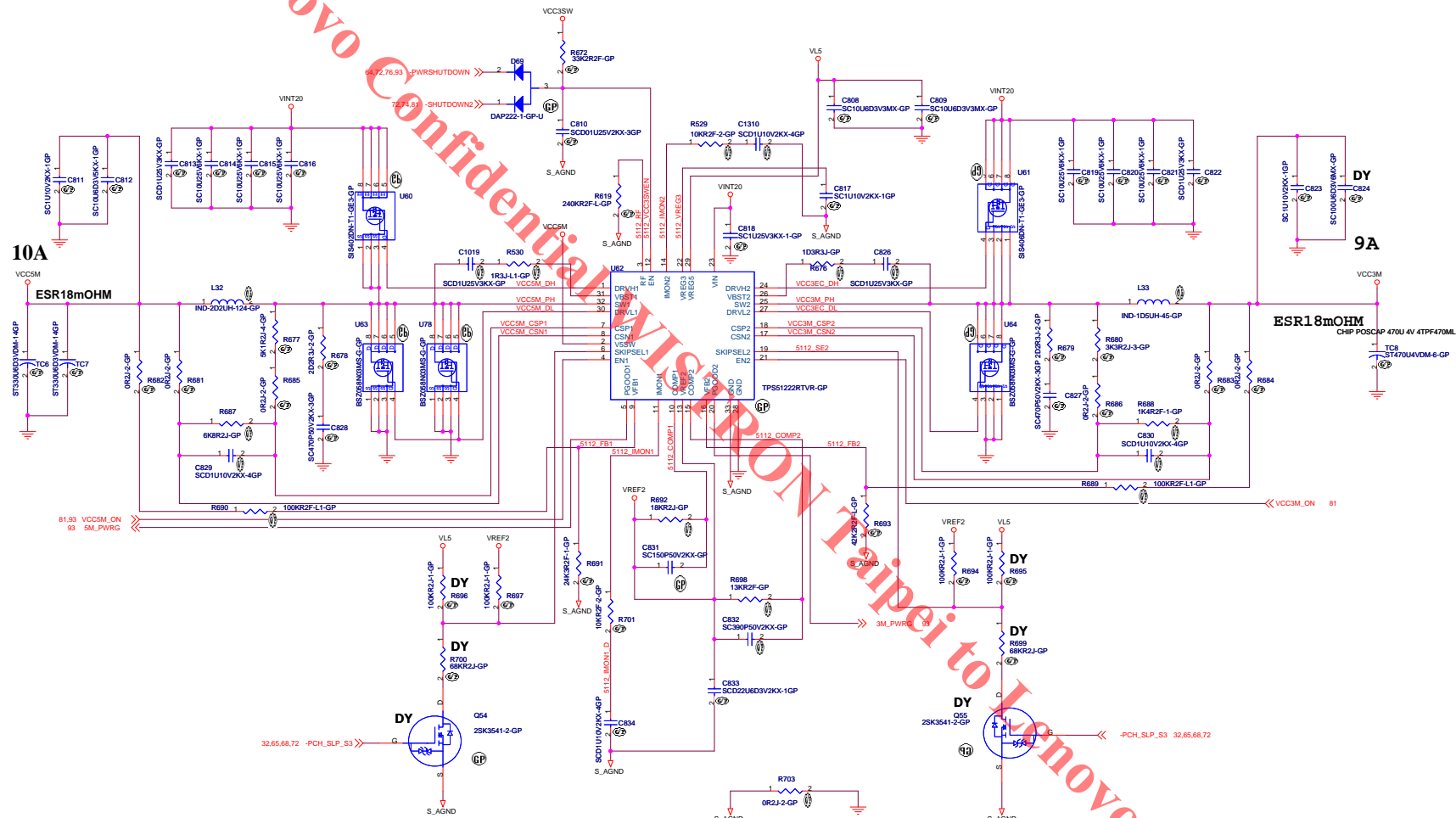
Lenovo Confidential WISTRON Taipei to Lenovo Japan

<Core Design>

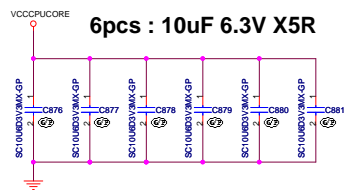
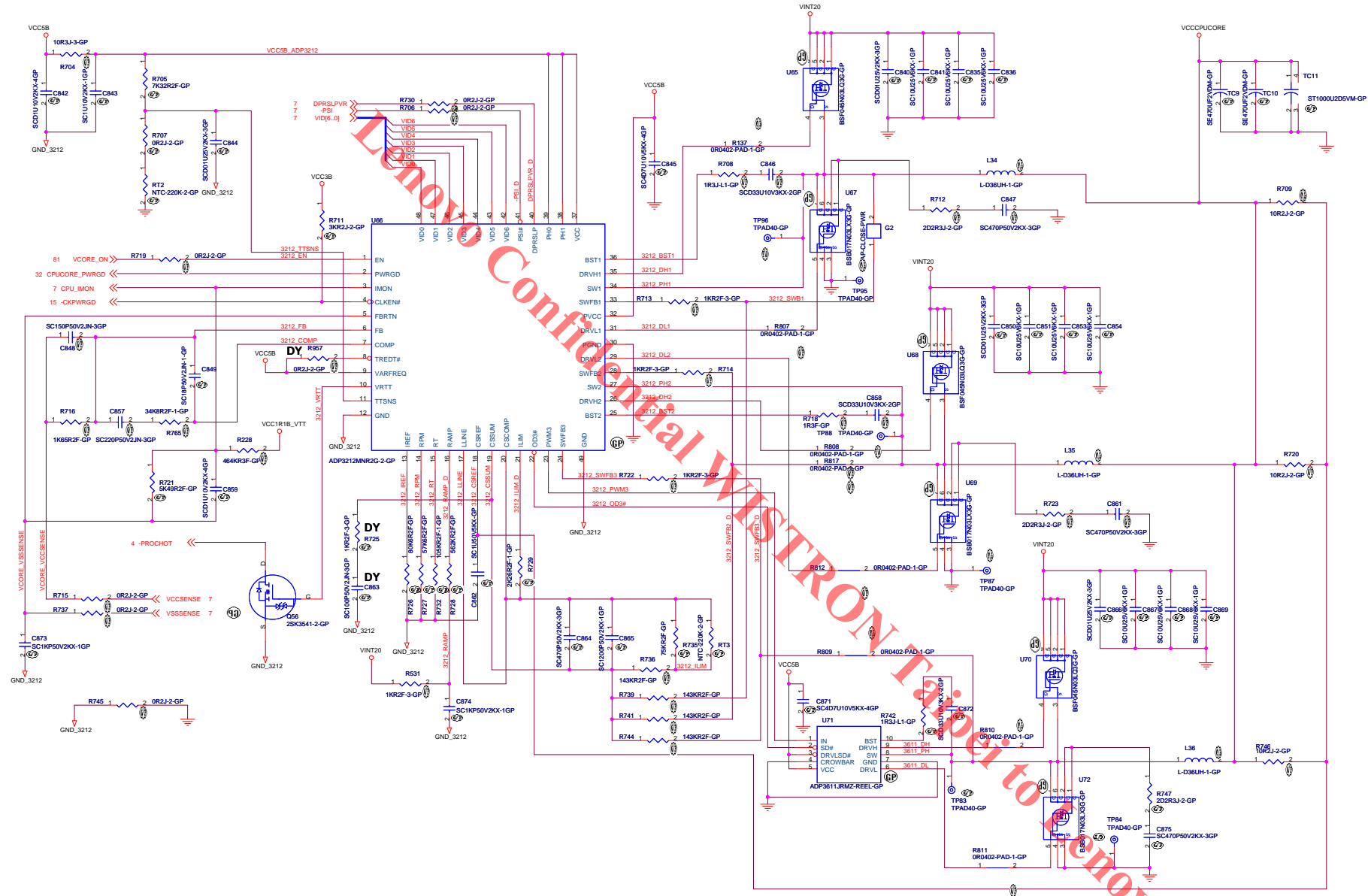
**緯創資通** **Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

**POWER SEQUENCE**

Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 81 of 109	



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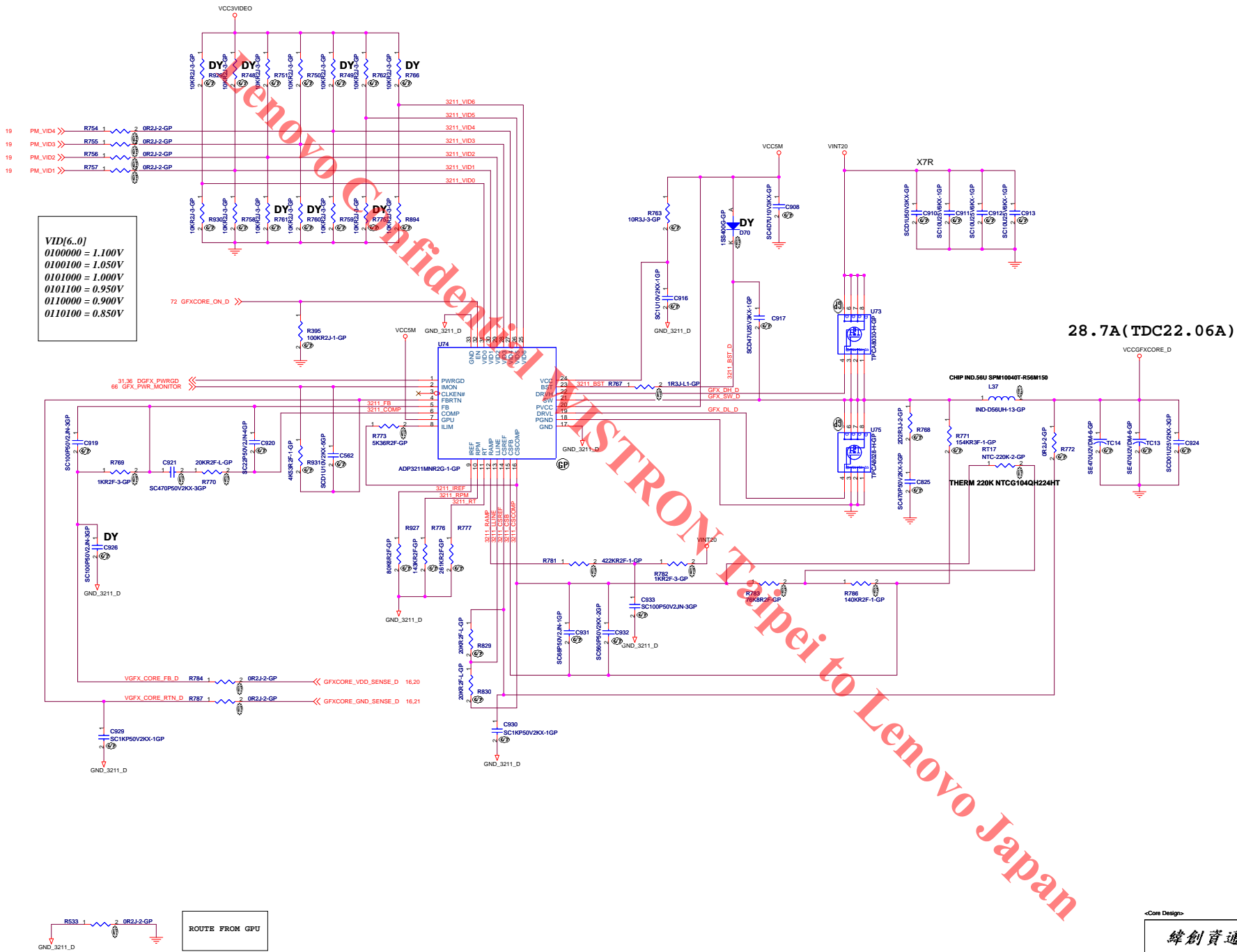


	XE	SV
R739	143K	121K
R741	143K	121K
R744	143K	121K
R729	2.26K	2.15K
R721	5.49K	5.23K
R228	464K	442K
TC9	ASM	NO_ASM
TC10	ASM	NO_ASM

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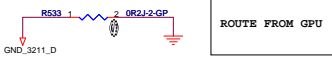
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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VID[6..0]  
 0100000 = 1.100V  
 0100100 = 1.050V  
 0101000 = 1.000V  
 0101100 = 0.950V  
 0110000 = 0.900V  
 0110100 = 0.850V

28.7A (TDC22.06A)



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Title		
<b>BLANK</b>		
Size	Document Number	Rev
A3	<b>Kendo-1 WS</b>	<b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 86 of	109

緯創資通 **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.



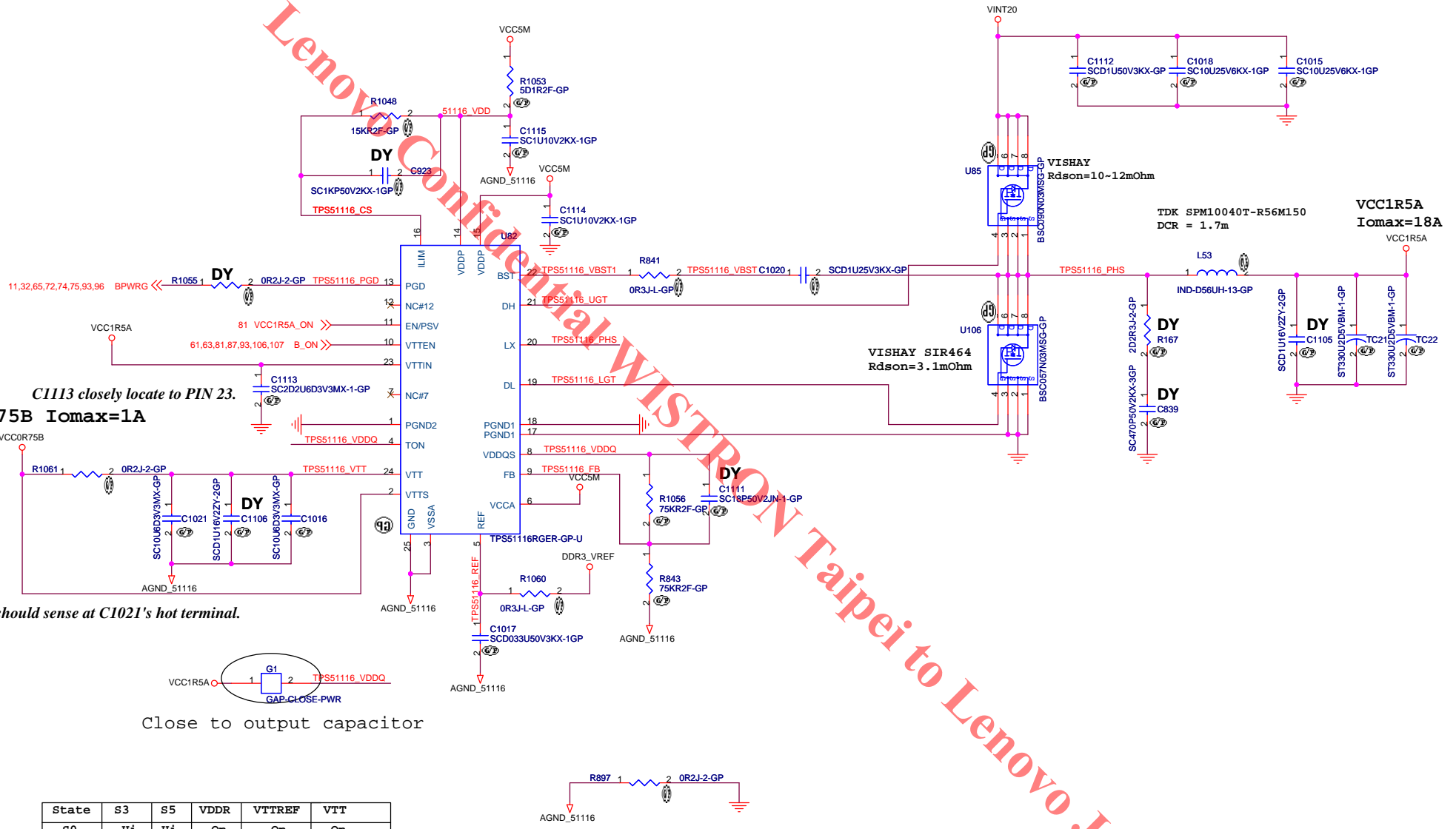
Lenovo Confidential WISTRON Taipei to Lenovo Japan

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Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 88 of 109



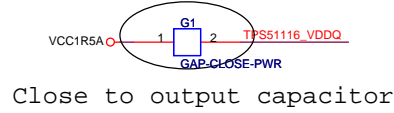
Lenovo Confidential WISTRON Taipei to Lenovo Japan



**VCC0R75B I<sub>omax</sub>=1A**

*C1113 closely locate to PIN 23.*

*VTTNS should sense at C1021's hot terminal.*



State	S3	S5	VDDR	VTTREF	VTT
S0	Hi	Hi	On	On	On
S3	Lo	Hi	On	On	Off (Hi-Z)
S4/S5	Lo	Lo	Off	Off	Off

Core Design>

**緯創資通**  
Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

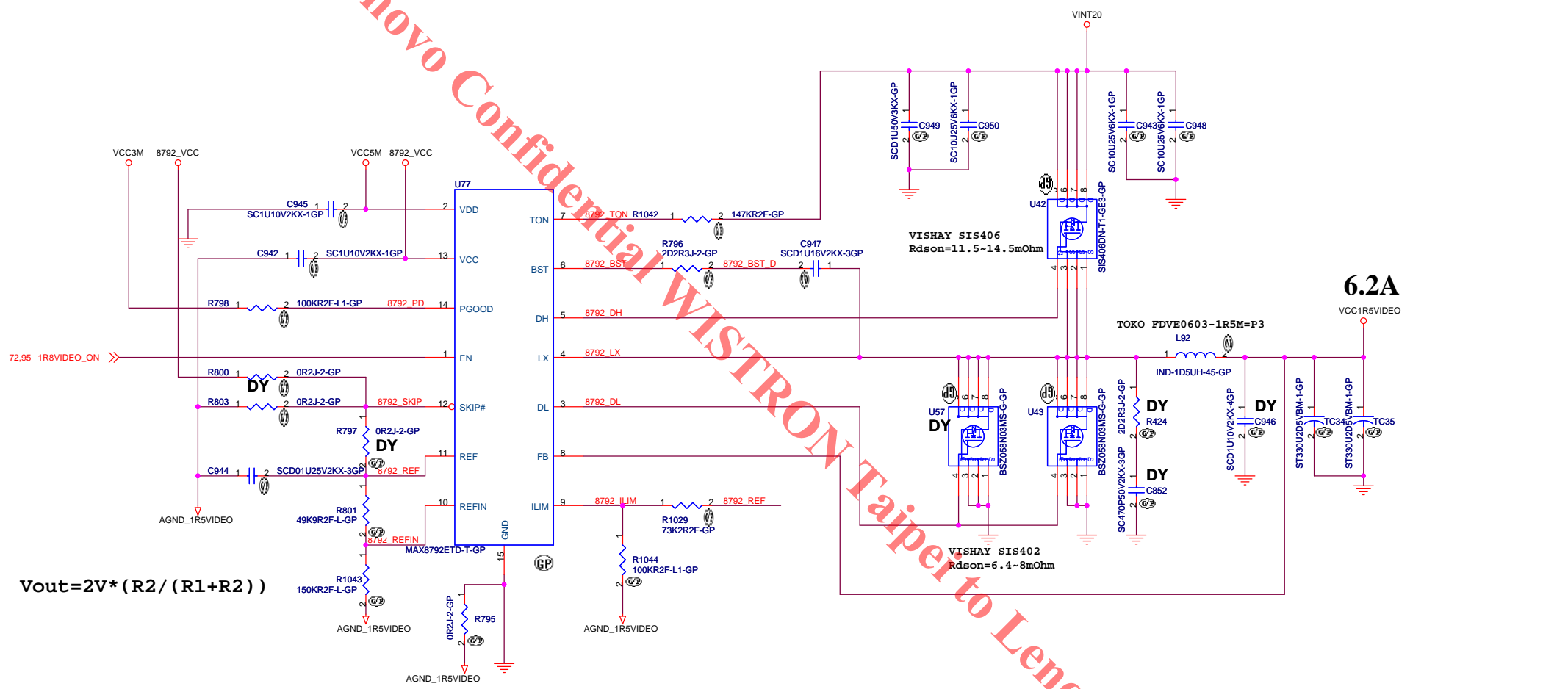
Title <b>DC-DC VCC0R75B</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 89	of 109

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<Variant Name>		
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 90 of 109

Lenovo Confidential WISTRON Taipei to Lenovo Japan

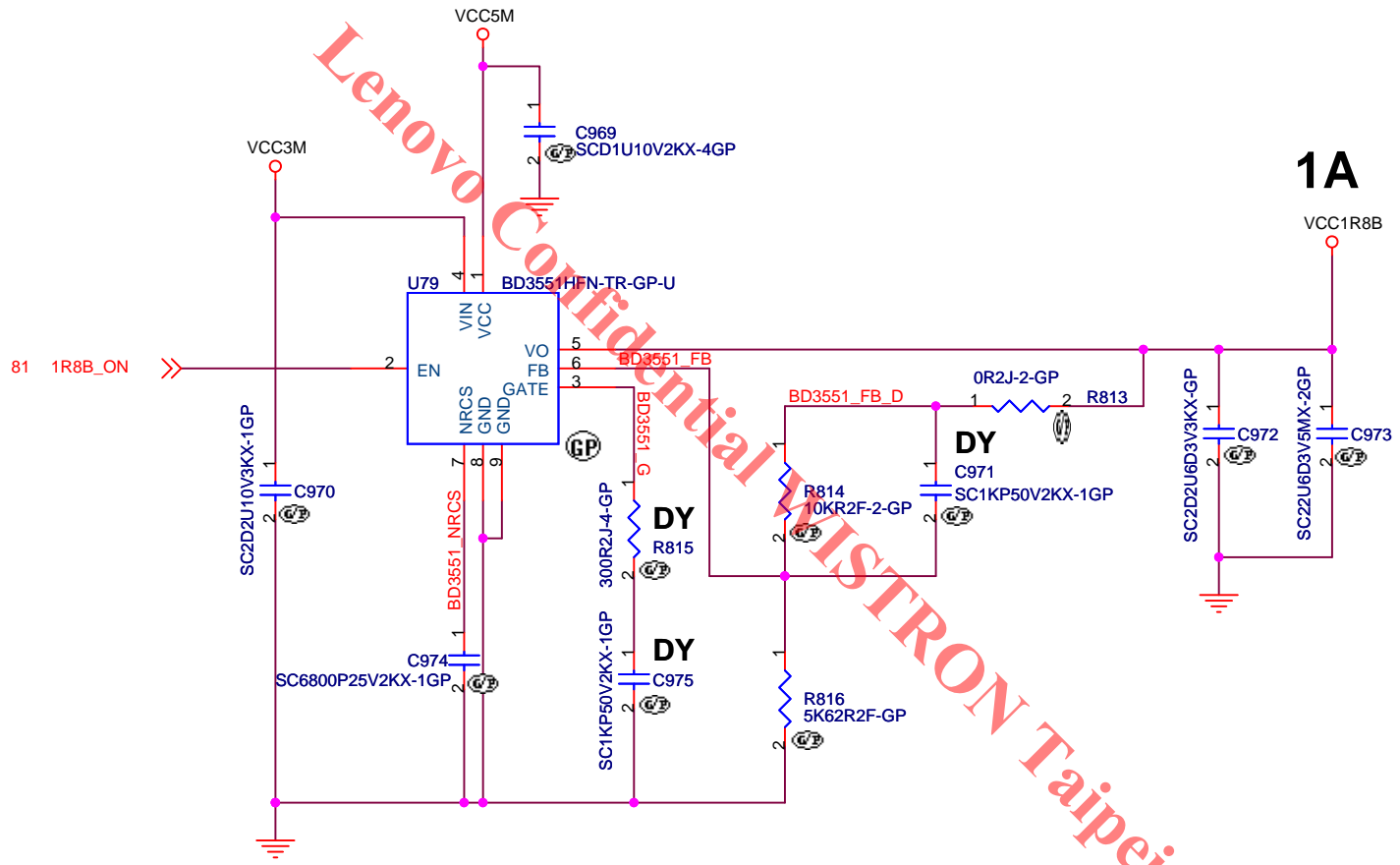


$$V_{out} = 2V * (R2 / (R1 + R2))$$

**6.2A**

<Core Design>


<b>緯創資通 Wistron Corporation</b>	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>DC-DC VCC1R5VIDEO</b>	
Title	
Size A3	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Rev <b>SC</b>
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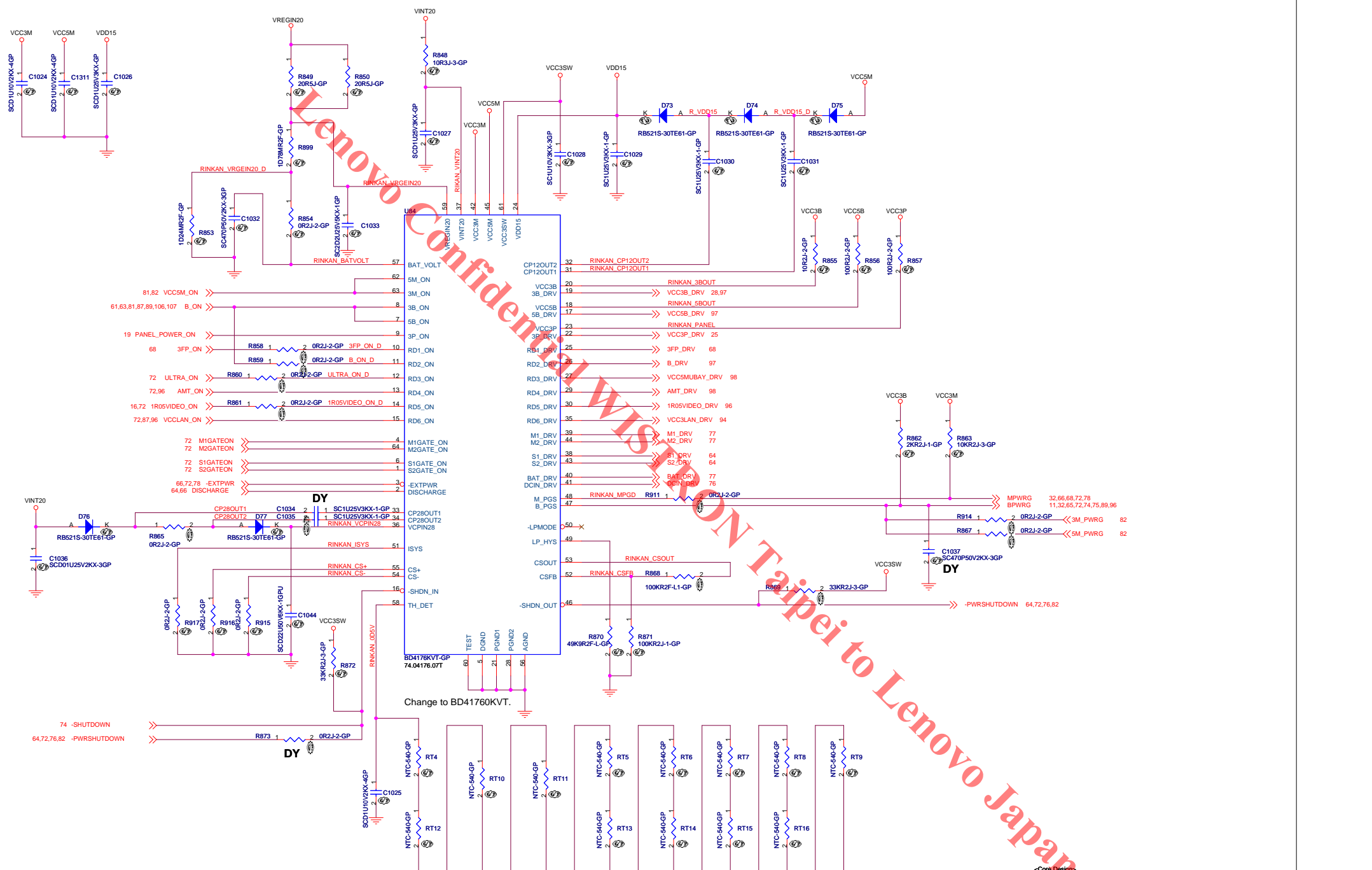


1A

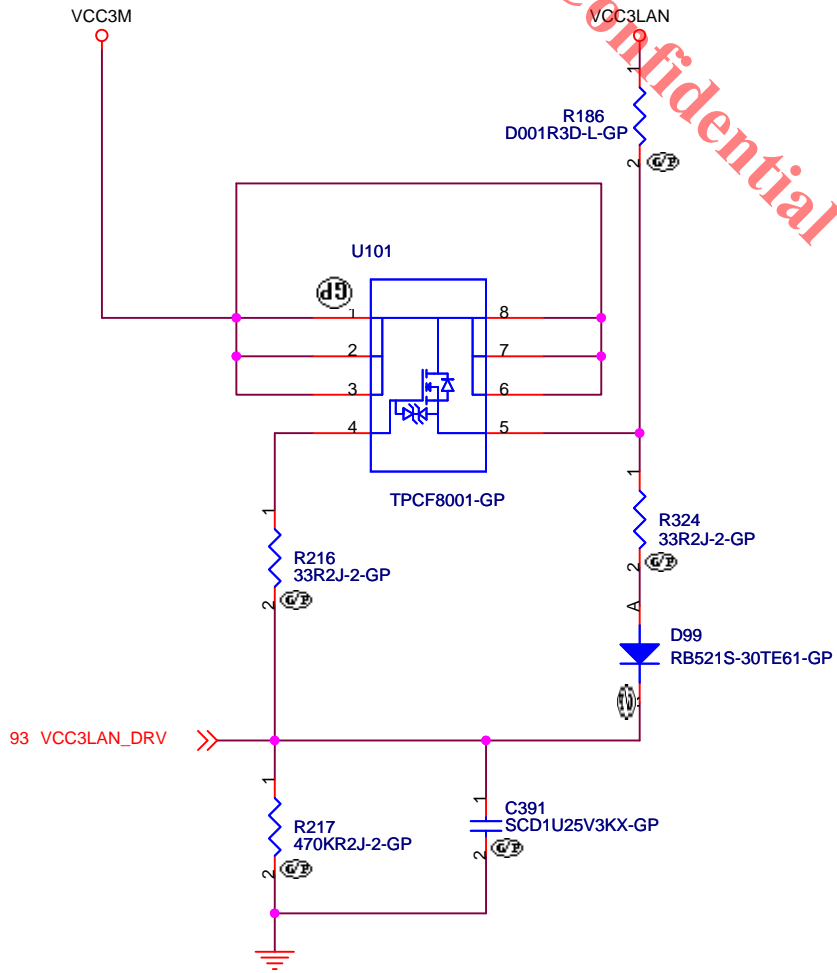
VCC1R8B

<Core Design>

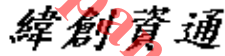
 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>DC-DC VCC1R8B</b>		
Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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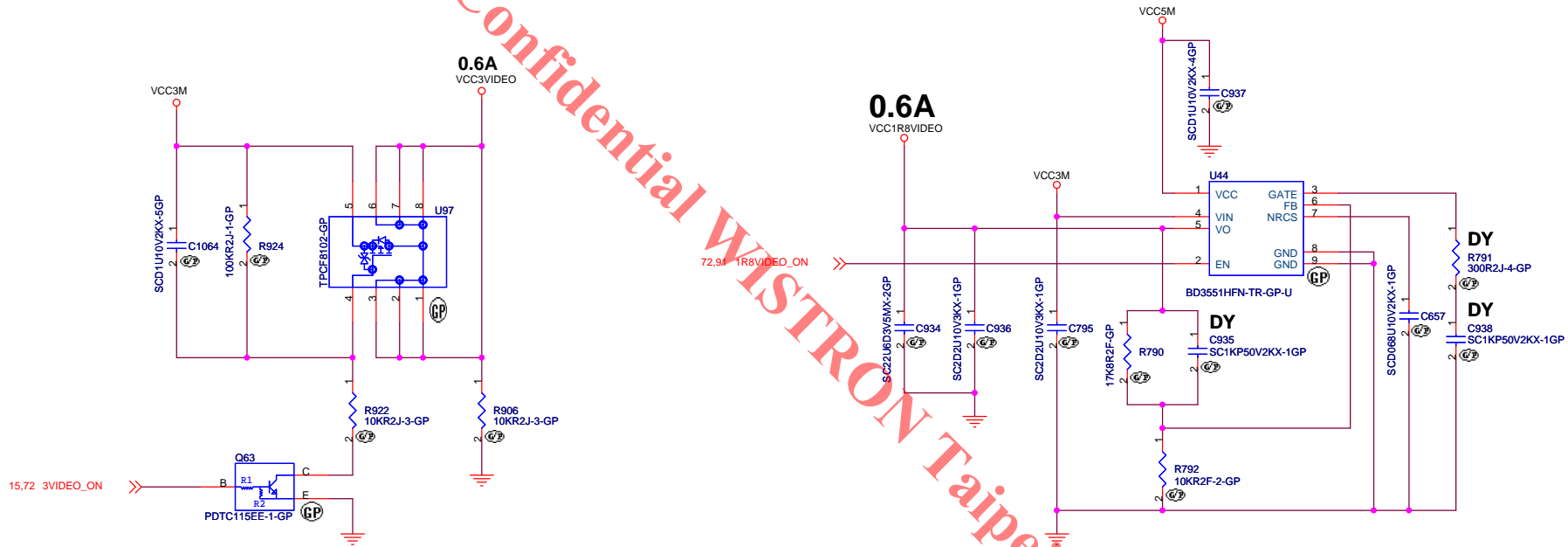
Lenovo Confidential WISTRON Taipei to Lenovo Japan

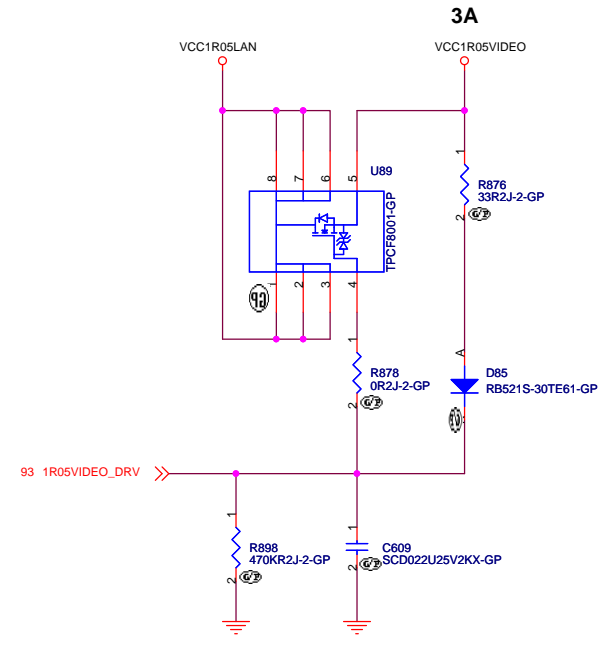
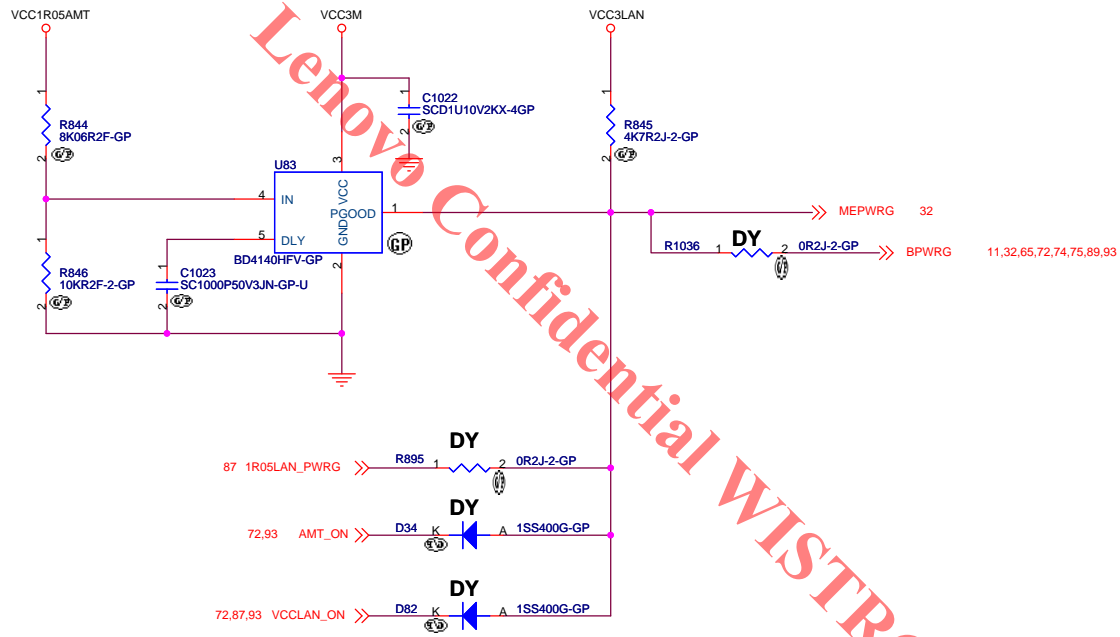


<Core Design>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>Title</b> <b>LOAD SW LAN &amp; LANPWRG</b>			
Size A4	Document Number <b>Kendo-1 WS</b>		Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 94 of	109

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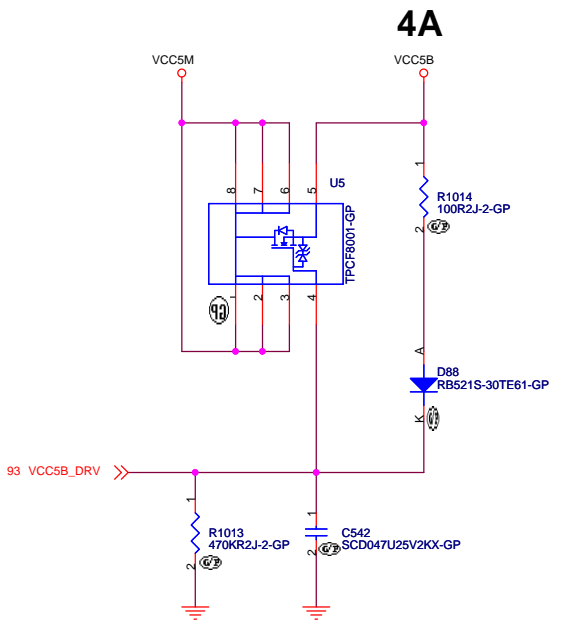
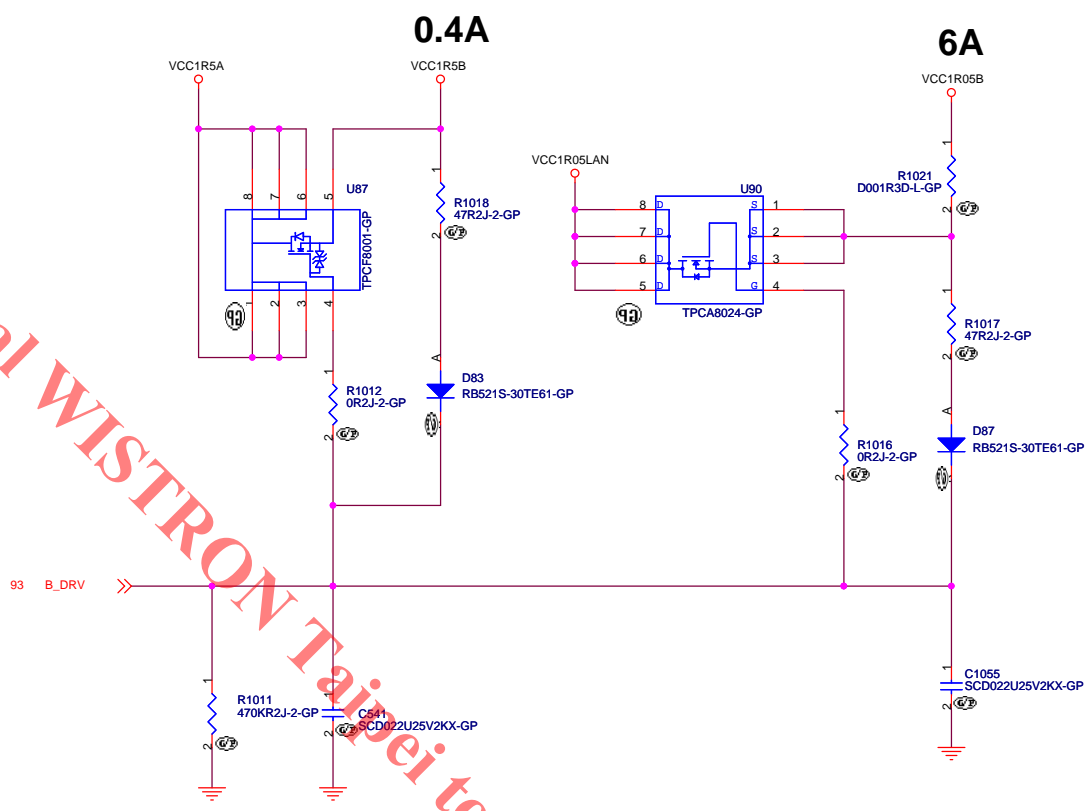
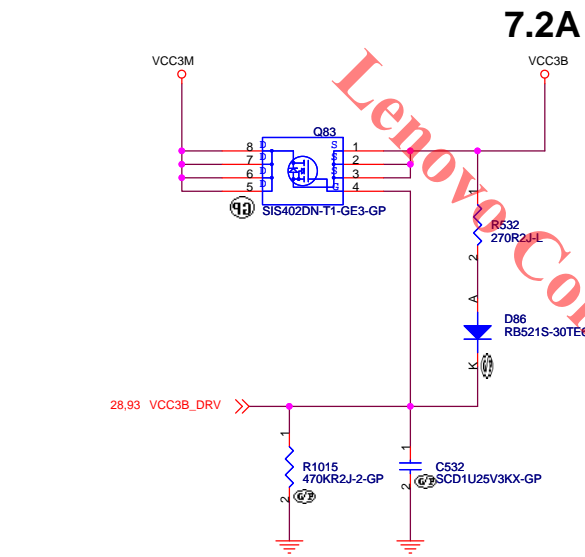
Lenovo Confidential WISTRON Taipei to Lenovo Japan

<Core Design>

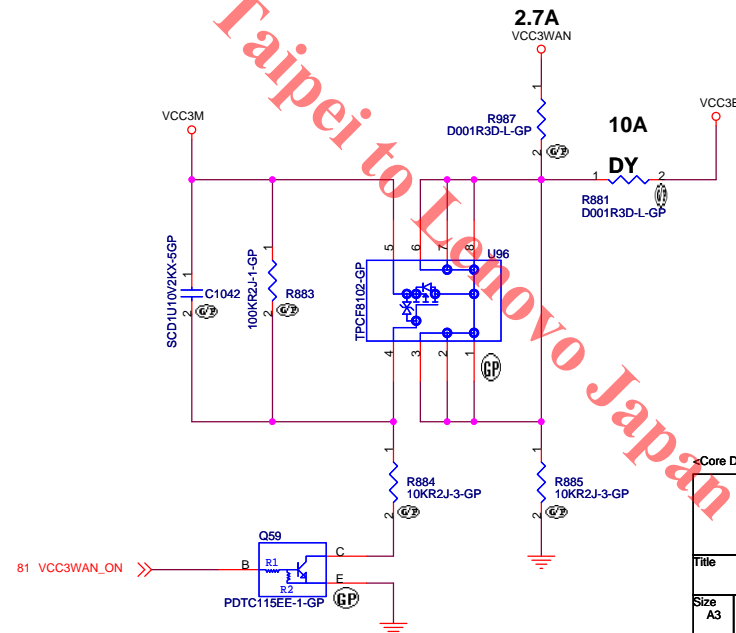
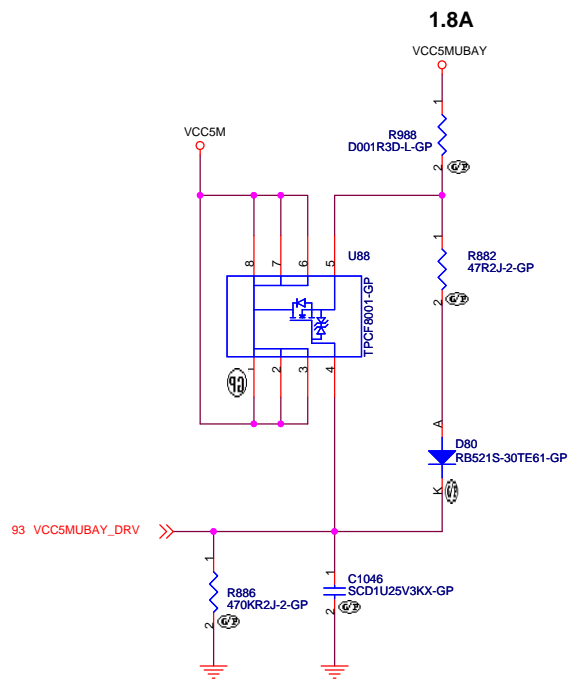
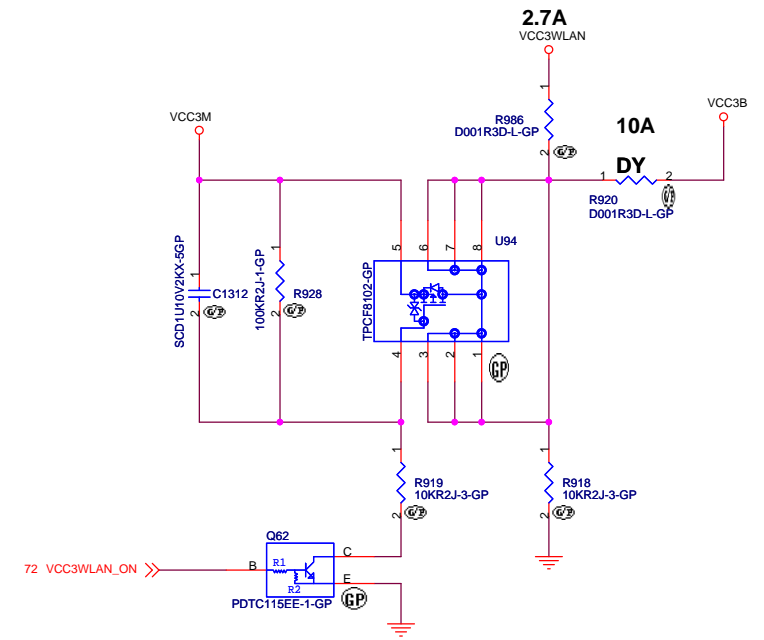
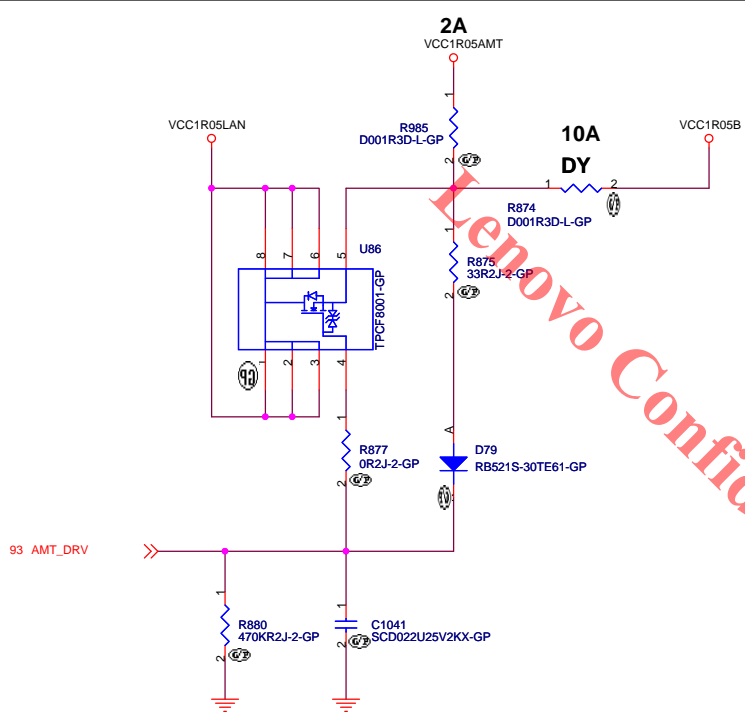
<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>SW AMT/MEPWRG</b>	
Title	
Size A3	Document Number
Date: Tuesday, May 05, 2009	<b>Kendo-1 WS</b> Sheet 96 of 109
	Rev <b>SC</b>



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Core Design>		
<b>緯創資通</b> <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>LOAD SW B</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		
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	REG/LITE	BB/NO
R881	NO-ASM	ASM
Q70	ASM	NO-ASM
R885	ASM	NO-ASM
R883	ASM	NO-ASM
C1042	ASM	NO-ASM
R884	ASM	NO-ASM
Q59	ASM	NO-ASM

Core Design>

**緯創資通** Wistron Corporation  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

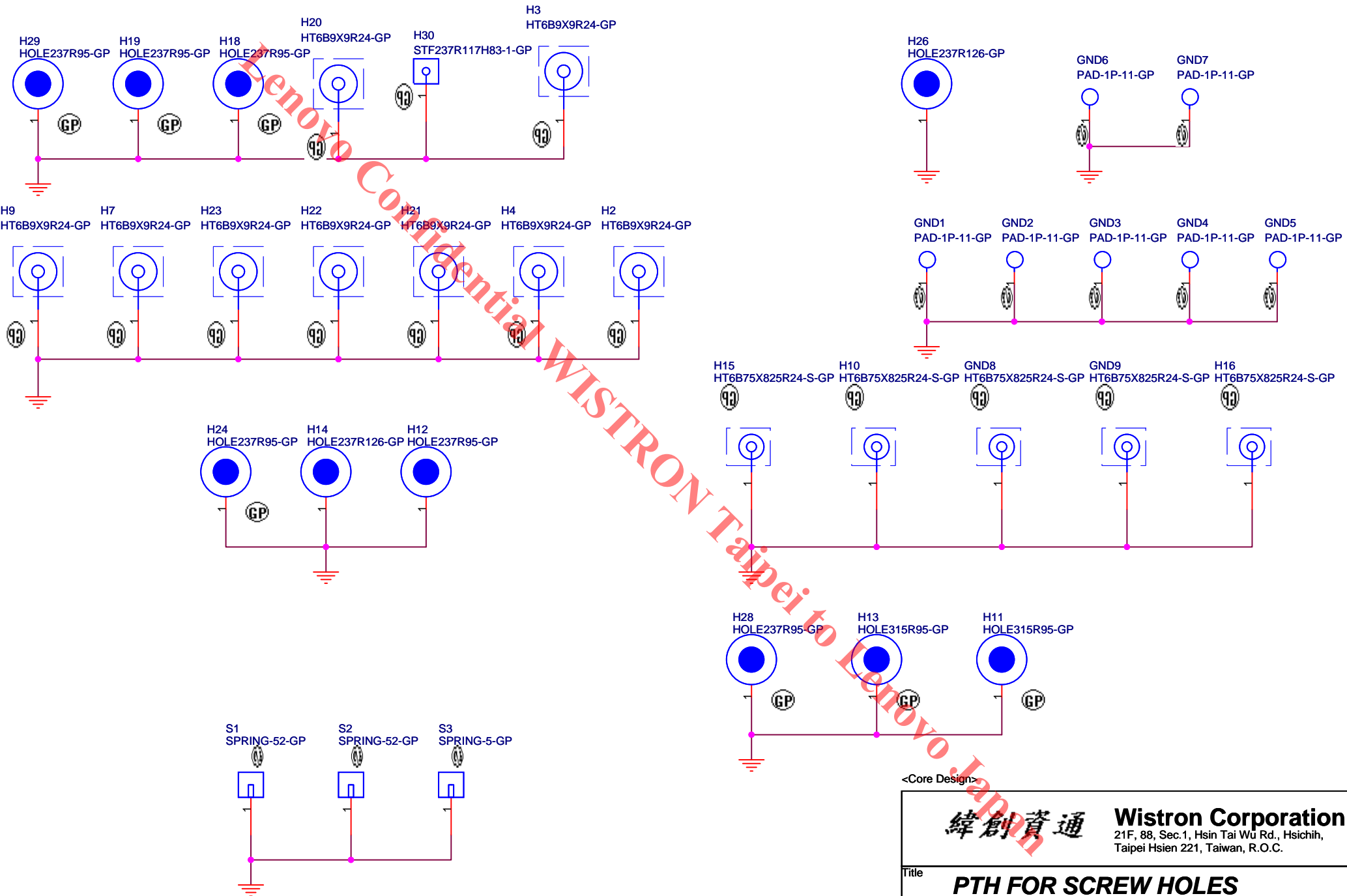
Title: **LOAD SW VCC5MBA**

Size A3	Document Number	Rev SC
	<b>Kendo-1 WS</b>	
Date: Tuesday, May 05, 2009	Sheet 98 of 109	

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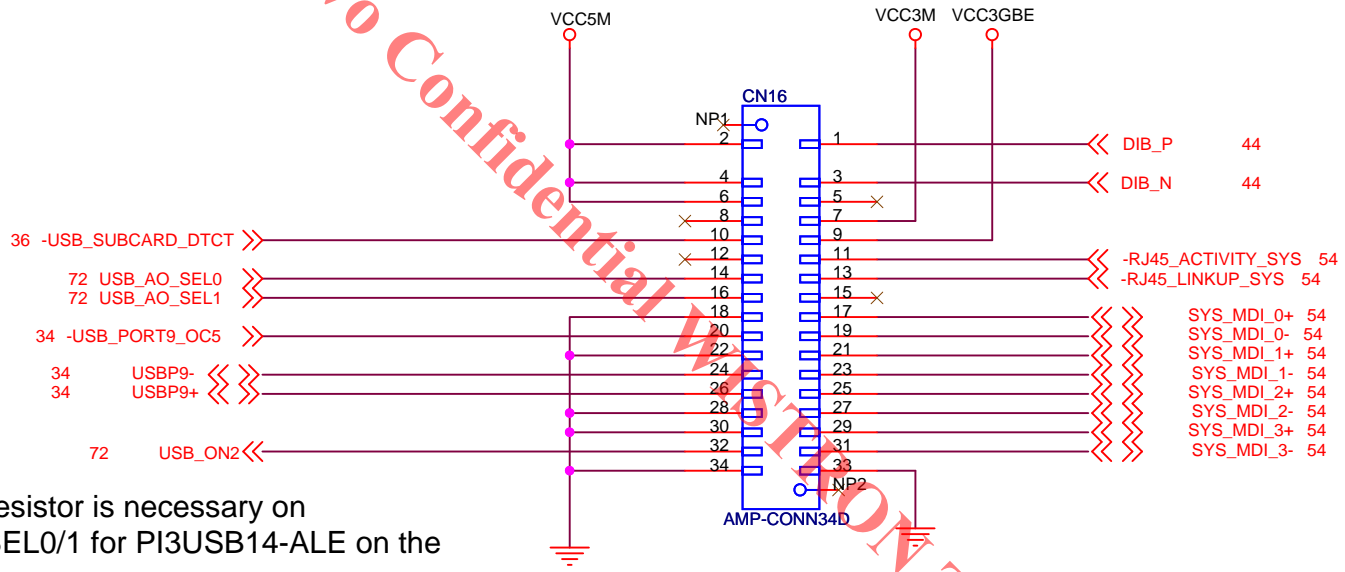
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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009		Sheet 99 of 109



<Core Design>

 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title		<b>PTH FOR SCREW HOLES</b>	
Size	Document Number	<b>Kendo-1 WS</b>	
A4		Rev	<b>SC</b>
Date:	Tuesday, May 05, 2009	Sheet	100 of 109

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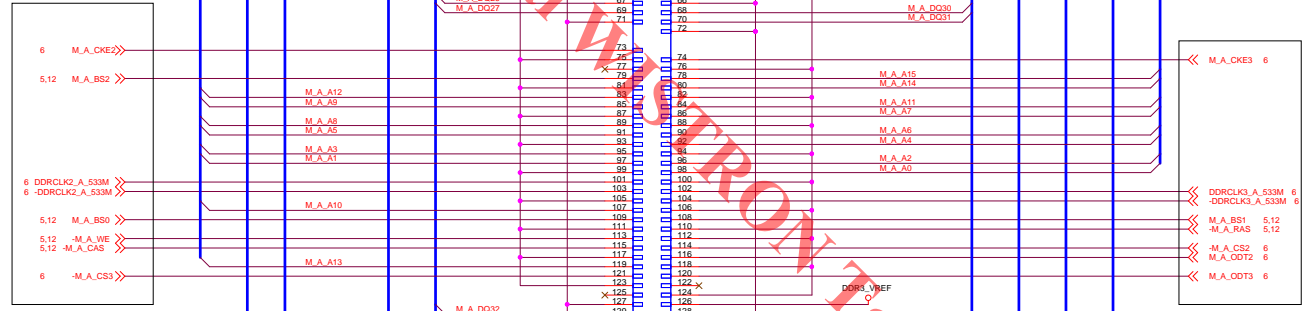
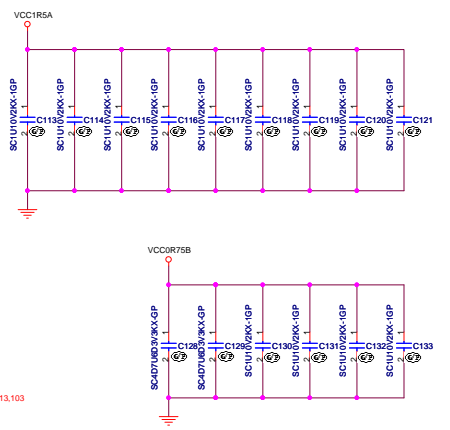
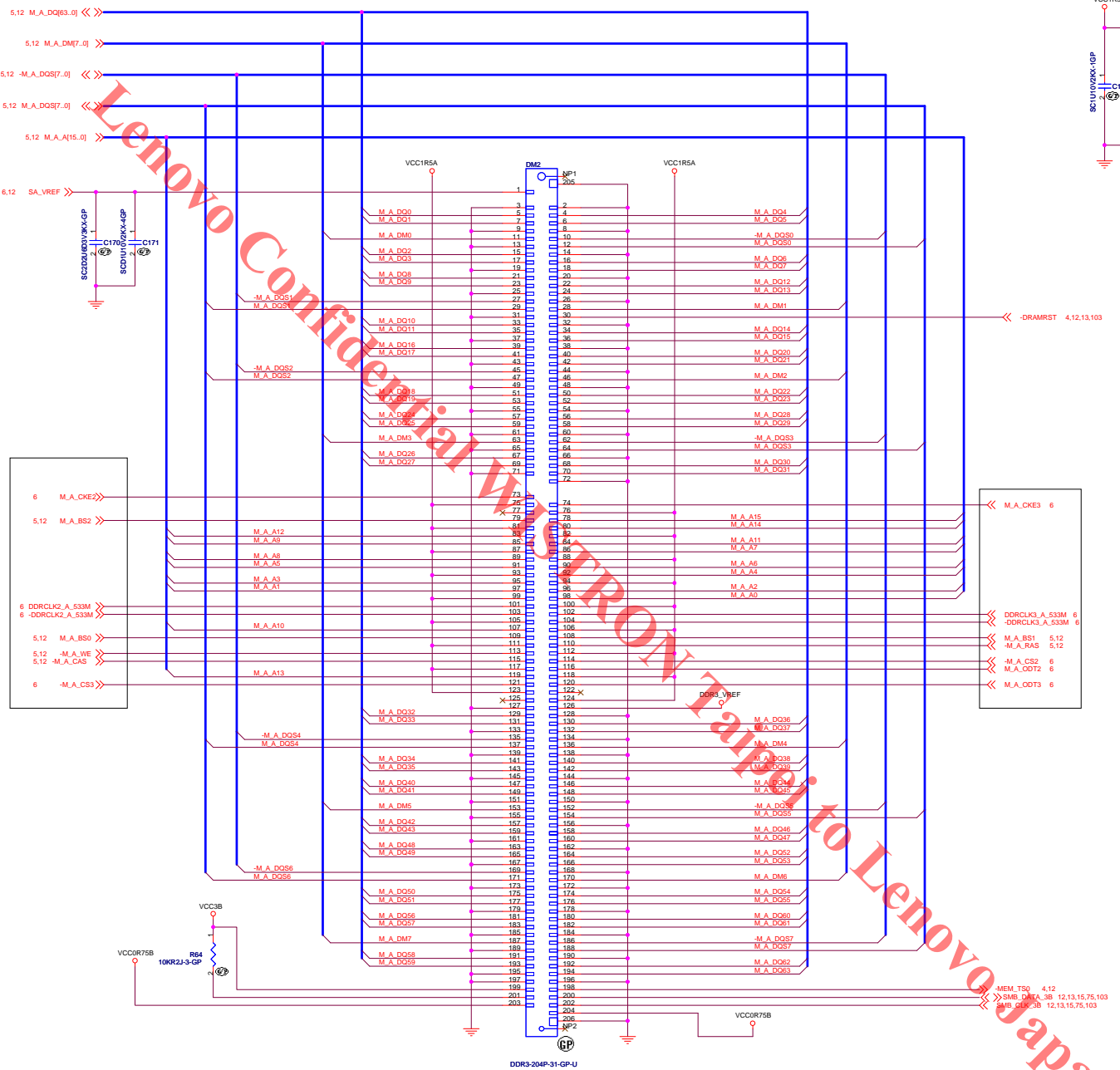
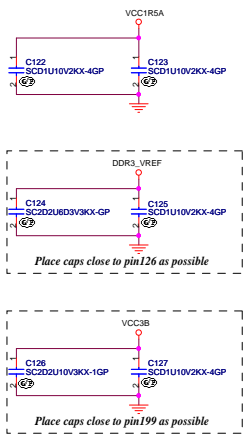


Pull-down resistor is necessary on USB\_AO\_SEL0/1 for PI3USB14-ALE on the USB BRD.

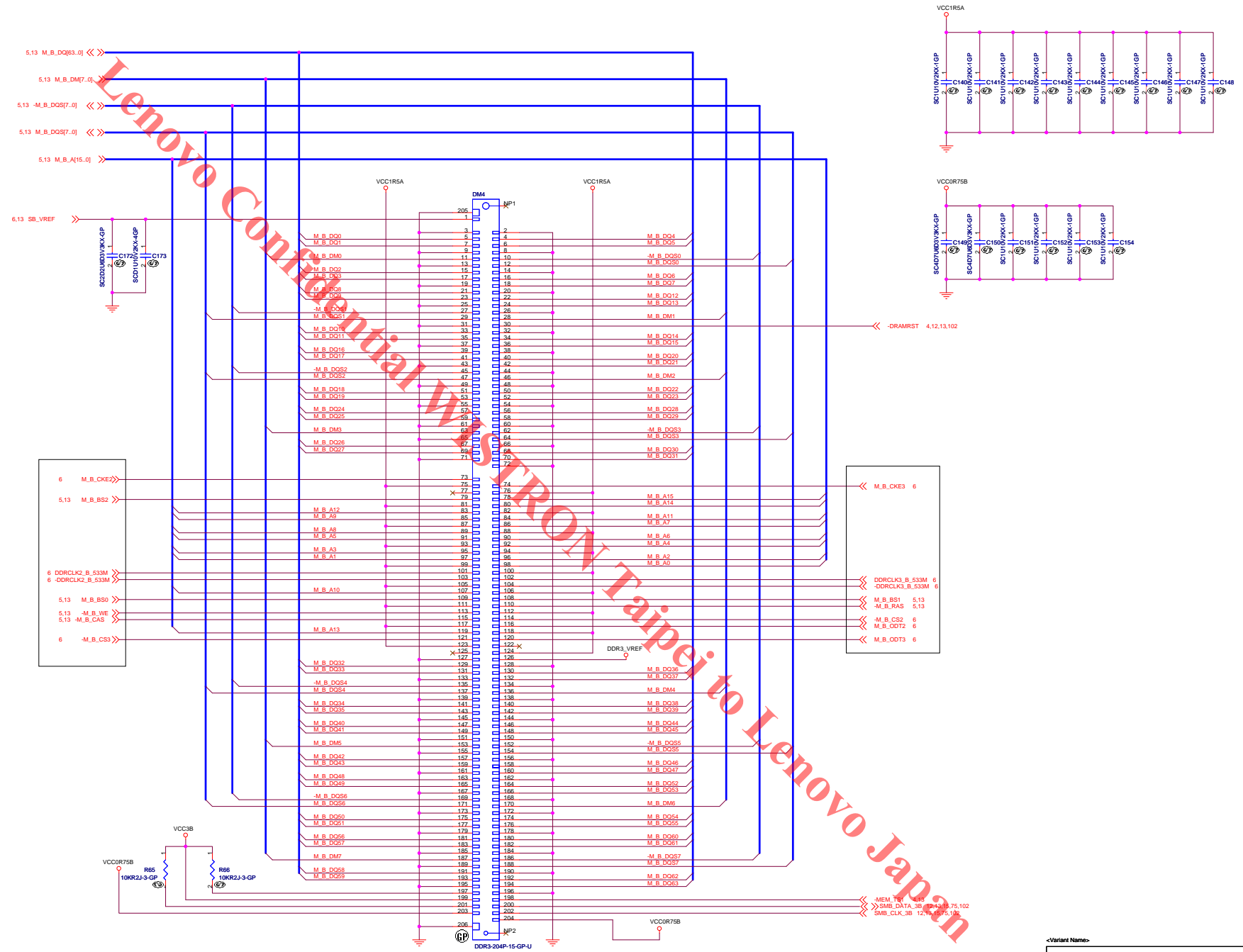
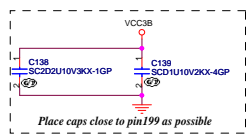
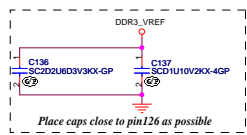
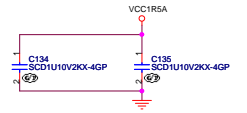
**Change Pin order and pin number to 34.**

<Variant Name>

<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>USB BOARD CONN</b>	
Size A4	Document Number <b>Kendo-1 WS</b>
Date Tuesday, May 05, 2009	Rev <b>SC</b>
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SPD Address : 52h  
 This connector should be placed on near side from CPU.



- 6 M\_B\_CKE2
- 5.13 M\_B\_BS2
- 6 DDRCLK2\_B\_533M
- 6 DDRCLK2\_B\_533M
- 5.13 M\_B\_BS0
- 5.13 -M\_B\_WE
- 5.13 -M\_B\_CAS
- 6 -M\_B\_CS3

- M\_B\_CKE3 6
- DDRCLK3\_B\_533M 6
- DDRCLK3\_B\_533M 6
- M\_B\_BS1 5.13
- M\_B\_RAS 5.13
- M\_B\_CS2 6
- M\_B\_OO2 6
- M\_B\_OO3 6

SPD Address : 53h  
 This connector should be placed on near side from CPU.

**SODIMM should be installed on CH0 Primary at first.**

From Clarksfield

SODIMM CH-A Secondary

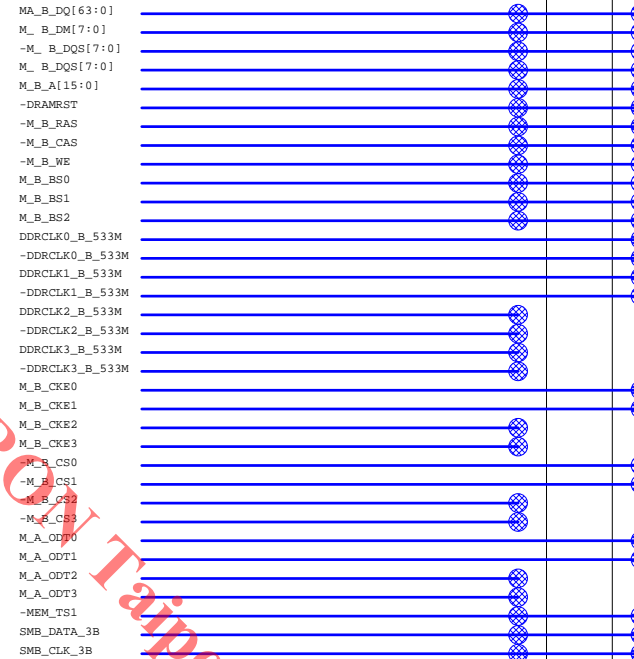
SODIMM CH-A Primary



From Clarksfield

SODIMM CH-B Secondary

SODIMM CH-B Primary



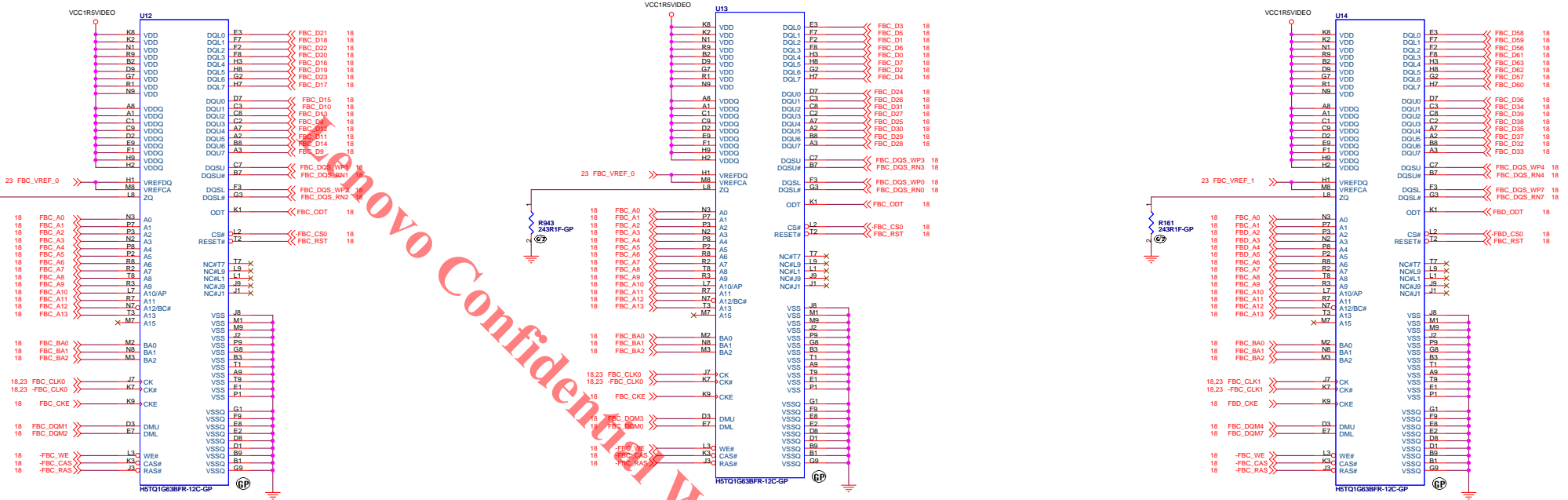
SODIMM IIC Address :  
 CH-A Primary : 50h  
 CH-B Primary : 51h  
 CH-A Secondary : 52h  
 CH-B Secondary : 53h

Pin1 on SODIM connector (VREF\_DQ) only connects to DDR Voltage divider.  
 Clarksfield H17/J17 is left.

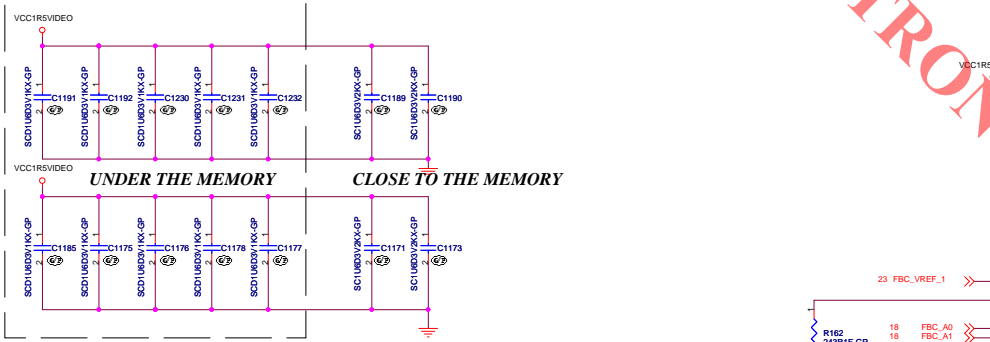
<Variant Name>

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<p><b>SODIMM CONFIGURATION</b></p>	
File	Rev
Size	Document Number
Az	<b>Kendo-1 WS</b>
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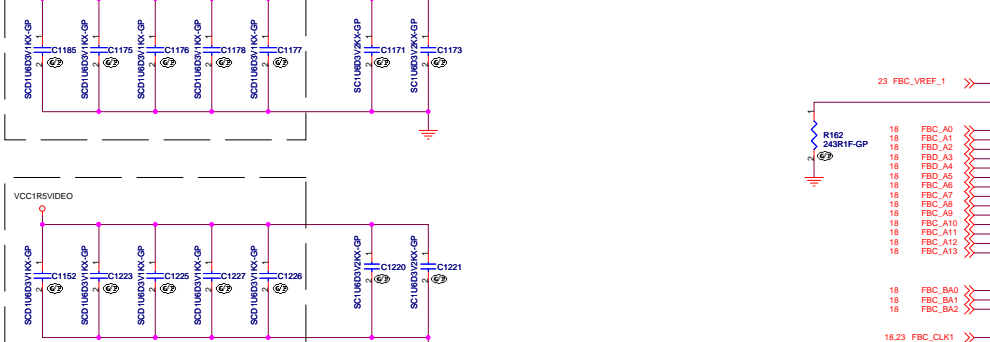




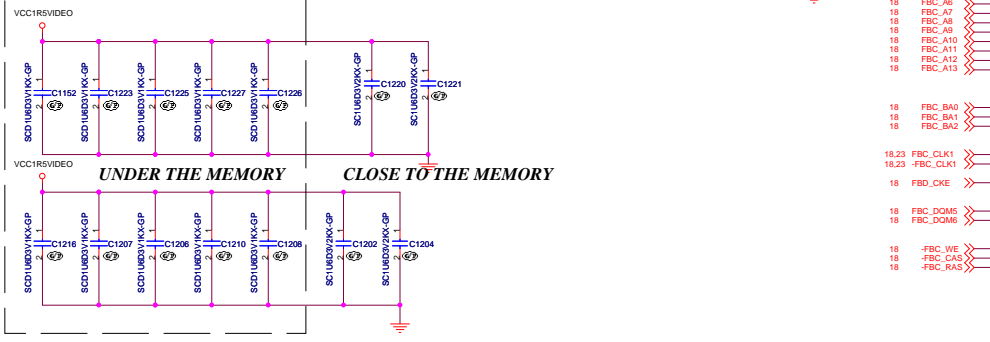
FOR U12



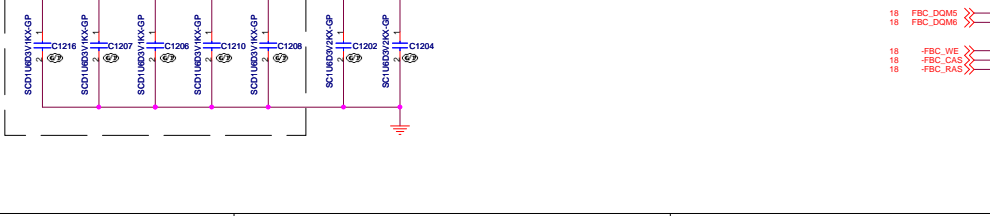
FOR U13



FOR U14



FOR U15

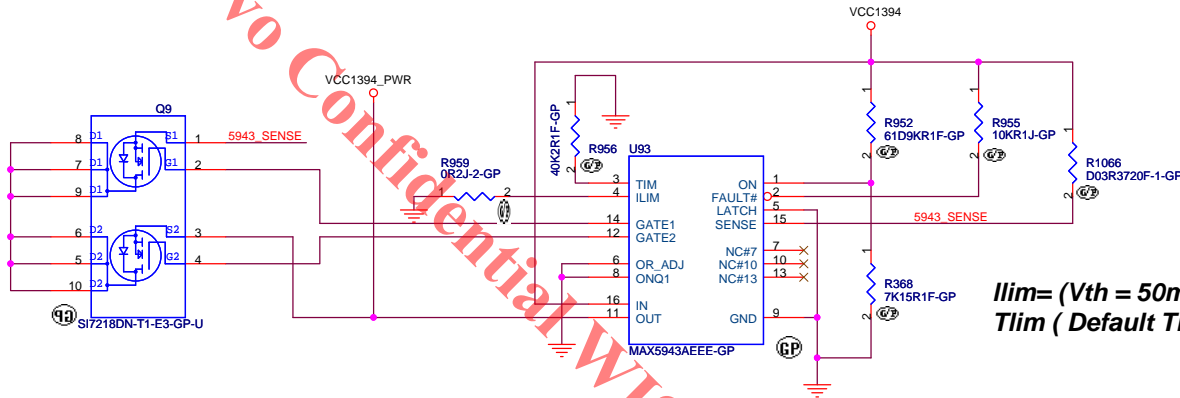


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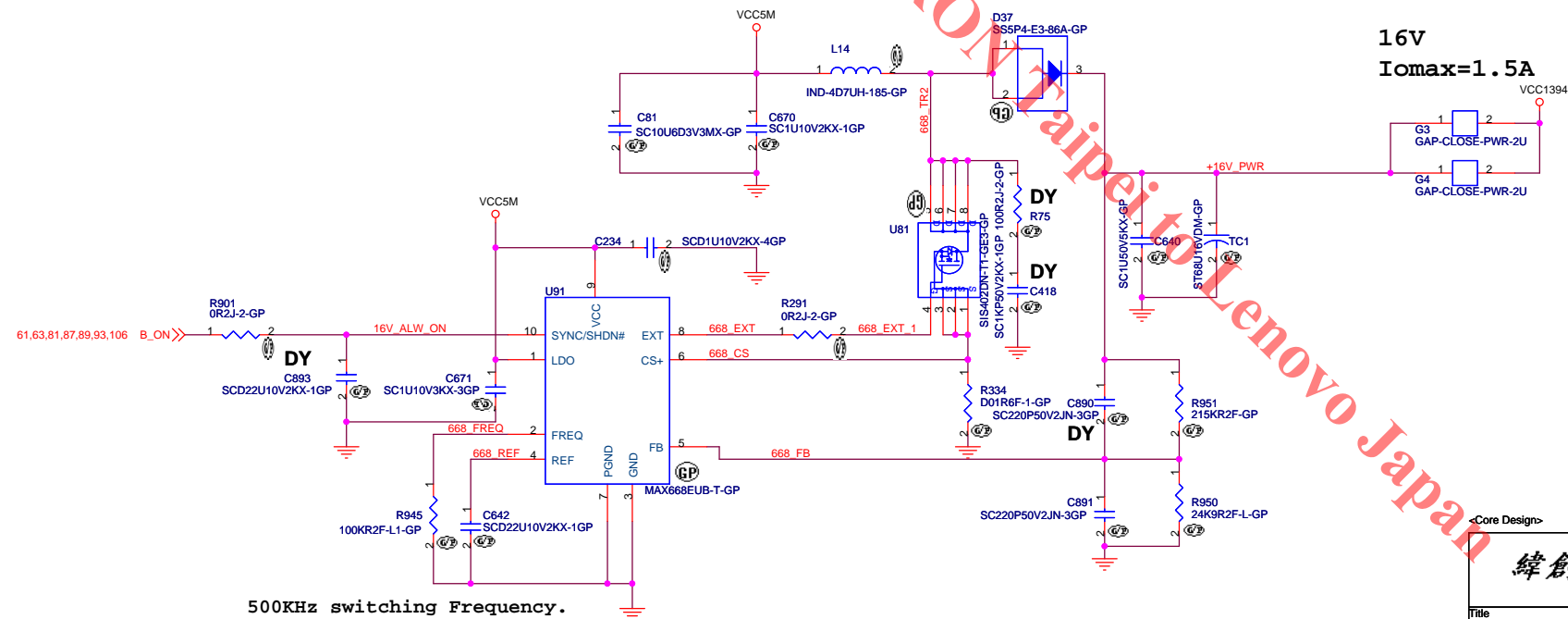
VIDEO FRAME BUFFER PORT C

-Core Design-  
**緯創資通 Wistron Corporation**  
 21F, 8th, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.  
 File: **VRAM CHANNEL-C**  
 Size: A4 Document Number: **Kendo-1 WS** Rev: SC  
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$I_{lim} = (V_{th} = 50mV) / (R_{sense} = 30mohm) = 1.66A$   
 $T_{lim} \text{ (Default Timeout duration)} = 2ms.$



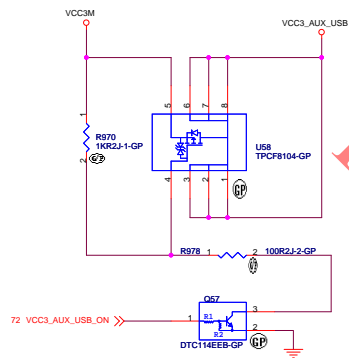
**16V**  
**I<sub>omax</sub> = 1.5A**

500KHz switching Frequency.

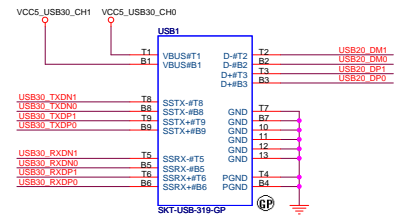
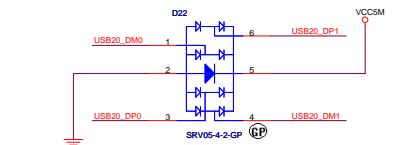
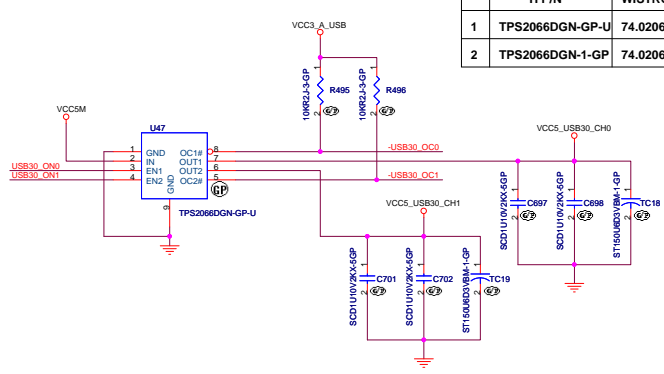
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<b>1394 PWR</b>		
Title Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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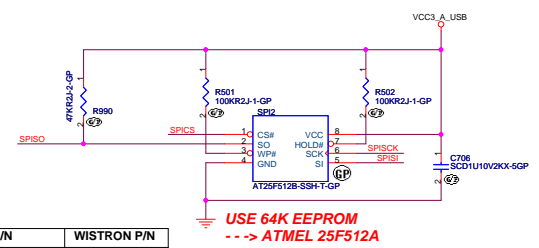
0.6A



	TI P/N	WISTRON P/N
1	TPS2066DGN-GP-U	74.02066.A71
2	TPS2066DGN-1-GP	74.02066.B71

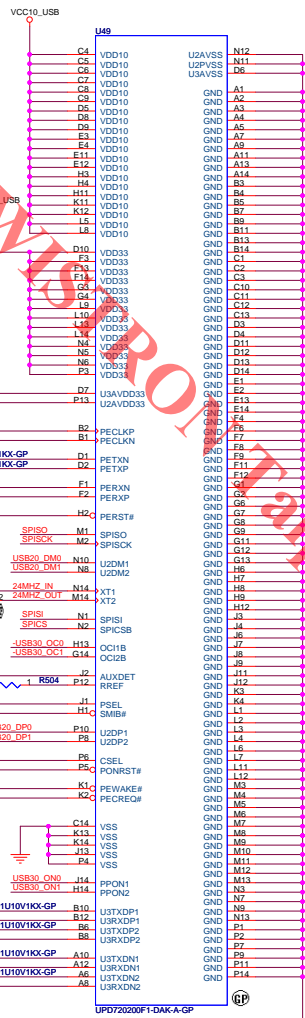
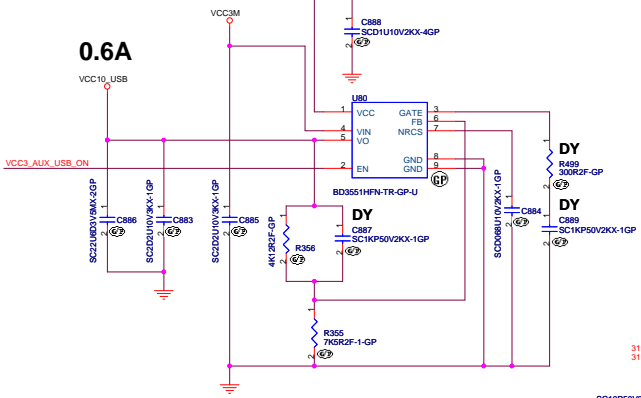


USB3.0 CONNECTOR

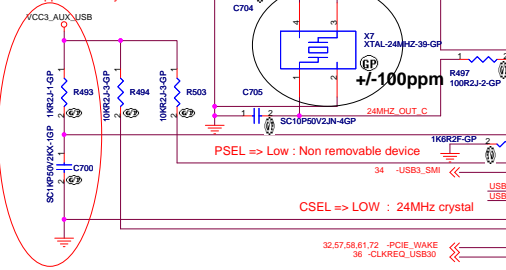


	Supplier	Vendo P/N	WISTRON P/N
1	ATMEL	AT25F128-SSH-T	72.25512.F01
2	MXIC	MX25L512MC-12G	72.25512.E01

0.6A



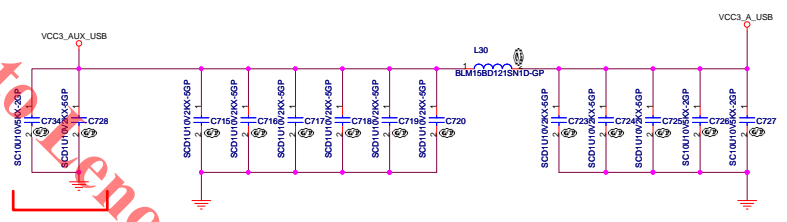
If Wakeup function from D3 cold is required, VCC3\_AUX should be applied from system.



CSEL => LOW : 24MHz crystal

	NEC P/N	WISTRON P/N	
1	UPD720200F1-DAK-A-GP	71.72020.A0U	MP
2	UPD720200F1-DAK-SSA-A	71.72020.00U	ES

NOT MANDATORY TO PLACE.



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USB 3.0

Kendo-1 WS

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HELE Recommended Conditions:  
 Normal Frequency: 24MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 12pF.  
 Effective Series Resistance: 50-ohm.  
 Effective Shunt Capacitance: 2pF.

# Long power trace EMI decoupling caps

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<Core Design>

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Title		
<b>EMI DECOUPLING</b>		
Size	Document Number	Rev
Custom	<b>Kendo-1 WS</b>	<b>SC</b>
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